Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Type

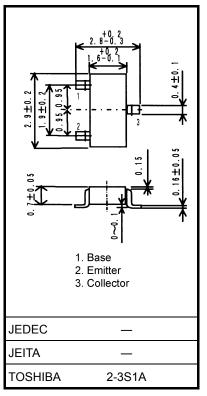
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High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: $h_{FE} = 400$ to 1000 (IC = 0.5 A)
- Low collector-emitter saturation voltage: V_{CE} (sat) = 0.12 V (max)
- High-speed switching: tf = 55 ns (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	100	V	
Collector-emitter voltage		V _{CEX}	80	V	
Collector-emitter voltage		V _{CEO}	50	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ic	4	Α	
	Pulse	I _{CP}	7		
Base current		ΙΒ	400	mA	
Collector power dissipation	DC	PC	800	mW	
	t = 10 s	(Note 1)	1250		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.01 g (typ.)

- Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)
- Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

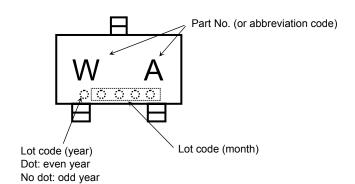
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = 100 \text{ V}, I_{E} = 0$	_	_	100	nA
Emitter cut-off current		I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	I _C = 10 mA, I _B = 0	50	_	_	V
DC current gain		h _{FE} (1)	V _{CE} = 2 V, I _C = 0.5 A	400	_	1000	
		h _{FE} (2)	V _{CE} = 2 V, I _C = 1.6 A	200	_	_	
Collector-emitter saturation voltage		V _{CE} (sat)	I _C = 1.6 A, I _B = 32 mA	_	_	0.12	V
Base-emitter saturation voltage		V _{BE} (sat)	I _C = 1.6 A, I _B = 32 mA	_	_	1.10	V
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	26	_	pF
Switching time	Rise time	t _r	See Figure 1 circuit diagram.	_	45	_	ns
	Storage time	t _{stg}	$V_{CC} \simeq 30 \text{ V}, R_L = 19 \Omega$	_	700	_	
	Fall time	t _f	$I_{B1} = -I_{B2} = 53.3 \text{ mA}$	_	55	_	

V_{CC} I_{B1} I_{B2} I_{B2} I_{B2} I_{B2} I_{B2} I_{B2} I_{B2} I_{B2} I_{B2}

Figure 1 Switching Time Test Circuit & Timing Chart

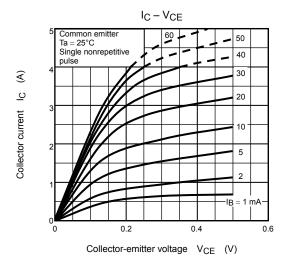
Duty cycle < 1%

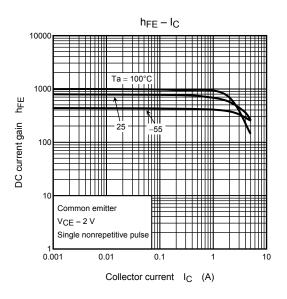
Marking

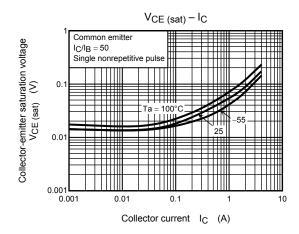


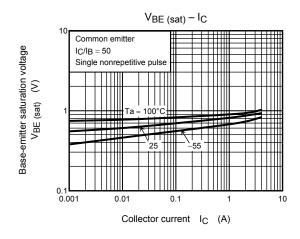
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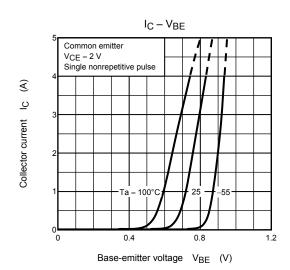
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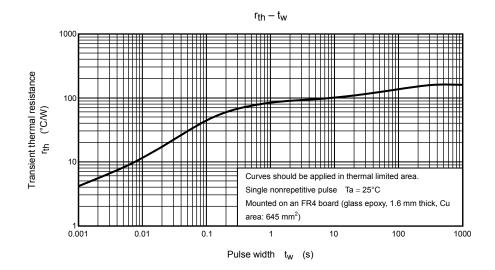


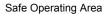


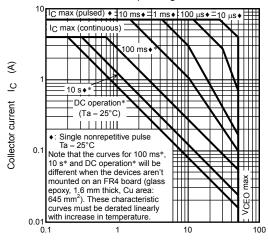




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Collector-emitter voltage V_{CE} (V)

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