



DATA BUS TERMINATOR / 3-PHASE, FULL WAVE BRIDGE

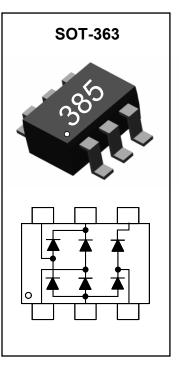
This highly integrated device is designed as rail to rail overvoltage protection clamp for up to 3 data lines. It is also ideal as a three-phase, full wave bridge. PanJit's PJ3L85 is ideal in portable applications where small form factors are required.

FEATURES

- Very Low Reverse Leakage Current
- Fast Switching
- Ultra Small SOT-363 Package Utilizing Minimal Board Space
- Lead Free Plating, 100% Matte Tin Finish, RoHS Compliant

APPLICATIONS

- PDAs
- Portable Computers





MAXIMUM RATINGS T_A = 25°C, unless otherwise noted

Rating	Symbol	Value	Units
Marking Code		385	
Reverse Voltage	VR	85	V
Continuous Forward Current	١F	160	mA
Non-Repetitive Surge Current, t=1s	IFSM	500	mA
Power Dissipation (Note 1)	PD	200	mW
Operating Junction Temperature Range	TJ	-65 to +150	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C

Note 1: Device Mounted on FR-4 board 1.0 inch x 0.85 inch x 0.062 inch, with minimum pad layout.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Units
Thermal Resistance, Junction to Ambient	R _{thja}	625	°C/W



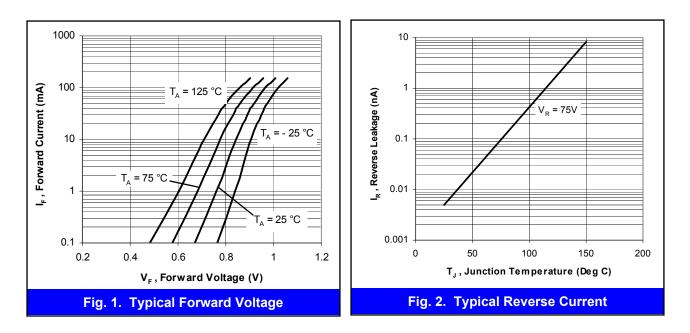
ELECTRICAL CHARACTERISTICS (Each Diode)

T_J = 25°C, unless otherwise noted

Characteristic		Symbol	Min	Тур	Мах	Units
Reverse Breakdown Voltag	je (Note 2) IR = 100u	VBR	85	-	-	V
	IF = 1.0m/	4	-	0.76	0.90	
Forward Voltage (Note 2)	IF = 10m/	4	-	0.84	1.00	
	IF = 50m/	V _F	-	0.92	1.10	V
	IF = 150m/	4	-	1.00	1.25	
Reverse Leakage Current (Note 2) VR = 75V VR = 75V, Tj = 150°C			-	-	5.0	
			-	-	80	nA
Total Capacitance	Per Elemer	t	-	1.25	2.0	
VR = 0V, f = 1.0 MHz	Data Line to Groun	d C _T	-	2.0	6	pF
	Between Data Line	s	-	3.0	7	
Reverse Recovery Time	IF = IR = 10m/ Irr = 1.0mA, RL = 100 Ohr	l Trr	-	-	3.0	us

Note 2: Short duration test pulse to minimize self heating

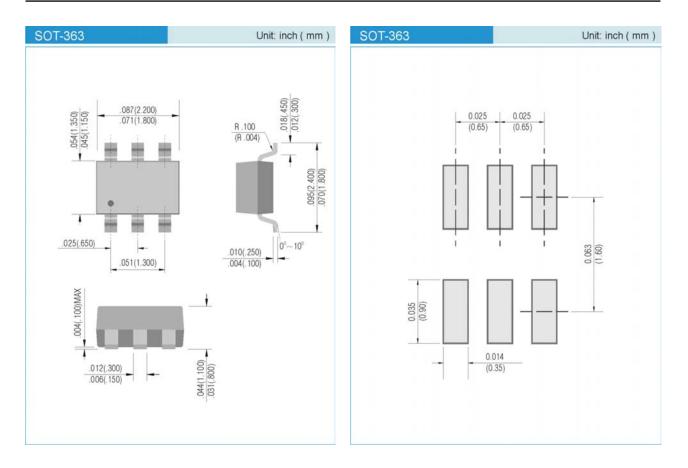
ELECTRICAL CHARACTERISTIC CURVES (Each Diode)







PACKAGE LAYOUT AND SUGGESTED PAD DIMENSIONS



ORDERING INFORMATION

PJ3L85 T/R7 - 7 inch reel, 3K units per reel

PJ3L85 T/R13 - 13 inch reel, 10K units per reel

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