

Parameter	Rating	Units
Blocking Voltage	1000	V_P
Load Current, $T_A=25^\circ\text{C}$		
With 5°C/W Heat Sink	1.75	A_{DC}
No Heat Sink	0.65	
On-Resistance	2	Ω
$R_{\theta JC}$	0.35	$^\circ\text{C/W}$

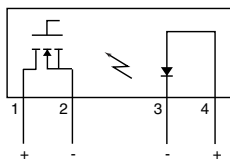
Features

- 100% Solid State
- Compact i4-PAC™ Power Package
- Low Thermal Resistance (0.35°C/W)
- $1.75A_{DC}$ Load Current with 5°C/W Heat Sink
- Electrically Non-conductive Thermal Pad for Heat Sink Applications
- No Moving Parts
- Low Drive Power Requirements
- Arc-Free With No Snubbing Circuits
- $2500V_{rms}$ Input/Output Isolation
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

Applications

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Transportation Equipment
- Aerospace/Defense

Pin Configuration



Description

Clare and IXYS have combined to bring OptoMOS® technology, reliability and compact size to a new family of High Power Solid State Relays. As part of this new family, the CPC1786 single pole normally open (1-Form-A) DC Solid State Relay employs optically coupled MOSFET technology to provide $2500V_{rms}$ of input to output isolation.

The output is constructed with an efficient MOSFET switch and photovoltaic die that uses Clare's patented OptoMOS architecture while the input GaAlAs infrared LED provides the optically coupled control. The combination of low on-resistance and high load current handling capability makes this relay suitable for a variety of high-performance DC switching applications.

The unique i4-PAC package pioneered by IXYS allows solid state relays to achieve the highest load current and power ratings. This package features an IXYS unique process where the silicon chips are soft soldered onto the Direct Copper Bond (DCB) substrate instead of the usual copper leadframe. The DCB ceramic, the same substrate used in high power modules, not only provides $2500V_{rms}$ isolation but also very low thermal resistance (0.35°C/W).

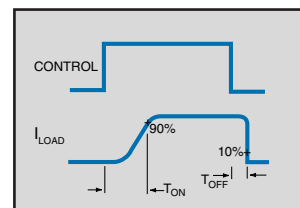
Approvals

- UL recognized component: File # E69938
- Certified to UL 508

Ordering Information

Part #	Description
CPC1786J	i4-PAC™ (25 per tube)

Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings

Parameter	Ratings	Units
Blocking Voltage	1000	V _P
Reverse Input Voltage	5	V
Input Control Current	100	mA
Peak (10ms)	1	A
Input Power Dissipation	150	mW
Isolation Voltage, Input to Output	2500	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical absolute maximum ratings are at 25°C

Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics $T_A=25^\circ\text{C}$						
Load Current ¹						
Peak	$t \leq 10\text{ms}$	I _L	-	-	10	A _p
Continuous	No Heat Sink				0.65	A _{DC}
Continuous	T _C =25°C	6.9				
Continuous	T _C =99°C	I _{L(99)}	0.8			
On-Resistance ²	I _L =1A, I _F =10mA	R _{ON}	-	1.5	2	Ω
Off-State Leakage Current	V _L =1000V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =20mA, V _L =10V	T _{ON}	-	1.9	20	ms
Turn-Off		T _{OFF}	-	0.08	5	
Output Capacitance	V=25V, f=1MHz	C _{OUT}	-	330	-	pF
Input Characteristics $T_A=25^\circ\text{C}$						
Input Control Current ³	I _L =120mA	I _F	-	-	10	mA
Input Dropout Current	-	I _F	0.6	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics $T_A=25^\circ\text{C}$						
Capacitance Input to Output	-	C _{I/O}	-	1	-	pF

¹ Higher load currents possible with proper heat sinking.

² Measurement taken within 1 second of on time.

³ For applications requiring high temperature operation (greater than 60°C) an LED drive current of 20mA is recommended.

Thermal Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Thermal Resistance (junction to case)	-	$R_{\theta JC}$	-	-	0.35	°C/W
Thermal Resistance (junction to ambient)	Free air	$R_{\theta JA}$	-	40	-	°C/W
Junction Temperature (operation)	-	T_J	-40	-	100	°C

Thermal Management

Device high current characterization was performed using Kunze heat sink KU 1-159, phase change thermal interface material KU-ALC 5, and transistor clip KU 4-499/1. This combination provided an approximate junction-to-ambient thermal resistance of 12.5°C/W.

Heat Sink Calculation

Higher load currents are possible by using lower thermal resistance heat sink combinations.

Heat Sink Rating

$$R_{\theta CA} = \frac{(T_J - T_A) I_{L(99)}^2 \cdot R_{\theta JC}}{I_L^2} - R_{\theta JC}$$

T_J = Junction Temperature (°C), $T_J \leq 100^\circ\text{C}$ *

T_A = Ambient Temperature (°C)

$I_{L(99)}$ = Load Current with Case Temperature @ 99°C (A_{rms})

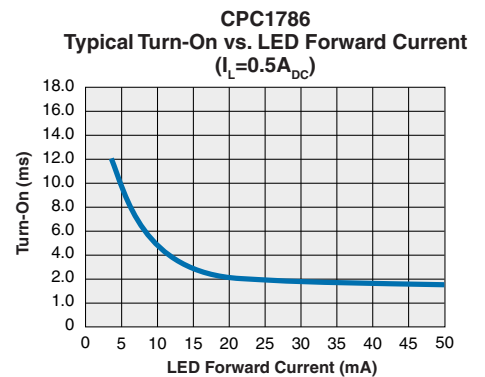
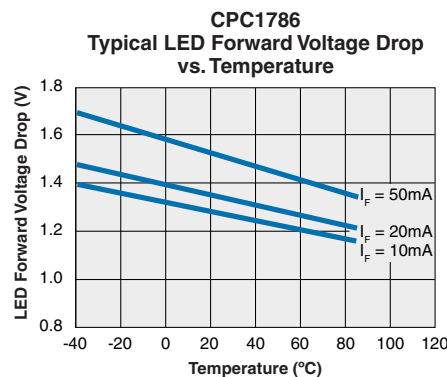
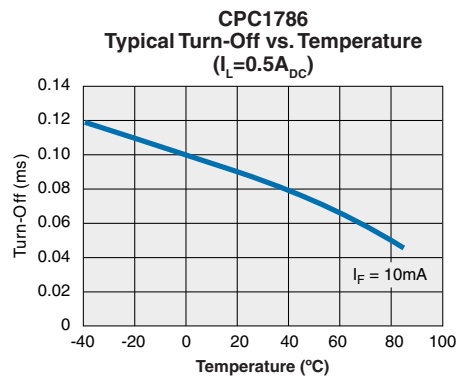
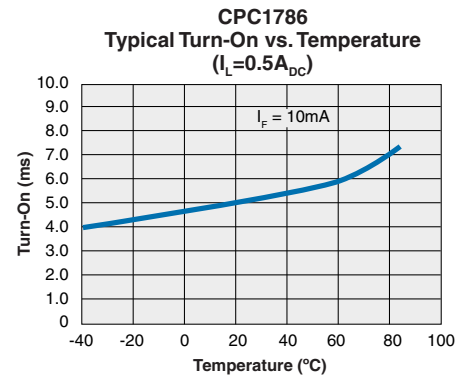
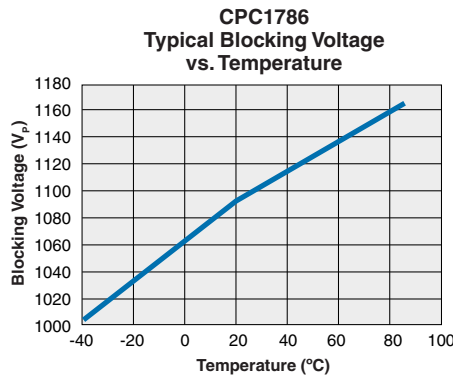
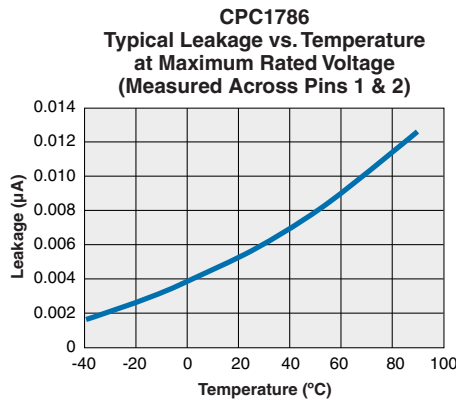
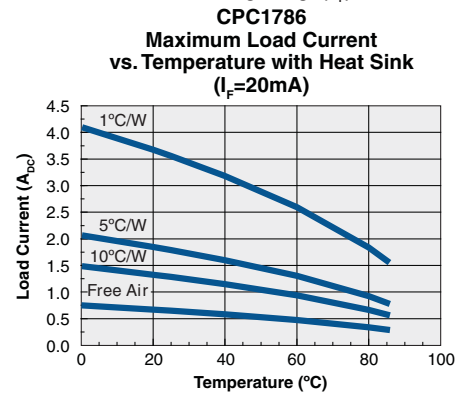
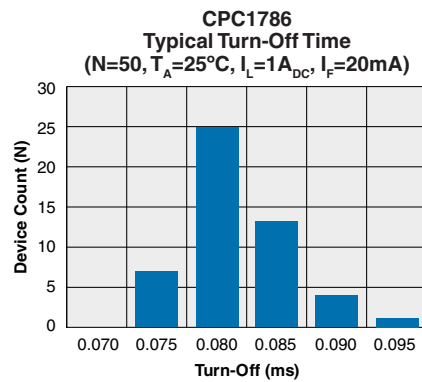
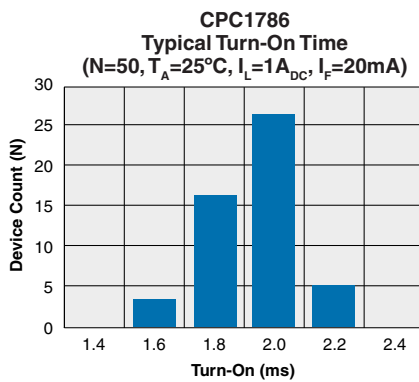
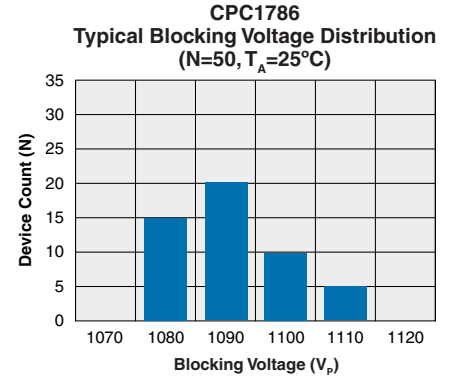
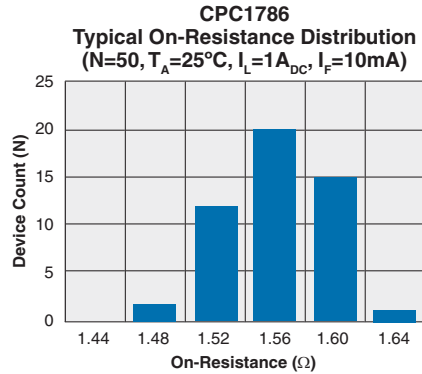
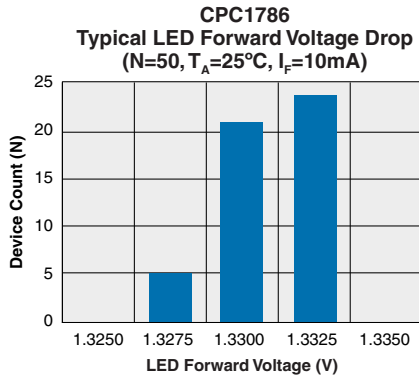
I_L = Desired Operating Load Current (A_{rms}), $I_L \leq I_{L(MAX)}$

$R_{\theta JC}$ = Thermal Resistance, Junction to Case (°C/W) = 0.35°C/W

$R_{\theta CA}$ = Thermal Resistance of Heat Sink & Thermal Interface Material, Case to Ambient (°C/W)

* Elevated junction temperature reduces semiconductor lifetime.

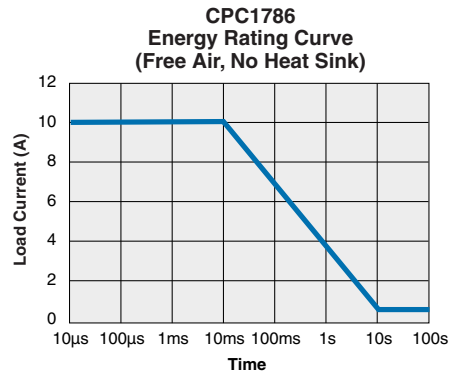
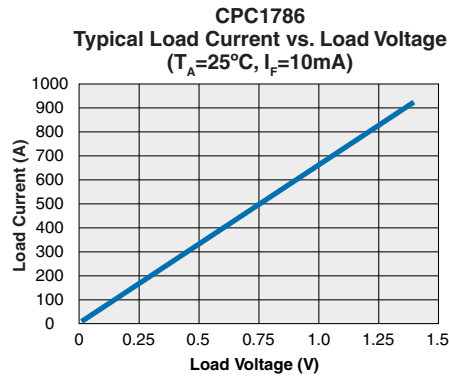
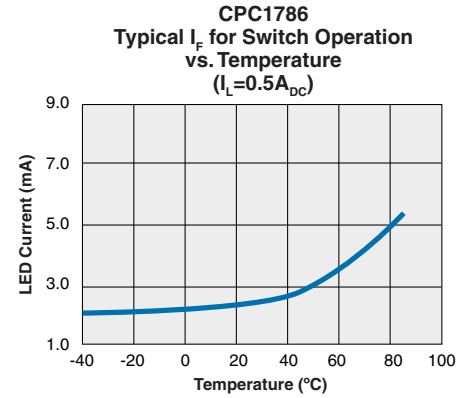
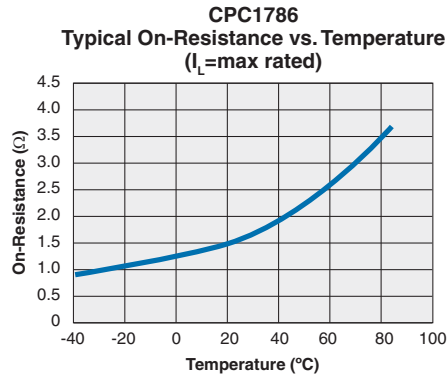
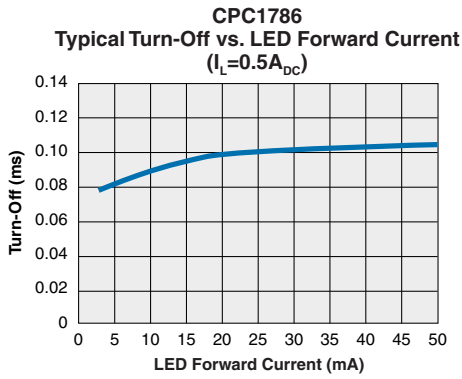
PERFORMANCE DATA*



Unless otherwise specified, all performance data was acquired without the use of a heat sink.

*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

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MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

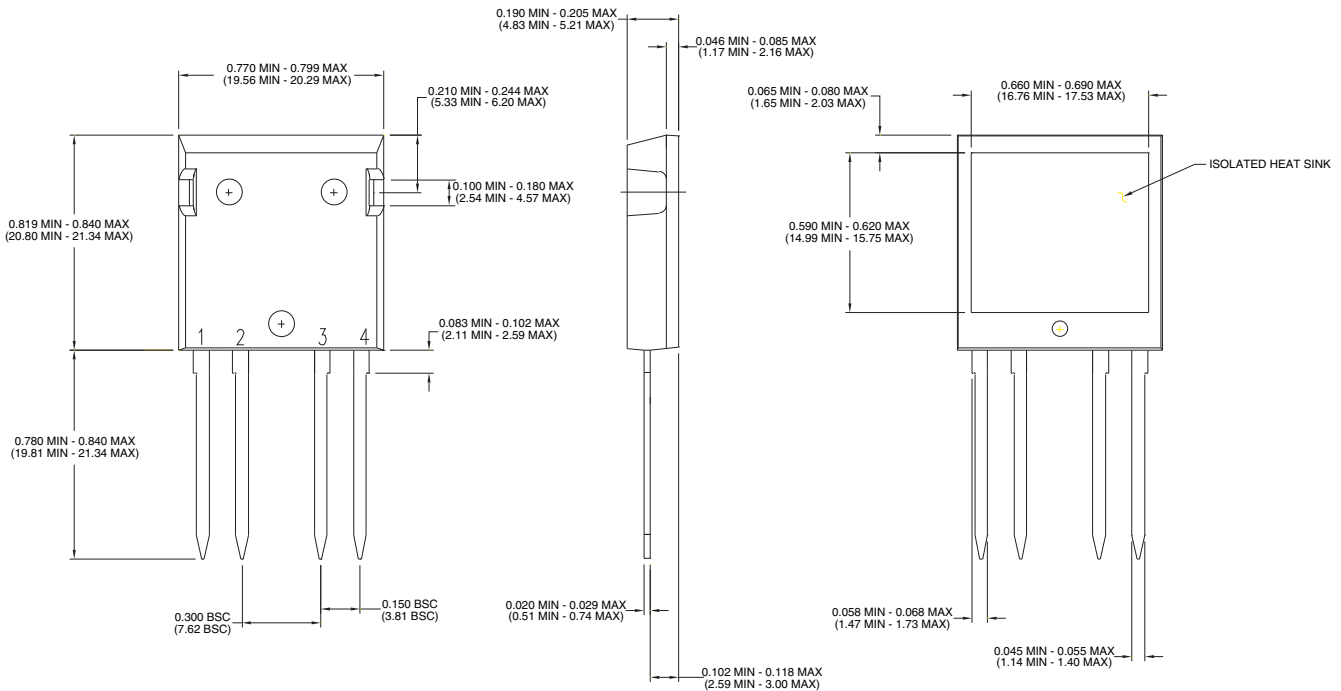
Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.



MECHANICAL DIMENSIONS:

4 Pin i4-PAC™



NOTE: Bottom heatsink meets 2500V_{rms} isolation to the other pins.

Dimensions:
inches
(mm)

For additional information please visit our website at: www.clare.com

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