



N-Channel 20-V (D-S) MOSFET

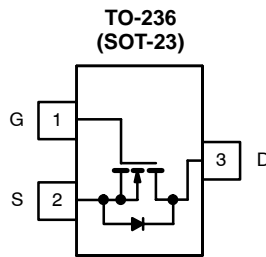
PRODUCT SUMMARY			
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
20	0.033 @ $V_{GS} = 4.5$ V	4.9	11.2
	0.040 @ $V_{GS} = 2.5$ V	4.4	
	0.051 @ $V_{GS} = 1.8$ V	3.9	

FEATURES

- 1.8-V Rated
- RoHS Compliant



Pb-free Available



Top View
Si2312DS (C2)*
*Marking Code

Ordering Information: Si2312DS-T1
Si2312DS-T1—E3 (Lead (Pb)-Free)

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	5 sec	Steady State	Unit	
Drain-Source Voltage	V_{DS}	20		V	
Gate-Source Voltage	V_{GS}	± 8			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	4.9	3.77	A
		$T_A = 70^\circ\text{C}$	3.9	3.0	
Pulsed Drain Current ^b	I_{DM}	15			
Avalanche Current ^b	I_{AS}	15			
Single Avalanche Energy	E_{AS}	L = 0.1 mH	11.25		mJ
Continuous Source Current (Diode Conduction) ^a			I_S	1.0	
Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	1.25	0.75	W
		$T_A = 70^\circ\text{C}$	0.80	0.48	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 5$ sec	75	100	$^\circ\text{C/W}$
		Steady State	120	166	
Maximum Junction-to-Foot	R_{thJF}	40	50		

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

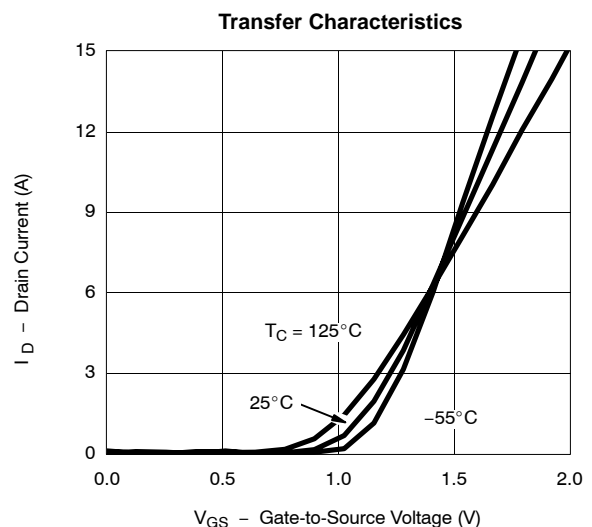
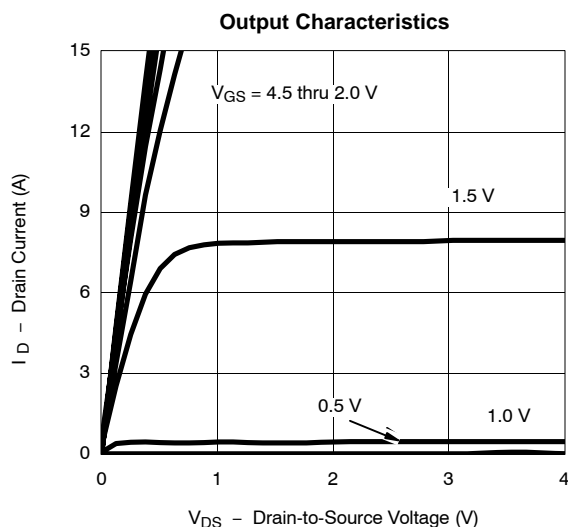
SPECIFICATIONS (T_A = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	20			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.45	0.65	0.85	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1	μA
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 70 °C			75	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 4.5 V	15			A
Drain-Source On-Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 5.0 A		0.027	0.033	Ω
		V _{GS} = 2.5 V, I _D = 4.5 A		0.033	0.040	
		V _{GS} = 1.8 V, I _D = 4.0 A		0.042	0.051	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5.0 A		40		S
Diode Forward Voltage	V _{SD}	I _S = 1.0 A, V _{GS} = 0 V		0.8	1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 5.0 A		11.2	14.0	nC
Gate-Source Charge	Q _{gs}			1.4		
Gate-Drain Charge	Q _{gd}			2.2		
Switching						
Turn-On Delay Time	t _{d(on)}	V _{DD} = 10 V, R _L = 10 Ω I _D ≅ 1.0 A, V _{GEN} = 4.5 V, R _g = 6 Ω		15	25	ns
Rise Time	t _r			40	60	
Turn-Off Delay Time	t _{d(off)}			48	70	
Fall-Time	t _f			31	45	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = 1.0 A, di/dt = 100 A/μs		13	

Notes

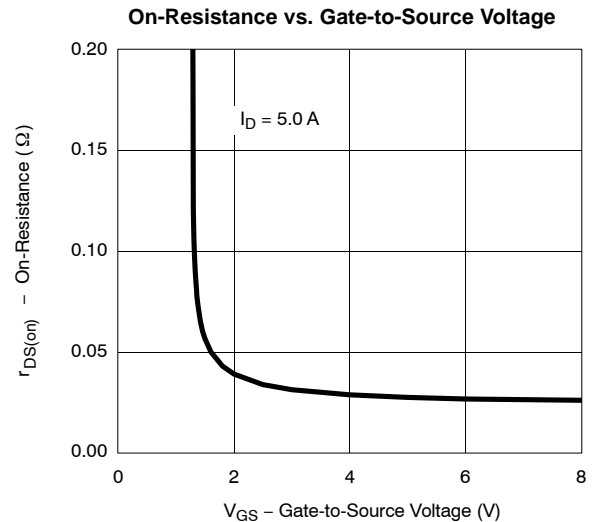
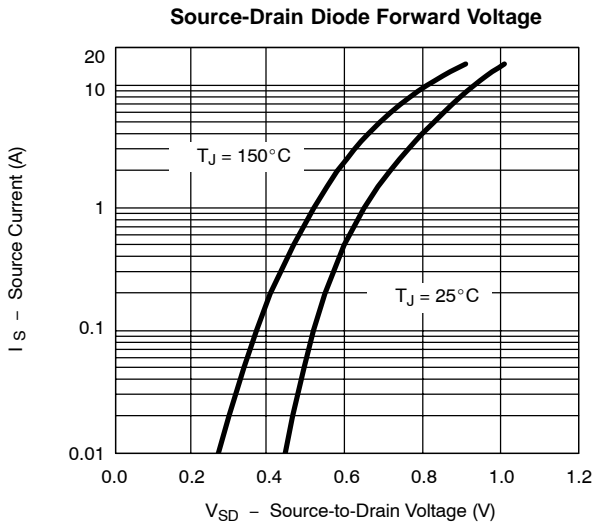
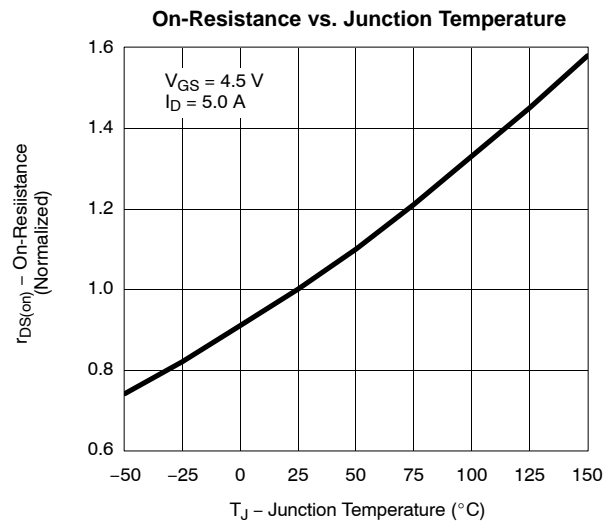
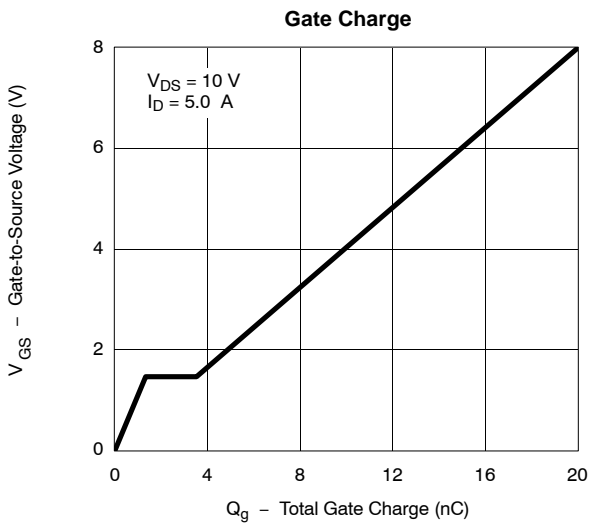
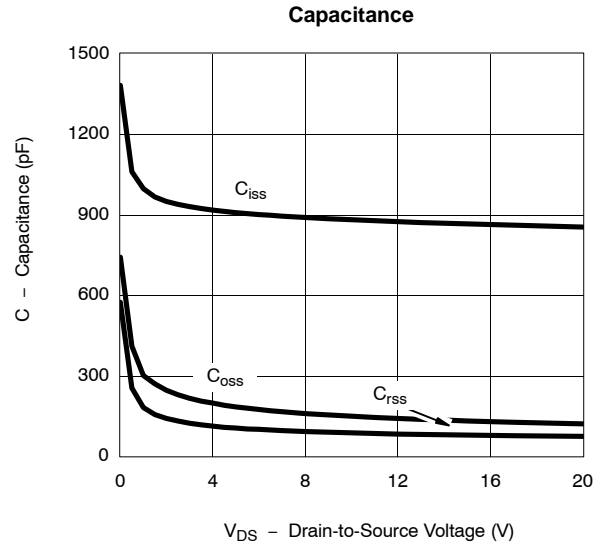
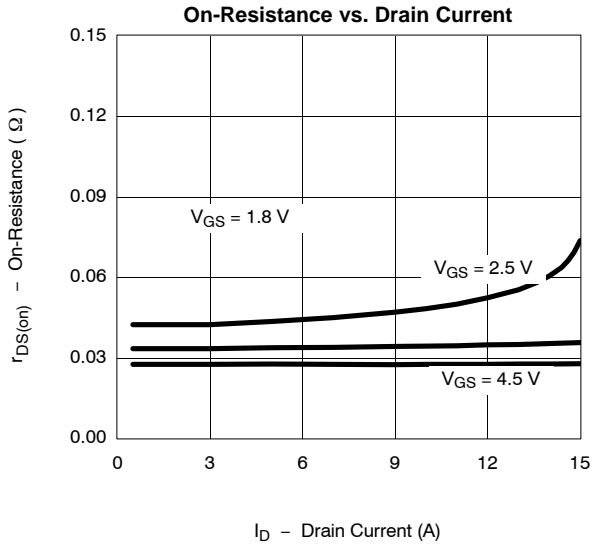
- a. Pulse test: PW ≤ 300 μs duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

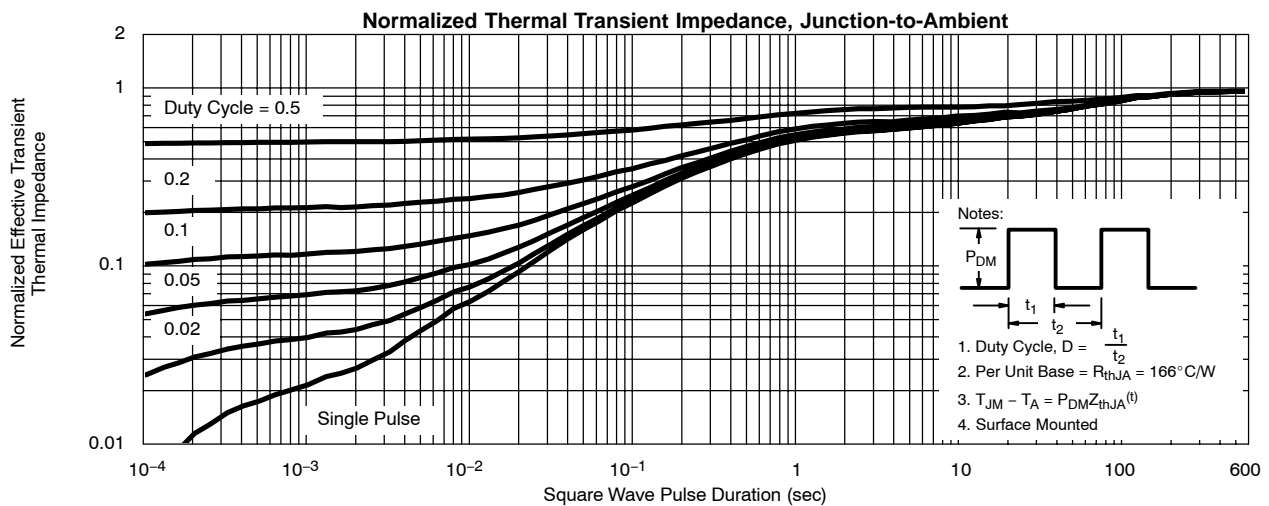
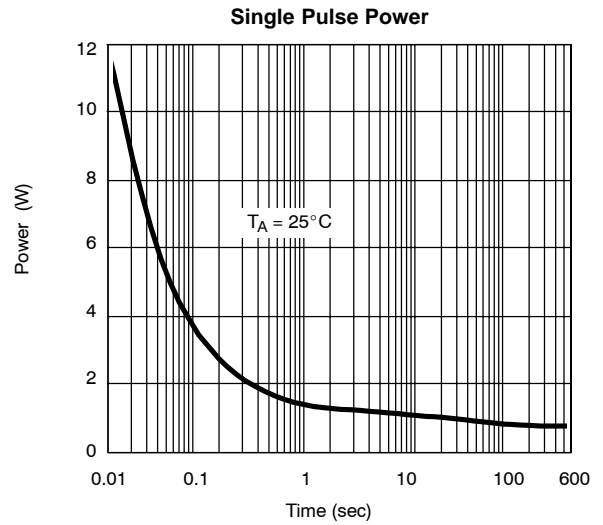
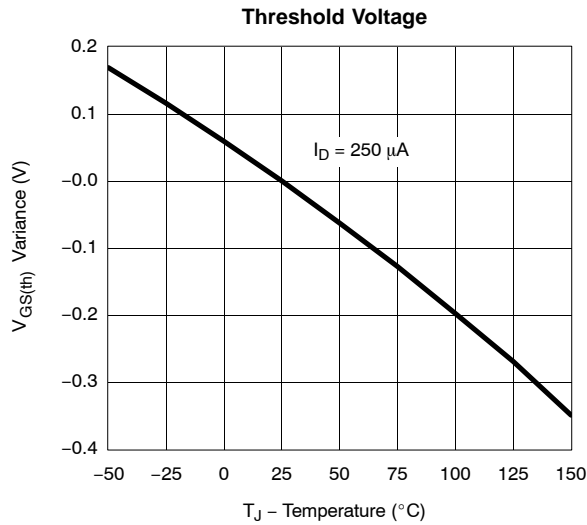
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)




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