VRE125/127 Precision High Temperature **Reference Supplies**



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FEATURES

• WIDE OPERATING RANGE: -55°C to +200°C

VERY HIGH ACCURACY: 5.000 V ±0.4 mV

• EXTREMELY LOW DRIFT: 0.6 mV (-55°C to +150°C)

2.0 mV (-55°C to +200°C)

• EXCELLENT STABILITY: 6 ppm / 1000Hrs.

• EXCELLENT LINE REGULATION: 6 ppm / V Typ.

• HERMETIC 14-PIN DIP

APPLICATIONS

- PRECISION A/D and D/A CONVERTERS
- ACCURATE COMPARATOR THRESHOLD VOLTAGE
- HIGH RESOLUTION SERVO SYSTEMS
- HIGH TEMPERATURE TEST and **MEASUREMENT SYSTEMS**
- TRANSDUCER EXCITATION
- GEOLOGICAL EQUIPMENT

DESCRIPTION

VRE125 series references are designed to operate over an extremely wide temperature range (-55°C to +200°C) and still provide excellent accuracy. The VRE125 provides a +5V output and the VRE127 provides a ±5V output. All types are available in commercial (C suffix) and military (M suffix) models. They are hermetically sealed and are screened for high reliability and quality. Two accuracy grades (standard and A) are available in both models. The adjacent selector guide shows the limits of the most important parameters of VRE125/127 series references.

The accuracy of the VRE125/127 series over temperature is achieved by using Thaler

SELECTION GUIDE

Туре	Output	Output Max. Volt Max. Volt Deviation (-55°C to+150°C) to+200°C)		Initial Error (Max)	
VRE125C	+5V	±0.9mV	±3.0mV	±0.8mV	
VRE125CA	+5V	±0.6mV	±2.0mV	±0.4mV	
VRE125M	+5V	±0.9mV	±3.0mV	±0.8mV	
VRE125MA	+5V	±0.6mV	±2.0mV	±0.4mV	
VRE127C	±5V	±0.9mV	±3.0mV	±0.8mV	
VRE127CA	±5V	±0.6mV	±2.0mV	±0.4mV	
VRE127M	±5V	±0.9mV	±3.0mV	±0.8mV	
VRE127MA	±5V	±0.6mV	±2.0mV	±0.4mV	

Corporation's patented multi-point compensation technique. The stability of the VRE125 series is enhanced by using a zener diode instead of a bandgap reference, which is typically used in 5V references. Zener diodes have better long term stability and don't suffer the significant shifts caused by temperature cycling that bandgap references do.

Other performance parameters, such as warm-up drift and long term stability are better than competitive models.

Superior stability, accuracy, and quality make these references ideal for high temperature applications such as A/D and D/A converter references.

ELECTRICAL SPECIFICATIONS Vps =±15V, T = 25°C, RL = 10KΩ unless otherwise noted.													
MODEL	С		CA		М			MA					
PARAMETERS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
ABSOLUTE MAXIMUM RATINGS													
Power Supply Operating Temperature Storage Temperature Short Circuit Protection	-65	ntinud	±22 200 150 ous	* *	*	* *	* *	*	* *	* * *	*	* *	ى 5 ئ
OUTPUT VOLTAGE													
VRE125 VRE127		+5 ±5			*			*			*		V V
OUTPUT VOLTAGE ERR	OUTPUT VOLTAGE ERRORS												
Initial Error Warmup Drift (1) -55°C to 150°C (3) 150°C to 200°C Long-Term Stability Noise (.1-10Hz)		2 6 3	0.8 0.9 3.0		1 *	0.4 0.6 2.0		2	0.8		*	0.4	mV ppm mV mV ppm/1000hr. μVpp
OUTPUT CURRENT		-	•	-	•	•	-	-		-		•	
Range	±10			*			*			*			mA
REGULATION				•	-			-			-		
Line Load		6 3	10		*	*		*	*		*	*	ppm/V ppm/mA
OUTPUT ADJUSTMENT													
Range Temperature Coefficient		10 4			*			*			*		mV μV/°C/mV
POWER SUPPLY CURRENTS (2)													
VRE125 +PS VRE127 +PS VRE127 -PS		4 6 4	7 9 6		* *	* *		* * *	* *		* * *	* * *	mA mA mA

NOTES: *Same as C Models.

- 1.Using the box method, the specified value is the maximum deviation from the output voltage at 25°C over the specified operating temperature range.
- 2. The specified values are unloaded.
- 3. Cone widening from 150 °C value to specified value.

TYPICAL PERFORMANCE CURVES V_{OUT} vs. TEMPERATURE V_{OUT} vs. TEMPERATURE V_{out} vs. TEMPERATURE **V_{OUT} vs. TEMPERATURE** 3.0 0.3 0.3 3.0 Upper Limit Upper Limit **Upper Limit** (E) 0.6 (mV) (E) 0.6 Upper Limit ∆Vout (mV) 0.9 0.9 **∆**Vout (Vout 0.6 -0.9 -0.9 _ower Limit Lower Limit Lower Limit Lower Limit -3.0 3.0 -3.0 -50 100 150 -50 100 150 -50 50 100 150 -50 100 150 Temperature °C VRE125/127CA Temperature °C Temperature °C Temperature °C VRE125/127C VRĖ125/127M VRE125/127MA **VRE125** QUIESCENT CURRENT VS. TEMP JUNCTION TEMP. RISE VS. OUTPUT CURRENT **PSRR VS. FREQUENCY** 7.0 120 Quiescent Current (mA) 100 Junction Temperature Rise Above Ambient 6.0 30 PSRR (dB) 80 5.0 60 40 -4.0 10 20 0 0 -50 0 50 10 100 1k 10k 100k 1M 8 Temperature °C **Output Current (mA)** Frequency (Hz) **VRE127 POSITIVE OUTPUT** JUNCTION TEMP. RISE VS. OUTPUT CURRENT **QUIESCENT CURRENT VS. TEMP PSRR VS. FREQUENCY** 7.0 120 Junction Temperature Rise Above Ambient C C C Quiescent Current (mA) 100 6.0 (dB) 80 5.0 60 PSRR 40 -4.0 20 0 0 -50 0 50 100 10 100 1k 10k 100k 1M 10M 6 **Output Current (mA)** Temperature °C Frequency (Hz) **NEGATIVE OUTPUT** JUNCTION TEMP. RISE VS. OUTPUT CURRENT QUIESCENT CURRENT VS. TEMP **PSRR VS. FREQUENCY** 6.0 120 40 Quiescent Current (mA) Junction Temperature Rise Above Ambient 100 5.0 PSRR (dB) 80 4.0 60 40 3.0 20 0 0 0 -50 50 100 0 10 100 1k 10k 100k 1M 6 8 Temperature °C **Output Current (mA)** Frequency (Hz) VRE125DS REV. C SEPT.1994

DISCUSSION OF PERFORMANCE

THEORY OF OPERATION

The following discussion refers to the schematic below. A FET current source is used to bias a 6.3V zener diode. The zener voltage is divided by the resistor network R1 and R2. This voltage is then applied to the noninverting input of the operational amplifier which amplifies the voltage to produce a 5.000V output. The gain is determined by the resistor networks R3 and R4: G=1 + R4/R3. The 6.3V zener diode is used because it is the most stable diode over time and temperature.

The current source provides a closely regulated zener current which determines the slope of the reference's voltage vs. tamperature function. By trimming the zener current a lower drift over temperature can be achieved. But since the voltage vs. temperature function is nonlinear, this method leaves a residual error over wide temperature ranges.

To remove this residual error, Thaler has developed a nonlinear compensation network of thermistors and resistors that is used in the VRE series references. This proprietary network eliminates most of the nonlinearity in the voltage vs. temperature function. By then adjusting the slope, Thaler Corporation produces a very stable voltage over wide temperature ranges. This network is less than 2% of the overall network resistance so it has a negligible effect on long term stability.

APPLICATION INFORMATION

Figure 1 shows the proper connection of the VRE125 series voltage reference with the optional trim resistors. When trimming the VRE127, the positive voltage should be trimmed first since the negative voltage tracks the positive side. Pay careful attention to the circuit layout to avoid noise pickup and voltage drops in the lines.

When using precision voltage references at high temperatures it is best to keep them powered up. If the zener diode isn't powered up at high temperatures the junction will collect ions, and then when power is applied, the voltage will drift until the charge build up is depleted.

The VRE125 series voltage references have the ground terminal brought out on two pins (pin 6 and pin 7) which are connected together internally. This allows the user to achieve greater accuracy when using a socket. Voltage references have a voltage drop across their power supply ground pin due to quiescent current flowing through the contact resistance. If the contact resistance was constant with time and temperature, this voltage drop could be trimmed out. When the reference is plugged into a socket, this source of error can be as high as 20ppm. By connecting pin 7 to the power supply ground and pin 6 to a high impedance ground point in the measurement circuit, the error due to the contact resistance can be eliminated. If the unit is soldered into place the contact resistance is sufficiently small that it doesn't effect performance.

VRE125

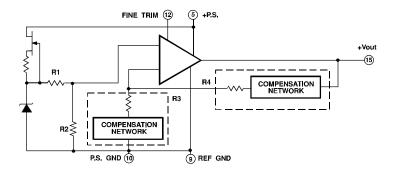


FIGURE 2

VRE127

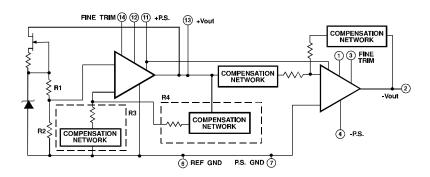
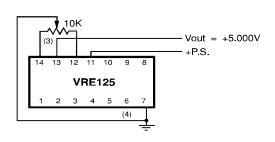


FIGURE 3

VRE125DS REV. C SEPT 1994

EXTERNAL CONNECTIONS



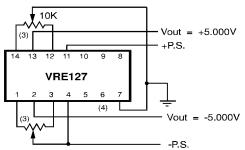
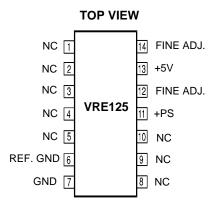
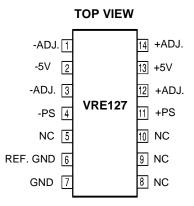


FIGURE 1

- 3. Optional Fine Adjust for approximately ±10mV. VRE127 center tap connects to -PS. 4. Pin 6 is internally connected to Pin 7 and can be used as Ref. GND.

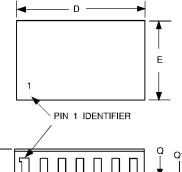
- PIN CONFIGURATION -



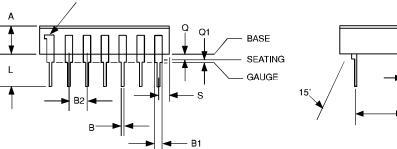


- MECHANICAL -

14-PIN HYBRID PACKAGE



	INCHES		MILLIMETER			INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX	DIM	MIN	MAX	MIN	MAX
Е	.480	.500	12.1	12.7	Α	.120	.155	3.0	4.0
L	.195	.215	4.9	5.4	Q	.015	.035	0.4	0.9
D	.775	.805	19.7	20.4	Q1	N/A	.030	N/A	0.7
В	.016	.020	0.4	0.5	С	.009	.012	0.2	0.3
B1	.038	.042	0.9	1.0	G1	.290	.310	7.3	7.8
B2	.095	.105	2.4	2.6					
S	.085	.105	2.1	2.6					
Р	.004	.006	0.10	0.15					



VRE125DS REV. C SEPT 1994