## TOSHIBA BiCD Digital Integrated Circuit Silicon Monolithic

## TB62750FTG

## Step Up Type DC－DC Converter for White LED


#### Abstract

TB62750FTG is the high efficient STEP－UP type DC／DC converter by which the optimal design was carried out at constant current driver of white LED． It is possible to drive white LEDs whose constant output current is 800 mA by the lithium ion battery． This IC contains the Nch－MOS transistor required for switching of external inductor． The forward current of LED is set up by the external resistor． This IC is the most suitable as a driver of camera flashes and strobes in the cellular phone，the PDA，the DSC，and other mobiles．


## Features

－Switching terminal current ：1．8A（Typ．）
－Maximum output power
－Maximum output voltage
：10V
－Operating frequency $: 2 \mathrm{MHz}$
－Four operating modes
Light mode
：ILED $=350 \mathrm{~mA}$（Typ．）$(\mathrm{R} 3=8.2 \mathrm{k} \Omega, \mathrm{R} 2=62 \mathrm{k} \Omega)$
Flash mode
：ILED＝800mA（Typ．）（RSENS＝0．375 $\Omega$ ）
Shutdown mode ：Icc＝0．01 $\mu \mathrm{A}$（Typ．）
－High efficiency ：Maximum efficiency ：80\％（In the flash mode）

（VQON24－0404－0．65）
Weight： 0.09 g （Typ．）
現品表示 ；

$■$ Pin Assignment (top view)


- Block Diagram



## ■ Explanation of the Terminal

| No. | Symbol | Function |
| :---: | :---: | :---: |
| 1 | K3 | Connected to the LED cathode. |
| 2 | RSENS3 | Connected to the current-setting resistor: R1 for the flash. |
| 3 | K2 | Connected to the LED cathode. |
| 4 | RSENS2 | Connected to the current-setting resistor: R1 for the flash. |
| 5 | K1 | Connected to the LED cathode. |
| 6 | RSENS1 | Connected to the current-setting resistor: R1 for the flash. |
| 7 | FB+ | Detecting the voltage of the current-setting resistor: R1 for the flash. It can change |
| 8 | FB- | the light-mode-current with the resistor: R3 which is connected to FB+. |
| 9 | NC | No Connect |
| 10 | NC | No Connect |
| 11 | IL | Connected to the ILED setting resistor: R2 in the light mode. <br> The light-mode-current is changed depending on the setting ratio of R2 and R3. |
| 12 | SS | Setting the constant to limit the current when DC-DC operation starts. <br> The rising time is changed depending on the constant of the condenser. |
| 13 | PGND3 | Ground terminal for the power device. |
| 14 | SW3 | Switching the DC-DC converter. Nch MOSFET is built-in. |
| 15 | PGND2 | Ground terminal for the power device. |
| 16 | SW2 | Switching the DC-DC converter. Nch MOSFET is built-in. |
| 17 | PGND1 | Ground terminal for the power device. |
| 18 | SW1 | Switching the DC-DC converter. Nch MOSFET is built-in. |
| 19 | OVP | Detecting the over-voltage. |
| 20 | EN2 | Inputting the logic signals which set the modes. <br> "ON" :Flash mode <br> "OFF" :Lighting mode |
| 21 | EN1 | Inputting the logic signals which set the modes. <br> "ON" :DC-DC operation <br> "OFF" :Stop |
| 22 | AGND | Ground terminal for analog. |
| 23 | VIN | Inputting the power supply voltage to the IC. The operating voltage is 2.8 to 5.5 V . |
| 24 | NC | No Connect |

Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{opr}}=25^{\circ} \mathrm{C}$ if not specified)

| Characteristics | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: |
| Power supply voltage | VCC | $-0.3 \sim 6.0$ | V |
| Input voltage | Vin | $-0.3 \sim \mathrm{VIN}+0.3$ | V |
| Switching terminal voltage | Vo(SW) | $-0.3 \sim 12$ | V |
| Output current | lout | 1000 | mA |
| Operating temperature range | Topr | $-40 \sim 85$ | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | $-55 \sim 150$ | ${ }^{\circ} \mathrm{C}$ |

Recommended Operating Condition ( $\mathrm{T}_{\mathrm{opr}}=-40$ to $85^{\circ} \mathrm{C}$ if not specified)

| Characteristics |  | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Power supply voltage |  | VIN | - | 2.8 | - | 5.5 | V |
| Logic Input <br> Voltage | HIGH | VIH | EN1,EN2 | 0.7 XVIN | - | VIN +0.15 | V |
|  | LOW | VIL |  | -0.15 | - | $0.3 \times \mathrm{VIN}$ |  |
| Constant-current output |  | lout | - | - | 800 | 1000 | mA |
| ISET <br> Resistor | Flash mode | R1 | - | - | 0.375 | - | $\Omega$ |
|  | Light mode | R2 | - | 51 | 62 | 75 | k $\Omega$ |
|  |  | R3 |  | - | 8.2 | - | $\mathrm{k} \Omega$ |
| Input condenser |  | Cin | - | - | 20 | - | $\mu \mathrm{F}$ |
| Output condenser |  | Cout | - | - | 10 | - | $\mu \mathrm{F}$ |
| Condenser for soft start |  | Css | - | 2200 | 3300 | 10000 | pF |
| External inductor |  | L | - | - | 3.9 | - | $\mu \mathrm{H}$ |

Electrical Characteristics ( $\mathrm{T}_{\mathrm{opr}}=-40 \sim 85^{\circ} \mathrm{C} \mathrm{V}_{\mathrm{Cc}}=2.8 \sim 5.5 \mathrm{~V}$ if not specified.)
Characteristics of DC-DC regulator. (Topr $=-40 \sim 85^{\circ} \mathrm{C}, \mathrm{VIN}=2.8 \sim 5.5 \mathrm{~V}$ if not specified)

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Power supply voltage | VIN | - | 2.8 | - | 5.5 | V |
| Operating consumption current | IIN(On) | $\begin{gathered} \mathrm{EN} 1=" \mathrm{H} ", \mathrm{EN} 2=" \mathrm{~L} " \\ \mathrm{Vin}=3.6 \mathrm{~V}, \mathrm{R} 2=62 \mathrm{k} \Omega \\ \mathrm{R} 3=8.2 \mathrm{k} \Omega, \mathrm{R} 1=0.375 \Omega \end{gathered}$ | - | TBD | - | mA |
| Quiescent consumption current | IIN(Off) | $\begin{gathered} \text { EN1="L", EN2="L" } \\ \text { Vin=3.6V } \end{gathered}$ | - | 0.01 | 0.5 | $\mu \mathrm{A}$ |
| Input current <br> (Logic input terminal) | IINEN1 IINEN2 | Vin $=3.6 \mathrm{~V}$ | - | - | 0.5 | $\mu \mathrm{A}$ |
| Integrated MOS-Tr <br> switching frequency | fosc | Vin=3.6V | 1.6 | 2 | 2.4 | MHz |
| SW terminal protection voltage | Vo(SW) | - | - | 12 | - | V |
| Switching terminal current | lo(SW) | - | - | 1.8 | - | A |
| Switching terminal leakage current | Ioz(SW) | - | - | 0.5 | 1 | $\mu \mathrm{A}$ |
|  |  | $\begin{gathered} \mathrm{EN} 1=" \mathrm{H}^{\prime}, \mathrm{EN} 2=" \mathrm{H} " \\ \mathrm{Vin}=3.6 \mathrm{~V}, \mathrm{R} 1=0.375 \Omega \\ \mathrm{Topr}=25^{\circ} \mathrm{C} \end{gathered}$ | 285 | 300 | 315 | mV |
| FB terminal feedback voltage | VFB | $\begin{gathered} \mathrm{EN} 1=" \mathrm{H} ", \mathrm{EN} 2=" \mathrm{~L} " \\ \mathrm{Vin}=3.6 \mathrm{~V}, \mathrm{R} 1=0.375 \Omega \\ \mathrm{R} 2=62 \mathrm{k} \Omega, \mathrm{R} 3=8.2 \mathrm{k} \Omega \\ \mathrm{Topr}=25^{\circ} \mathrm{C} \end{gathered}$ | TBD | 130 | TBD | mV |
| FB terminal line regulation | $\triangle$ VFB | $\begin{aligned} & \text { Vin }=3.6 \mathrm{~V} \text { (typ.) } \\ & \mathrm{Vin}=3.0 \sim 4.2 \mathrm{~V} \end{aligned}$ | - | 2 | - | \% |
| OVD voltage (OVD terminal) | $\mathrm{V}_{\text {OVD }}$ | - | - | 10.7 | - | V |
| TSD |  |  | 120 | 157 | 180 | ${ }^{\circ} \mathrm{C}$ |

Characteristics of constant-current of $\operatorname{SINK}$ (Topr $=25^{\circ} \mathrm{C}, \mathrm{Vcc}=3.6 \mathrm{~V}$ if not specified.)

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ISET output current (Light mode) | ISET | $\begin{gathered} \mathrm{EN} 1=\text { "H" } \\ \mathrm{EN} 2=" \mathrm{~L} " \\ \mathrm{R} 2=62 \mathrm{k} \Omega, \mathrm{R} 3=8.2 \mathrm{k} \Omega \\ \mathrm{R} 1=0.375 \Omega \\ \hline \end{gathered}$ | - | 20 | - | $\mu \mathrm{A}$ |
| ISET output voltage (Light mode) | VISET | $\begin{aligned} & \mathrm{EN} 1=\text { "H" } \\ & \mathrm{EN} 2=\text { "L" } \\ & \text { R2= } 62 \mathrm{k} \Omega \end{aligned}$ | - | TBD | - | V |
| Lighting time in flash mode | t FLASH | Tolerance time under EN2 = " H " and EN1 = "H". $R 1=0.375 \Omega$ | - | 100 | 300 | ms |

## Explanation of operation

## 【Setting the operating mode】

1.Selecting the four modes shown below with logic input terminals: EN1 and EN2.

| EN1 | EN2 | MODE |
| :---: | :---: | :---: |
| L | L | Shutdown mode |
| L | H |  |
| H | L | Flash mode |
| H | H |  |

- Flash mode

It can be driven with the constant-current at 800 mA (max.). The maximum lightning time is 300 ms .
Because it controls the $\mathrm{FB}+$ at 300 mV , the equation shown below is obtained.

$$
\operatorname{lout}(\mathrm{mA})=300(\mathrm{mV}) \div \mathrm{R} 1(\Omega)
$$

Pay attentions to the differences of the connected resistors and the power consumption.
The recommended resistor is as follows;

- Three or more resistors ( $1 / 4 \mathrm{~W}$ ) in parallel.
- Light mode

It can light continually by driving at constant-current ( 350 mA in max.).
It sets the current of K1 to K3 based on the resistors: R1, R2, and R3.

$$
\text { lout }(\mathrm{mA})=\frac{0.3 \mathrm{~V}-\left(1.2 \mathrm{~V}(\text { Internal Vref) }) \times \frac{\mathrm{R} 3}{\mathrm{R} 2}\right)}{\mathrm{R} 1}
$$

The current value of IL is recommended at about $20 \mu \mathrm{~A}$.
Set R2 at $51 \mathrm{k} \Omega$ to $75 \mathrm{k} \Omega$.
When R3 is set at $0 \Omega$, the same current is set in the flash mode and the light mode.

- Shutdown mode

It stops the operation.

## 【Setting the input condenser】

TBD

## 【Setting the output condenser】

TBD

## 【Setting the external inductor】

Operating frequency： 2 MHz
The circuit operation has the consecutive mode method．The ability of the output current is changed by the constant of the inductor．

## 【Soft start function】

This function is built－in to avoid the over－inserting current and the power drop when the flash mode and the light mode switch．
When the condenser（Approximately 3300pF）is connected to the SS terminal，the rising time becomes about $300 \mu$ $s$ in the flash mode．

## 【OVD function】

OVP voltage：Detecting the over－load at 10.7 V （TYP）．
When the loaded voltage of OVP rises because of the LED opening or something other， this function shutdowns the IC．
The operation recovers just after the voltage of OVP falls to 10.7 V or less．

## 【TSD function】

MAX $180^{\circ} \mathrm{C}$
MIN $120^{\circ} \mathrm{C}$

## SOLDERABILITY

The following conditions apply to solderability.

- Solderability
(1) Use of $\mathrm{Sn}-63 \mathrm{~Pb}$ solder bath
- solder bath temperature $=230^{\circ} \mathrm{C}$, dipping time $=5$ seconds, number of times $=$ once, use of R -type flux
(2) Use of $\mathrm{Sn}-3.0 \mathrm{Ag}-0.5 \mathrm{Cu}$ solder bath


## CAUTION

- Particular care is necessary in the design of the output, VCC, COMMON and GND lines since the IC may be destroyed by short circuits between outputs, air contamination faults, or faults arising from improper grounding.
- Do not insert devices in the wrong orientation. Make sure that the positive and negative terminals of power supplies are connected correctly. Otherwise the rated maximum current or power dissipation may be exceeded and the device may break down or undergo performance degradation, causing it to catch fire or explode and resulting in injury.
- Note that the IC may be destroyed as a result of damage to or misconnection of external components.


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