TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA2028F,TA2028P

Filter IC For Σ - Δ Modulation System DA Converter

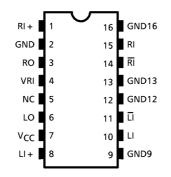
TA2028F, TA2028P are an analog filter IC for $\Sigma\text{--}\Delta$ modulation system DA converter.

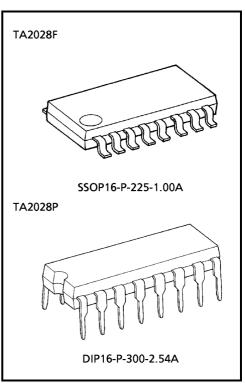
Using the TA2028F, TA2028P in combination the TC9237BF, TC9237BN (the Σ - Δ modulation system DA converter with a built-in digital filter), it is possible to construct a DA conversion system with less external parts.

Features

- Built-in CR for LPFs and output (differential) amplifiers for the left and right channel.
- Single power supply operation. (+9V operation: BS tuner system)
- Noise distortion factor and S / N ratio are as follows (when operating at +5V single power supply): Noise distortion factor: -86dB (typ.) S / N: 100dB (typ.)

Pin Connection (top view)



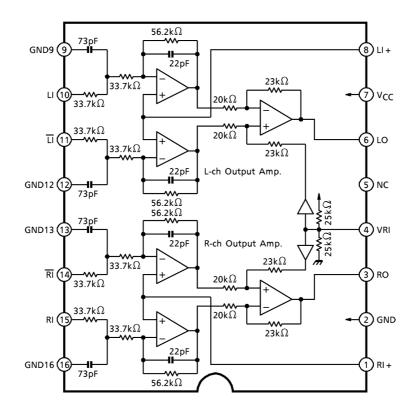


Weight

SSOP16-P-225-1.00A: 0.14g (typ.) DIP16-P-300-2.54A: 1.00g (typ.)

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Block Diagram



Description Of Pin Functions

Pin No.	Symbol	1/0	Function & Operation	Remarks
1	RI +	I	R channel operational amplifier forward input pin. Connect to VRI.	_
2	GND	_	Ground pin.	—
3	RO	0	R channel analog output pin.	—
4	VRI	—	Reference voltage pin. (V _{CC} / 2)	See the block diagram
5	NC	_	Non-connecting pin. NC pin is used in the open state.	—
6	LO	0	L channel analog output pin.	—
7	V _{CC}	—	Supply voltage pin.	—
8	LI +	I	L channel operational amplifier forward input pin. Connect to VRI.	—
9	GND9	_	Ground pin for L channel reverse input side filter.	_
10	LI	I	L channel forward input pin.	Connect to LO of TC9237BF, TC9237BN
11	LI	I	L channel reverse input pin.	Connect to LO of TC9237BF, TC9237BN
12	GND12	_	Ground pin for L channel forward input side filter.	_
13	GND13	_	Ground pin for R channel forward input side filter.	_
14	 RI	I	R channel reverse input pin.	Connect to RO of TC9237BF, TC9237BN
15	RI	Ι	R channel forward input pin. Connect to RO TC9237BF, TC	
16	GND16	_	Ground pin for R channel reverse input side filter.	

Maximum Ratings (Ta = 25°C)

Charao	cteristic	Symbol	Rating	Unit	
Supply voltage		V _{CC}	11	V	
Power dissipation	TA2028F	Pa	350 (*)	mW	
Fower dissipation	TA2028P	PD	1388 (**)	IIIVV	
Operating tempera	ture	T _{opr}	-25~75	°C	
Storage temperatu	re	T _{stg}	-55~150	°C	

(*) Reduce 2.8mW / °C at Ta = above 25° C.

(**) Reduce 11.2 mW / °C at Ta = above 25 °C.

Electrical Characteristics (unless otherwise specified, V_{CC} = 5V, Ta = 25°C)

Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Operating supply voltage	V _{CC}	_	Ta = –35~85°C	8.0	9.0	10	V
Operating supply current	ICCQ	_	At no signal	8.1	11.0	13.7	mA
Reference voltage	VRI	_	—	4.4	4.5	4.6	V
	THD (1)	1	1kHz, V _o = 2mV _{rms}	_	-86	-83	dB
Noise distortion factor	THD (2)		10kHz, V _o = 2mV _{rms}	_	-86	-83	
	THD (3)		1kHz, V _o = 100mV _{rms}	_	-74	-70	
Cross talk	СТ	1	1kHz, V _o = 2mV _{rms}	_	-100	-90	dB
Attenuation	ATT (1)	1	40kHz, V _o = -10dBV _{rms}	0.51	0.71	1.41	dB
Allenualion	ATT (2)		80kHz, V _o = -10dBV _{rms}	1.50	2.70	4.50	uБ
Max. output level	V _{omax}	1	1kHz, THD = 1%	2.5	2.6	—	V _{rms}
Differential balance	G _{VB}	1	1kHz, 1.1dBV _{rms} In–phase input	_	_	-40	dB
LR output difference	G _{VD}	1	1kHz, 1.1dBV _{rms} Differential input	_	0	0.5	dB

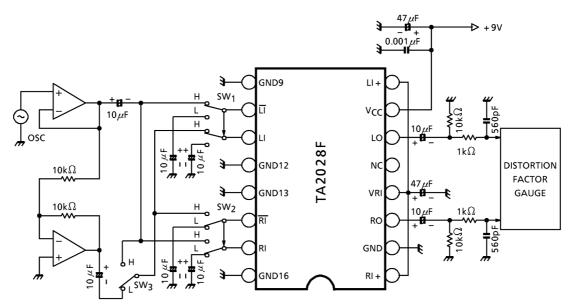
(Note 1) When the TC9327BF, TC9237BN (+5V) and +9V single power supply are operated : Full scale = 2mV_{rms} (typ.).

(Note 2) The amount of attenuations is based on 1kHz, $V_0 = -10 dBV_{rms}$.

(Note 3) Measuring circuit–1: Indicates the measuring circuit.

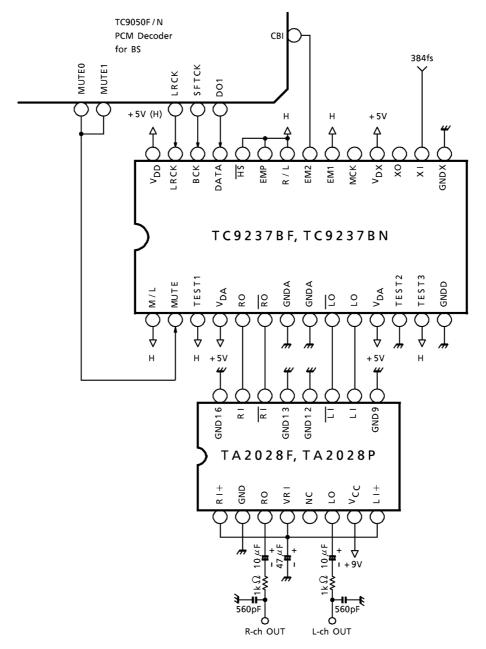
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Test Circuit–1



SW ₁	SW ₂	SW3	Measuring Item
L	L	_	Operating supply voltage, reference voltage
L	Н	L	Cross talk ($R \rightarrow L$)
н	L	L	Cross talk $(L \rightarrow R)$
Н	н	L	Noise distortion factor, attenuation, maximum output level, LR output difference
Н	Н	Н	Difference balance

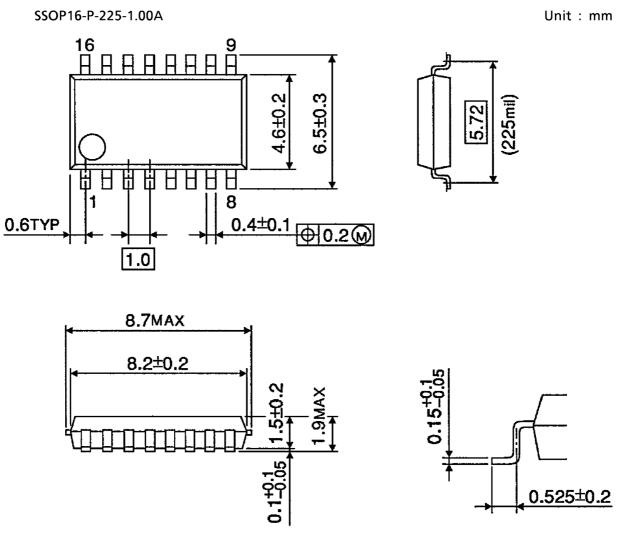
Application Circuit Example



(Cautions)

- Quality of crystal oscillation waveform largely effects $\rm S$ / $\rm N$ ratio.
- Further, this is also true when system clock is input externally through the XI pin of pin(16).
- Suppress glitch of input signals (LRCK, BCK, DATA) as could as possible.
- The wiring between the TC9237BF, TC9237BN output and the analog filter amplifier input must be made the shortest
- The capacitor between $V_{\mbox{DA}}$ and GNDA shall be connected as close to the pin as possible.
- NC pin is used in the open state.

Package Dimensions

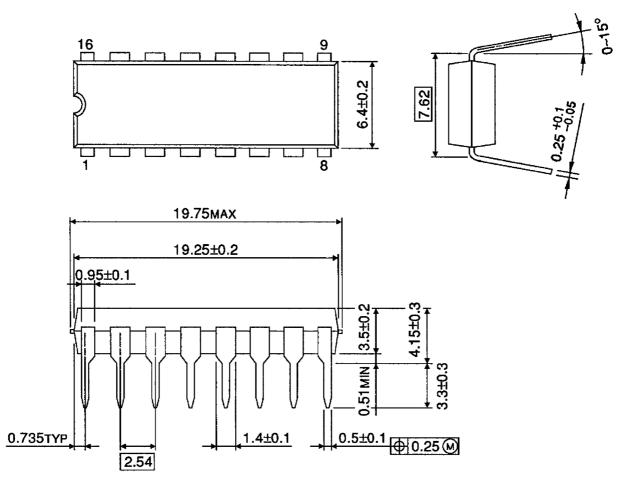


Weight: 0.14g (typ.)

Package Dimensions

DIP16-P-300-2.54A

Unit : mm



Weight: 1.00g (typ.)

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