

STA423N

PNP Silicon Transistor

Descriptions

• High voltage application

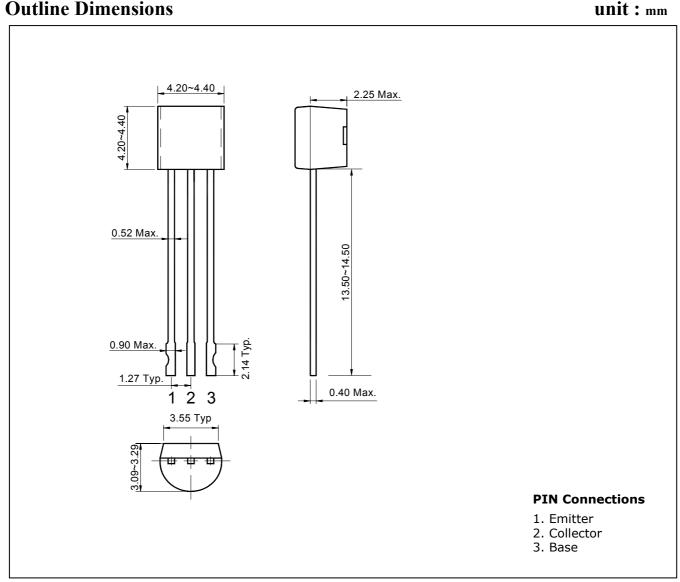
Features

- \bullet High collector-emitter voltage : $V_{\text{CEO}} = -250 \text{V}$
- Complementary pair with STC422N

Ordering Information

Type NO.	Marking	Package Code		
STA423N	STA423	TO-92N		

Outline Dimensions



KSD-T0C028-001

STA423N

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-250	V
Collector-emitter voltage	V_{CEO}	-250	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_{C}	-100	mA
Collector power dissipation	P_{C}	400	mW
Junction temperature	T ₁	150	°C
Storage temperature range	T_{stg}	-55~150	°C

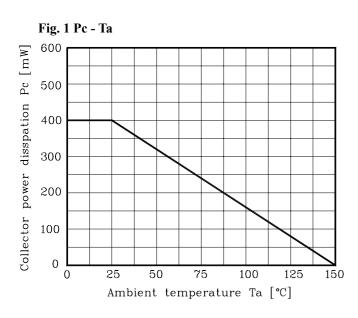
Electrical Characteristics

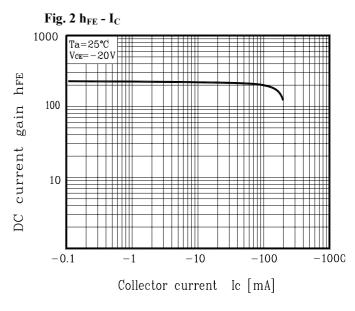
(Ta=25°C)

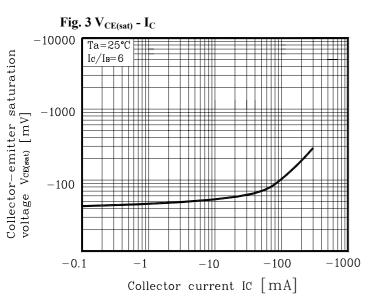
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-emitter breakdown voltage	BV _{CEO}	I_C =-1mA, I_B =0	-250	1	1	V
Collector cut-off current	I_{CBO}	V_{CB} =-250V, I_{E} =0	-	ı	-100	nA
Emitter cut-off current	I_{EBO}	V_{EB} =-5V, I_C =0	-	1	-100	nA
DC current gain	h _{FE}	V_{CE} =-20V, I_{C} =-25mA	50	1	1	-
Collector-emitter saturation voltage	$V_{CE(sat)}$	I_C =-30mA, I_B =-5mA	-	ı	-0.6	V
Base-emitter voltage	V_{BE}	V_{CE} =-20V, I_{C} =-25mA	-	-0.67	-0.9	V
Transition frequency	f_T	V_{CE} =-20V, I_{C} =-10mA	-	80	1	MHz
Collector output capacitance	C _{ob}	V_{CB} =-20V, I_E =0, f=1MHz	-	1	-	pF

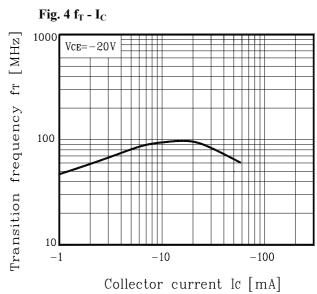
KSD-T0C028-001 2

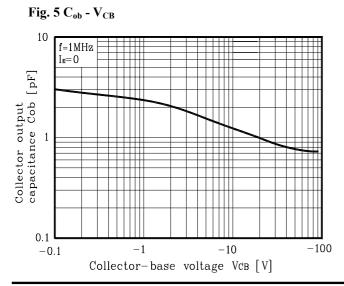
Electrical Characteristic Curves











KSD-T0C028-001 3

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KSD-T0C028-001 4