



ST19WG18 ST19WG34, ST19WG66

Smartcard MCU

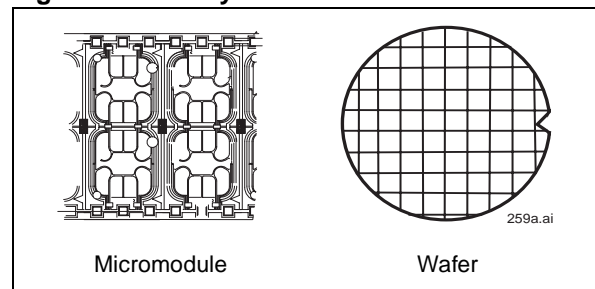
With 18, 34, 66 Kbytes High Density EEPROM

DATA BRIEF

PRODUCT FEATURES

- ENHANCED 8 BIT CPU WITH EXTENDED ADDRESSING MODES
- 224 KBYTES USER ROM WITH PARTITIONING
- 6 KBYTES USER RAM WITH PARTITIONING
- 18 (ST19WG18), 34 (ST19WG34), 66 (ST19WG66) KBYTES USER EEPROM WITH PARTITIONING including 128 BYTES USER and ST OTP AREA:
 - Highly reliable CMOS EEPROM submicron technology
 - Error Correction Code for single bit fail correction within a byte
 - 10 year data retention
 - 500,000 Erase/Write cycles endurance
 - 1 to 64 bytes Erase or Program in 2 ms
- SECURITY FIREWALLS FOR MEMORIES, and DES ACCELERATOR.
- VERY HIGH SECURITY FEATURES INCLUDING EEPROM FLASH PROGRAMMING AND CLOCK MANAGEMENT.
- 3x8 BIT TIMERS WITH INTERRUPT CAPABILITY
- HARDWARE DES ACCELERATOR WITH LIBRARY SUPPORT FOR SYMMETRICAL ALGORITHMS:
 - DES, triple DES, DESX computations and CBC chaining mode...
- ISO 3309 CRC CALCULATION BLOCK
- FIPS 140-2 COMPLIANT RANDOM NUMBER GENERATOR WITH TWO GUN REGISTERS (Generators of Unpredictable Number)
- 1.62 V TO 5.5 V SUPPLY VOLTAGE
- EXTERNAL CLOCK FREQUENCY UP TO 8 MHz
- HIGH PERFORMANCE PROVIDED USING INTERNAL CLOCK FREQUENCY
- UNIQUE SERIAL NUMBER ON EACH DIE
- POWER SAVING STANDBY MODE
- CONTACT ASSIGNMENT COMPATIBLE ISO 7816-2
- SERIAL ACCESS I/O, ISO 7816-3 COMPATIBLE
- ESD PROTECTION GREATER THAN 5000 V

Figure 1. Delivery Form



Function	Speed ⁽¹⁾
Triple DES (with keys loaded)	24 μ s
Single DES (with keys loaded)	15 μ s

1. Typical values, independent from external clock frequency and supply voltage.

HARDWARE DESCRIPTION

The product, member of the ST19W platform, is a serial access microcontroller specially designed for cost effective secure portable applications.

It is manufactured using an advanced highly reliable ST CMOS EEPROM technology.

It is based on the STMicroelectronics 8 bit CPU already implemented on the ST19X product family and includes on-chip memories: User ROM, User RAM and EEPROM with state of the art security features. ROM, RAM and EEPROM memories can be configured into partitions with customized access rules.

An additional ST ROM contains all ST provided functions and libraries.

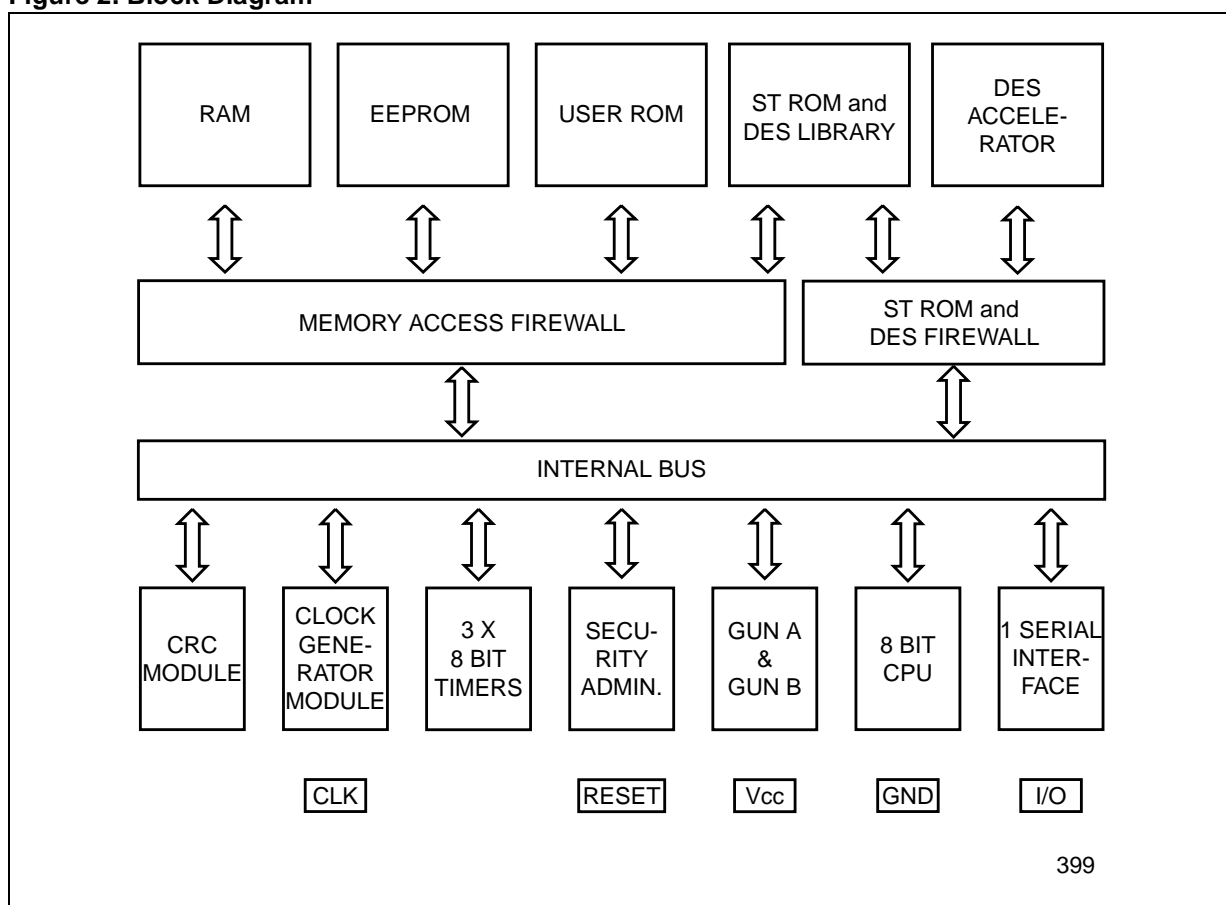
Access from any memory area to another are protected by hardware FIREWALLS. Access rules are User defined and can be selected by mask options or during the life of the product.

The chip includes a DES accelerator which is accessible via cryptographic software libraries located in ST ROM.

As with all the other ST19W products, a serial interface fully compatible with the ISO7816 standard for Smartcard applications is available.

A CRC calculation block is also available and is directly accessible by the User.

Figure 2. Block Diagram



SOFTWARE DEVELOPMENT

Software development and firmware generation (ROM and options) are supported by a comprehensive set of development tools, dedicated at development and validation of softwares:

- Smartcard ICs Emulator,
- ST19X simulation package,
- ScDevTools pack environment for all Windows™ based stations,
- Powerful C/C++ compiler and debugger are also available (third party tools).

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