



PRELIMINARY

SOLID STATE DEVICES, INC

14849 Firestone Boulevard · La Mirada, CA 90638
Phone: (714) 670-SSDI (7734) · Fax: (714) 522-7424

SSR2045-28

Designer's Data Sheet

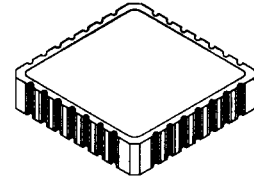
FEATURES:

- Low Reverse Leakage
- Low Forward Voltage Drop
- Hermetically Sealed Surface Mount Package
- Guard Ring for Overvoltage protection
- Eutectic Die Attach
- 175°C Operating Temperature

- TX, TXV and Space Level Screening Available

**20 AMP
45 VOLTS
SCHOTTKY
RECTIFIER**

28 PIN CLCC



MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse and DC Blocking Voltage	VRRM	45	Volts
	VRWM		
	VR		
Average Rectified Forward Current (Resistive Load, 60Hz, Sine Wave, TA=25°C)	IO	20	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave Superimposed on IO, allow junction to reach equilibrium between pulses, TA=25°C) <i>note 1</i>	IFSM	100	Amps
Operating and storage temperature	Top & Tstg	-65 to +175	°C
Maximum Thermal Resistance Junction to Case	RθJC	4.0	°C/W

Note 1: All pins tied together

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET#: RS0139 A

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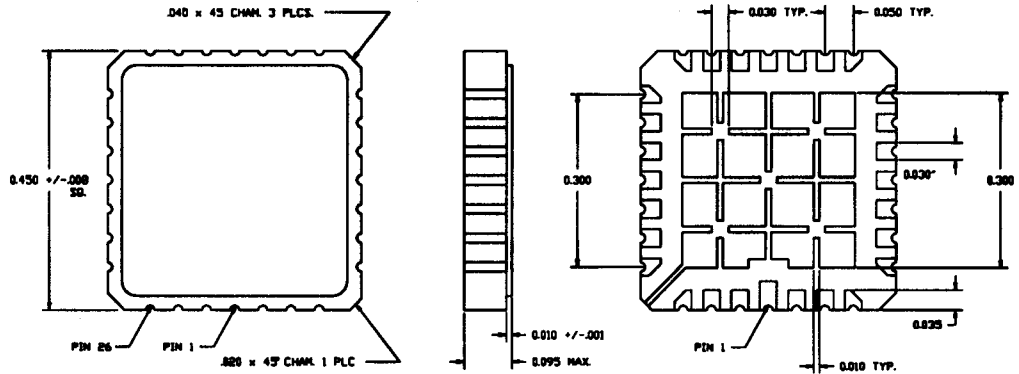
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ELECTRICAL CHARACTERISTICS

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop ($I_F = 10 \text{ Adc}$, $T_A = 25^\circ\text{C}$, 300 μs Pulse) ($I_F = 20 \text{ Adc}$, $T_A = 25^\circ\text{C}$, 300 μs Pulse)	VF1	0.65 0.89	Vdc
Instantaneous Forward Voltage Drop ($I_F = 10 \text{ Adc}$, $T_A = -55^\circ\text{C}$, 300 μs Pulse)	VF2	0.71	Vdc
Reverse Leakage Current (Rated V_R , $T_A = 25^\circ\text{C}$, 300 μs pulse minimum)	IR1	200	μA
Reverse Leakage Current (Rated V_R , $T_A = 100^\circ\text{C}$, 300 μs pulse minimum)	IR2	20	mA
Junction Capacitance ($V_R = 10 \text{ Vdc}$, $T_A = 25^\circ\text{C}$, $f = 1 \text{ MHz}$)	CJ	800	pf

CASE OUTLINE: 28 PIN CLCC PIN OUT:

PIN 5-11: CATHODE
 PIN 1, 15-28: ANODE
 PIN 2, 3, 13, 14: N/C



TYPICAL OPERATING CURVES ($T_A = 25^\circ\text{C}$ Unless otherwise specified)

