

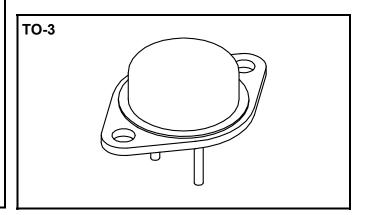
SSR2010CT/3

Designer's Data Sheet

FEATURES:

- PIV: 100 Volts
- Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Package
- Guard Ring for Overvoltage Protection
- Available in Isolated and Non-isolated versions
- Eutectic Die Attach
- 175°C Operating Temperature
- Also Available in the following Configurations: Common Anode: SSR2010CA/3 Doubler: SSR1010D/3
- TX, TXV, and Space Level Screening Available

20 AMPS 100 VOLTS POSITIVE CENTERTAP SCHOTTKY RECTIFIER



MAXIMUM RATINGS		Symbol	Value	Units
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SSR2010CT/3	V _{RRM} V _{RWM} V _R	100	Volts
Average Rectified Forward Current $\frac{1}{2}$ (Resistive Load, 60 Hz, Sine Wave, T _A =25°C)		Io	20	Amps
Peak Surge Current $\frac{1}{}$ (8.3 ms Pulse, Half Sine Wave Superimposed on I _O , allow junction to reach equilibrium between pulses, T _A =25 °C)		I _{FSM}	300	Amps
Operating and Storage Temperature		T _{OP} & Tstg	-65 to +175	°C
Maximum Thermal Resistance ^{1/} Junction to Case		R _{θJC}	1.2	°C/W

Notes: $\underline{1}$ / Both Legs Tied Together.

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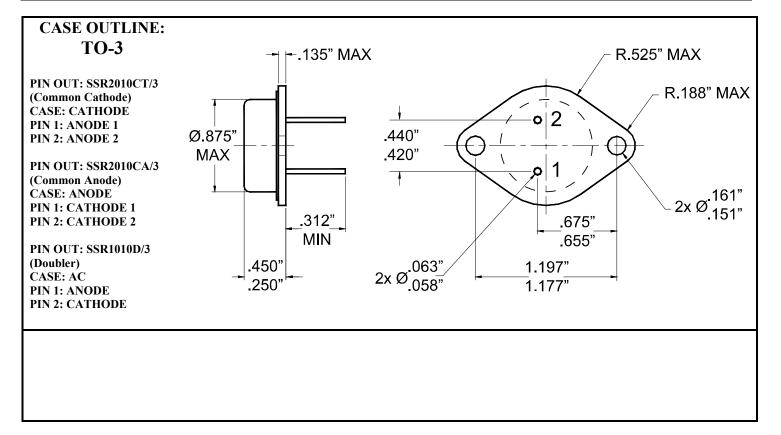


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ELECTRICAL CHARACTERISTICS (Per Leg)		Symbol	Max	Unit
Instantaneous Forward Voltage Drop (T _A = 25°C, Pulse)	$I_F = 3 \text{ Amps}$ $I_F = 5 \text{ Amps}$ $I_F = 10 \text{ Amps}$	V_{F1} V_{F2} V_{F3}	0.7 0.72 0.80	Volts
Instantaneous Forward Voltage Drop $(I_F = 5 \text{ Amps}, T_A = -55 \text{ °C}, \text{ Pulse})$		V _{F4}	0.85	Volts
Reverse Leakage Current (Rated V_R , $T_A = 25^{\circ}C$, Pulse)		I _{R1}	100	μA
Reverse Leakage Current (Rated V_R , $T_A = 100^{\circ}$ C, Pulse)		I _{R2}	5	mA
Junction Capacitance ($V_R = 10 V_{DC}, T_A = 25^{\circ}C, f = 1 MHz$)		CJ	400	pF



NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.	DATA SHEET #: RS0071C	DOC
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