

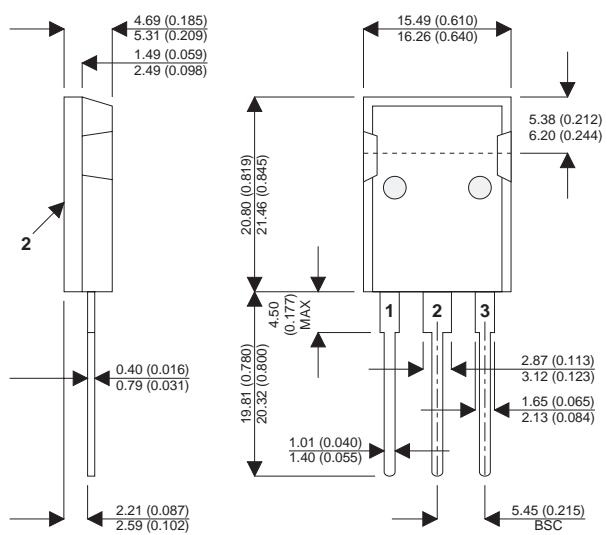


SEMELAB

SML80T27

T247clip Package Outline.

Dimensions in mm (inches)



Pin 1 – Gate

Pin 2 – Drain

Pin 3 – Source

N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

V_{DSS} **800V**

I_{D(cont)} **27A**

R_{DS(on)} **0.300Ω**

- Faster Switching
- Lower Leakage
- 100% Avalanche Tested
- New T247clip Package
(Clip-mounted TO-247 Package)

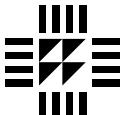
StarMOS is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimises the JFET effect, increases packing density and reduces the on-resistance. StarMOS also achieves faster switching speeds through optimised gate layout.

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

| | | | |
|-----------------------------------|--|------------|------|
| V _{DSS} | Drain – Source Voltage | 800 | V |
| I _D | Continuous Drain Current | 27 | A |
| I _{DM} | Pulsed Drain Current ¹ | 108 | A |
| V _{GS} | Gate – Source Voltage | ±30 | V |
| V _{GSM} | Gate – Source Voltage Transient | ±40 | |
| P _D | Total Power Dissipation @ T _{case} = 25°C | 520 | W |
| | Derate Linearly | 4.16 | W/°C |
| T _J , T _{STG} | Operating and Storage Junction Temperature Range | -55 to 150 | °C |
| | | 300 | |
| I _{AR} | Avalanche Current ¹ (Repetitive and Non-Repetitive) | 27 | A |
| E _{AR} | Repetitive Avalanche Energy ¹ | 50 | |
| E _{AS} | Single Pulse Avalanche Energy ² | 2500 | mJ |

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Starting T_J = 25°C, L = 6.86mH, R_G = 25Ω, Peak I_L = 27A



**SEME
LAB**

SML80T27

STATIC ELECTRICAL RATINGS ($T_{case} = 25^\circ C$ unless otherwise stated)

| | Characteristic | Test Conditions | Min. | Typ. | Max. | Unit |
|--------------|--|--|------|------|-----------|----------|
| BV_{DSS} | Drain – Source Breakdown Voltage | $V_{GS} = 0V, I_D = 250\mu A$ | 800 | | | V |
| I_{DSS} | Zero Gate Voltage Drain Current ($V_{GS} = 0V$) | $V_{DS} = V_{DSS}$ | | | 25 | μA |
| | | $V_{DS} = 0.8V_{DSS}, T_C = 125^\circ C$ | | | 250 | |
| I_{GSS} | Gate – Source Leakage Current | $V_{GS} = \pm 30V, V_{DS} = 0V$ | | | ± 100 | nA |
| $V_{GS(TH)}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 2.5mA$ | 2 | | 4 | V |
| $I_{D(ON)}$ | On State Drain Current ² | $V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max $V_{GS} = 10V$ | 27 | | | A |
| $R_{DS(ON)}$ | Drain – Source On State Resistance ² | $V_{GS} = 10V, I_D = 0.5 I_D$ [Cont.] | | | 0.300 | Ω |

DYNAMIC CHARACTERISTICS

| | Characteristic | Test Conditions | Min. | Typ. | Max. | Unit |
|--------------|--------------------------------|-------------------|------|------|------|------|
| C_{iss} | Input Capacitance | $V_{GS} = 0V$ | | 6600 | | pF |
| C_{oss} | Output Capacitance | | | 645 | | |
| C_{rss} | Reverse Transfer Capacitance | | | 320 | | |
| Q_g | Total Gate Charge ³ | $V_{GS} = 10V$ | | 340 | | nC |
| Q_{gs} | Gate – Source Charge | | | 31 | | |
| Q_{gd} | Gate – Drain (“Miller”) Charge | | | 170 | | |
| $t_{d(on)}$ | Turn-on Delay Time | $V_{GS} = 15V$ | | 16 | | ns |
| t_r | Rise Time | | | 14 | | |
| $t_{d(off)}$ | Turn-off Delay Time | | | 59 | | |
| t_f | Fall Time | $R_G = 0.6\Omega$ | | 8 | | |

SOURCE – DRAIN DIODE RATINGS AND CHARACTERISTICS

| | Characteristic | Test Conditions | Min. | Typ. | Max. | Unit |
|----------|------------------------------------|--|------|------|------|---------|
| I_S | Continuous Source Current | (Body Diode) | | | 27 | A |
| I_{SM} | Pulsed Source Current ¹ | | | | 108 | |
| V_{SD} | Diode Forward Voltage ² | $V_{GS} = 0V, I_S = -I_D$ [Cont.] | | | 1.3 | V |
| t_{rr} | Reverse Recovery Time | $I_S = -I_D$ [Cont.], $dI_S / dt = 100A/\mu s$ | | 850 | | ns |
| Q_{rr} | Reverse Recovery Charge | $I_S = -I_D$ [Cont.], $dI_S / dt = 100A/\mu s$ | | 22 | | μC |

THERMAL CHARACTERISTICS

| | Characteristic | Min. | Typ. | Max. | Unit |
|-----------------|---------------------|------|------|------|--------------|
| $R_{\theta JC}$ | Junction to Case | 0.24 | | | $^\circ C/W$ |
| $R_{\theta JA}$ | Junction to Ambient | | | 40 | |

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 μs , Duty Cycle < 2%

3) See MIL-STD-750 Method 3471



CAUTION — Electrostatic Sensitive Devices. Anti-Static Procedures Must Be Followed.