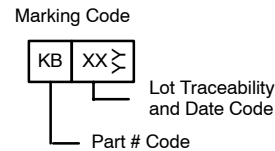
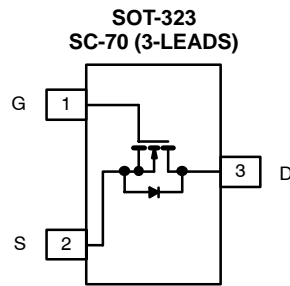




## N-Channel 25-V (D-S) MOSFET

**TrenchFET<sup>®</sup>**  
Power MOSFETs

PRODUCT SUMMARY			
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ)
25	0.350 @ $V_{GS} = 4.5$ V	0.75	1.3
	0.450 @ $V_{GS} = 2.5$ V	0.66	



Ordering Information: Si1304DL-T1

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	5 secs	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	25		V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	0.75	0.70	A
		$T_A = 70^\circ\text{C}$	0.60	0.56	
Pulsed Drain Current	$I_{DM}$	3.0			
Continuous Diode Current (Diode Conduction) <sup>a</sup>	$I_S$	0.28	0.24		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	0.33	0.29	W
		$T_A = 70^\circ\text{C}$	0.21	0.19	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 5$ sec	315	375	$^\circ\text{C/W}$
		Steady State	380	450	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	285	340		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

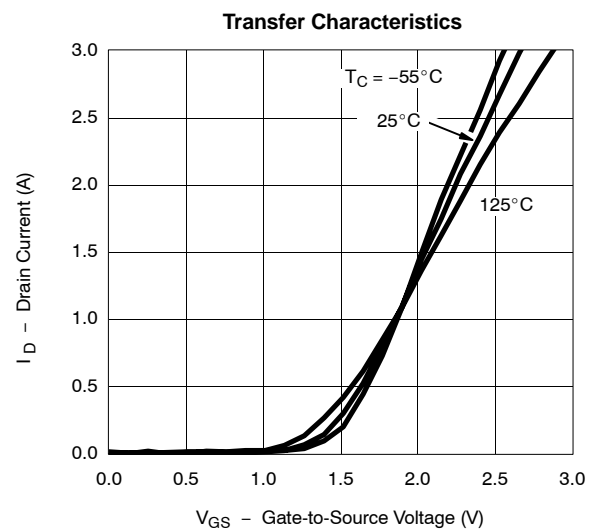
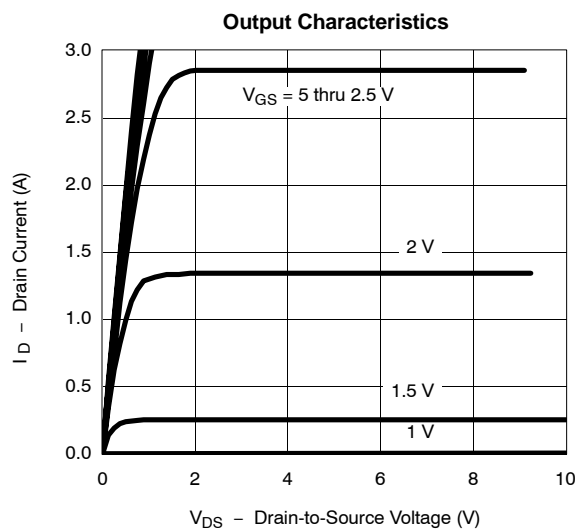
**SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	0.6		1.3	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 4.5 V	3.0			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.75 A		0.280	0.350	Ω
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 0.50 A		0.355	0.450	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.75 A		1.5		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 0.24 A, V <sub>GS</sub> = 0 V		0.8	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.75 A		1.3	2.0	nC
Gate-Source Charge	Q <sub>gs</sub>			0.31		
Gate-Drain Charge	Q <sub>gd</sub>			0.5		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, R <sub>L</sub> = 20 Ω I <sub>D</sub> ≅ 0.75 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 6 Ω		11	20	ns
Rise Time	t <sub>r</sub>			18	30	
Turn-Off Delay Time	t <sub>d(off)</sub>			17	30	
Fall Time	t <sub>f</sub>			11	20	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 0.24 A, di/dt = 100 A/μs		30	60	

## Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
b. Guaranteed by design, not subject to production testing.

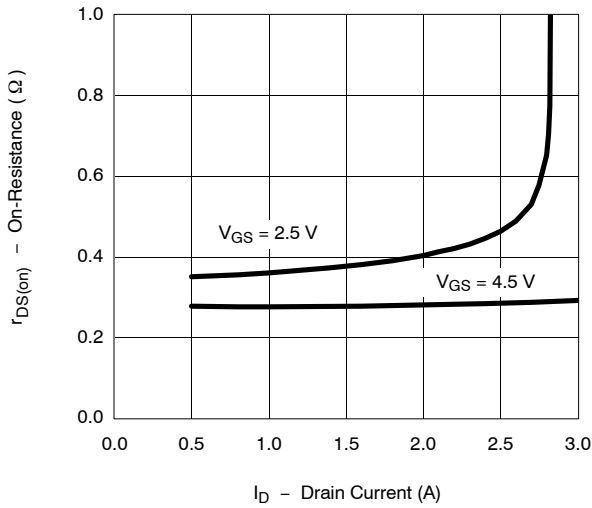
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

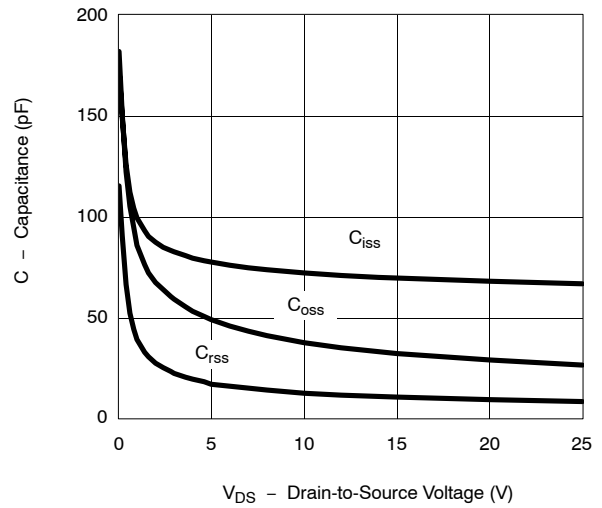


**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

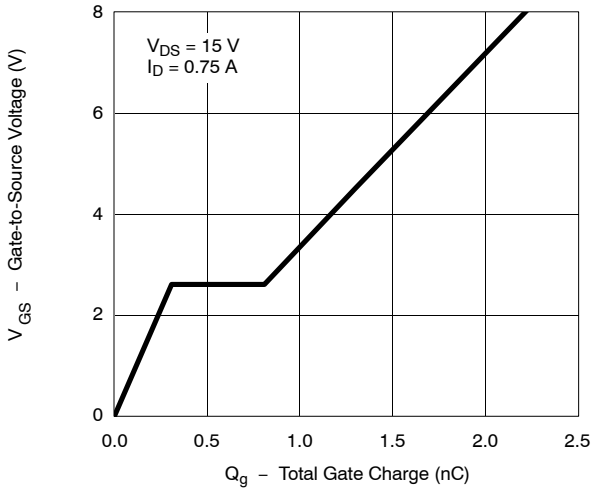
**On-Resistance vs. Drain Current**



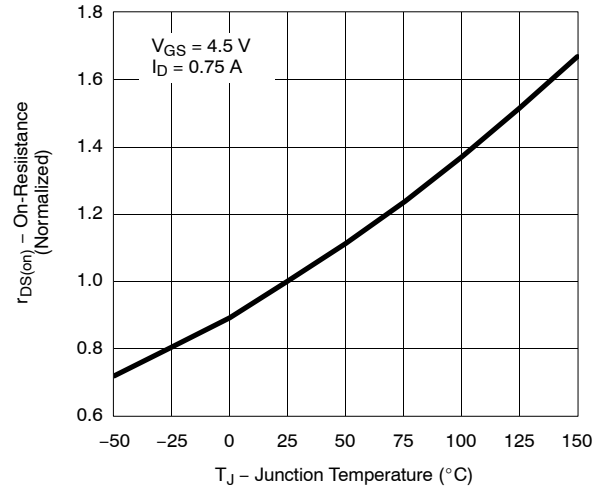
**Capacitance**



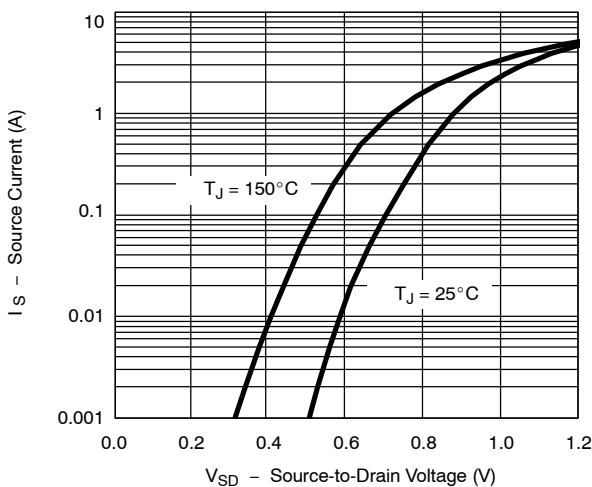
**Gate Charge**



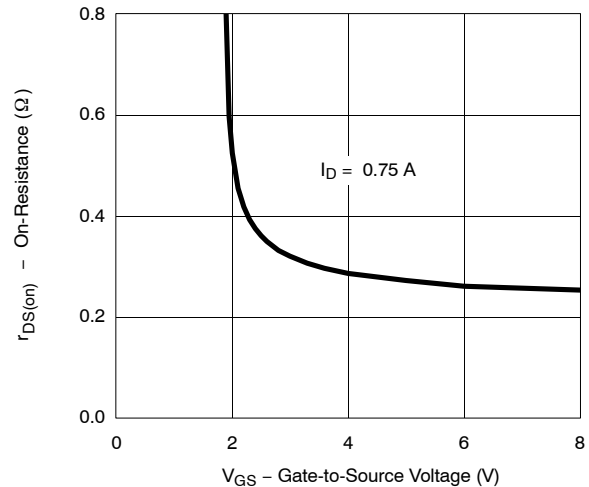
**On-Resistance vs. Junction Temperature**



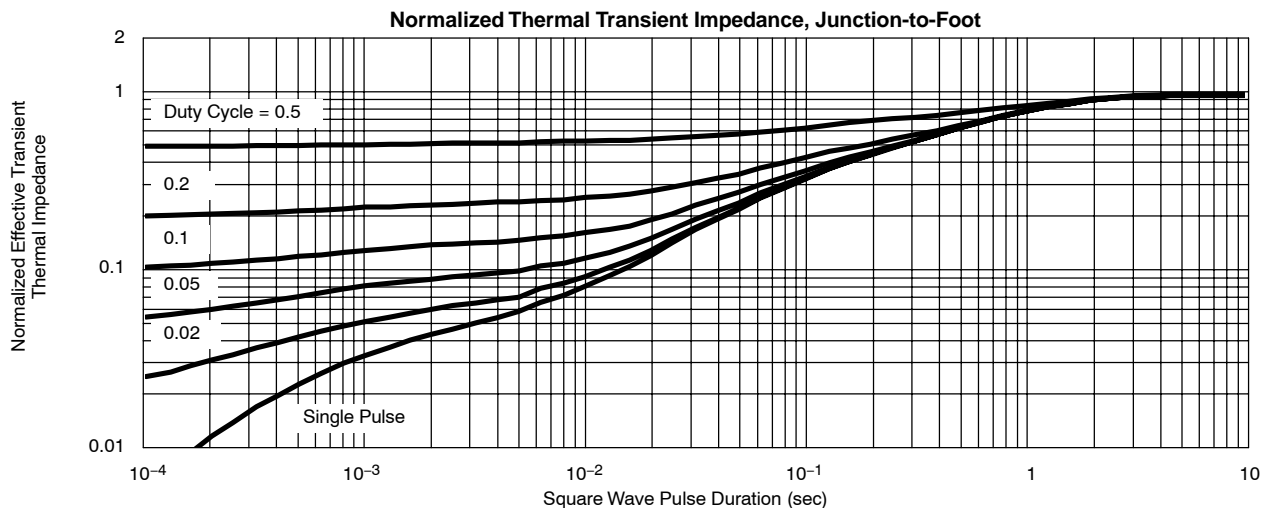
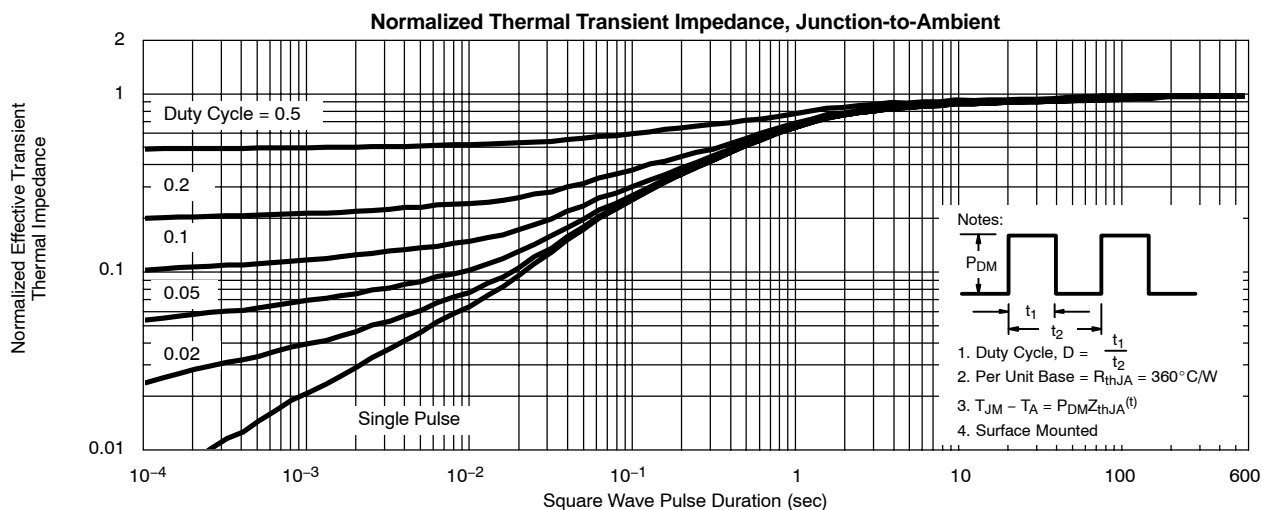
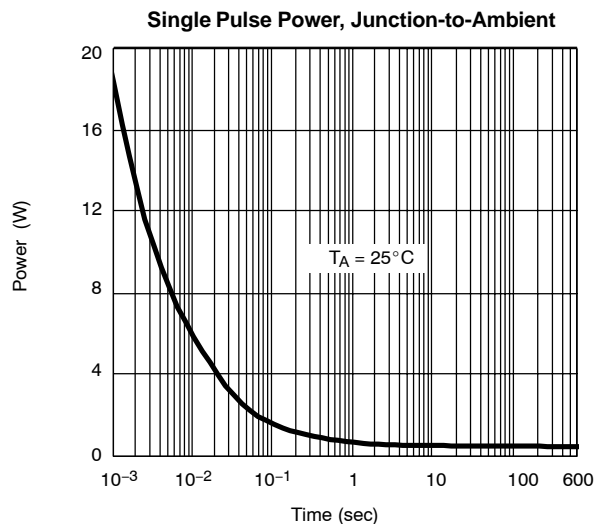
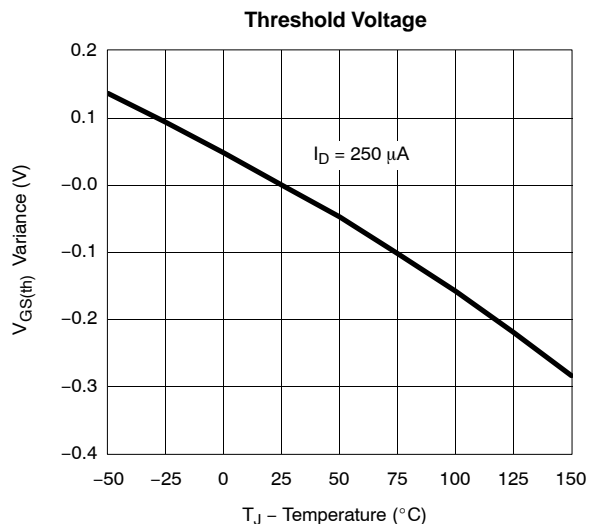
**Source-Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



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