SENSITRON SEMICONDUCTOR

TECHNICAL DATA DATA SHEET 4998, REV. -

RAD TOLERANT LOW R_{DS} HERMETIC POWER MOSFET - P-CHANNEL

FEATURES:

- 100 Volt, 0.023 Ohm, 90A MOSFET (current limited to 50A by package)
- Characterized for V_{GS} of 4.5V for Logic Level Drive
- Total Dose Characterized to 300 Krad
- Single Event Effect Capability Characterized to 60 MeVcm²/mg LET
- Isolated Hermetic Metal Package; Ultra Low R_{DS (on)}
- Ceramic Seals with Glidcop leads
- Also available with glass seals and copper core alloy 52 leads

MAXIMUM RATINGS ALL RATINGS A	ALL RATINGS ARE AT T _C = 25°C UNLESS OTHERWISE SPECIFIED.						
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS		
GATE TO SOURCE VOLTAGE	V _{GS}	-	-	±20	Volts		
ON-STATE DRAIN CURRENT	I _{D25}	-	-	- 50	Amps		
PULSED DRAIN CURRENT	I _{DM}	-	-	- 90	Amps		
OPERATING AND STORAGE TEMPERATURE	T _J /T _{STG}	-55	-	+150	°C		
TOTAL DEVICE DISSIPATION	PD	-	-	225	Watts		
THERMAL RESISTANCE, JUNCTION TO CASE	$R_{ ext{ heta}JC}$	-	-	0.55	°C/W		

ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	-100	-	-	Volts
V _{GS} = 0V, I _D = - 250μA					
STATIC DRAIN TO SOURCE ON STATE RESISTANCE	R _{DS(ON)}				Ω
$V_{GS} = -10V, I_{D} = -20A$		-	0.019	0.023	
V _{GS} = - 4.5V, I _D = - 15A		-	0.021	0.025	
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = -250 \mu A$	V _{GS(th)}	- 1	-	- 3	Volts
FORWARD TRANSCONDUCTANCE	9 _{fs}	-	80	-	S(1/Ω)
V _{DS} = - 15V, I _D = - 20A					
ZERO GATE VOLTAGE DRAIN CURRENT					
V_{DS} = 0.8 x Max. rating, V_{GS} = 0V, T_{J} = 25°C	I _{DSS}	-	-	- 1	μA
T _J = 125°C				- 500	
GATE TO SOURCE LEAKAGE FORWARD V _{GS} = 20V	I _{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE V _{GS} = -20V				-100	
TURN ON DELAY TIME $V_{DD} = -50V$	t _{d(ON)}	-	20	30	
RISE TIME $I_D = -50A$	t _r		510	855	nsec
TURN OFF DELAY TIME V _{GS} = - 10V					
	t _{d(OFF)}		145	220	
FALL TIME $R_G = 1\Omega$	t _f		870	1300	
DIODE FORWARD VOLTAGE $I_F = -20A, V_{GS} = 0V$	V_{SD}	-	- 1.0	- 1.5	Volts
Pulse test, t \leq 300 μ s, duty cycle d \leq 2 %					
REVERSE RECOVERY TIME $T_J = 25^{\circ}C$					
I _F = - 20A, V _R = - 50V	t _{rr}	-	80	120	nsec
di/dt = - 100A/µsec					
INPUT CAPACITANCE $V_{GS} = 0 V$,	C _{iss}	-	11100	-	_
OUTPUT CAPACITANCE $V_{DS} = -50 V$,	C _{oss}		700		pF
REVERSE TRANSFER CAPACITANCE f = 1.0MHz	C _{rss}		1700		

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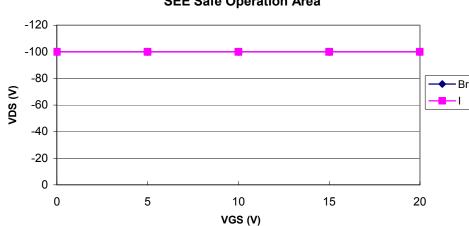
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Post-Total Dose (up to TID ratings) Irradiation Data

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	-100	-	-	Volts
$V_{GS} = 0V, I_D = -250 \mu A$					
STATIC DRAIN TO SOURCE ON STATE RESISTANCE	R _{DS(ON)}				Ω
$V_{GS} = -10V, I_D = -20A$		-	0.019	0.023	
V _{GS} = - 4.5V, I _D = - 15A			0.021	0.025	
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = -250 \mu A$	V _{GS(th)}	-1	-	-3	Volts
ZERO GATE VOLTAGE DRAIN CURRENT					
V_{DS} = 0.8 x Max. rating, V_{GS} = 0V, T_{J} = 25°C	I _{DSS}	-	-	-1	μA
GATE TO SOURCE LEAKAGE FORWARD V _{GS} = 20V	I _{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE V _{GS} = -20V				-100	
DIODE FORWARD VOLTAGE $I_F = -20A, V_{GS} = 0V$	V _{SD}	-	- 1.0	-1.5	Volts
Pulse test, t \leq 300 μ s, duty cycle d \leq 2 %					

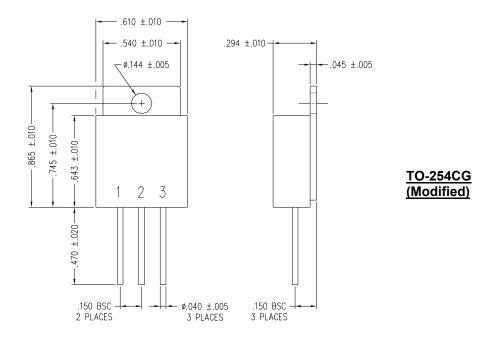
Single Event Effect Safe Operating Area

lon	LET	Energy	Range	V _{DS} (V)				
lon	(MeVcm ² /mg)	(MeV)	(µm)	V _{GS} =0V	V _{GS} = 5V	V _{GS} = 10V	V _{GS} = 15V	V _{GS} = 20V
Br	37.47	278	36.1	-100	-100	-100	-100	-100
I	59.72	320	31.1	-100	-100	-100	-100	-100



SEE Safe Operation Area

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MECHANICAL DIMENSIONS: in Inches / mm

PINOUT TABLE

DEVICE TYPE	PIN-1	PIN-2	PIN-3
P-CHANNEL MOSFET MODIFIED TO-254 PACKAGE	DRAIN	SOURCE	GATE

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