DC-DC Converter (-20V, -3.0A) RTL030P02

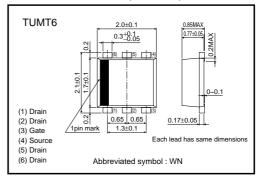
Features

- 1) Low on-resistance. ($80m\Omega$ at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

DC-DC converter

•External dimensions (Unit : mm)



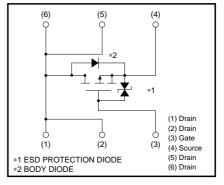
Structure

Silicon P-channel MOS FET

Packaging specifications

Туре	Package	Taping	
	Code	TR	
	Basic ordering unit (pieces)	3000	
RTL030P02		0	

Equivalent circuit



Transistors

•Absolute maximum ratings (Ta=25°C)

Parameter Drain-source voltage		Symbol	Limits	Unit			
		Vdss	-20	V			
Gate-source voltage		Vgss	±12	V			
Drain current	Continuous	ID	±3	А			
	Pulsed	IDP	±12	A *1			
Source current (Body diode)	Continuous	ls	-0.8	A *1			
	Pulsed	Isp	-12	Α			
Total power dissipation		PD	1	W *2			
Channel temperature		Tch	150	°C			
Range of Storage temperature		Tstg	-55 to +150	°C			

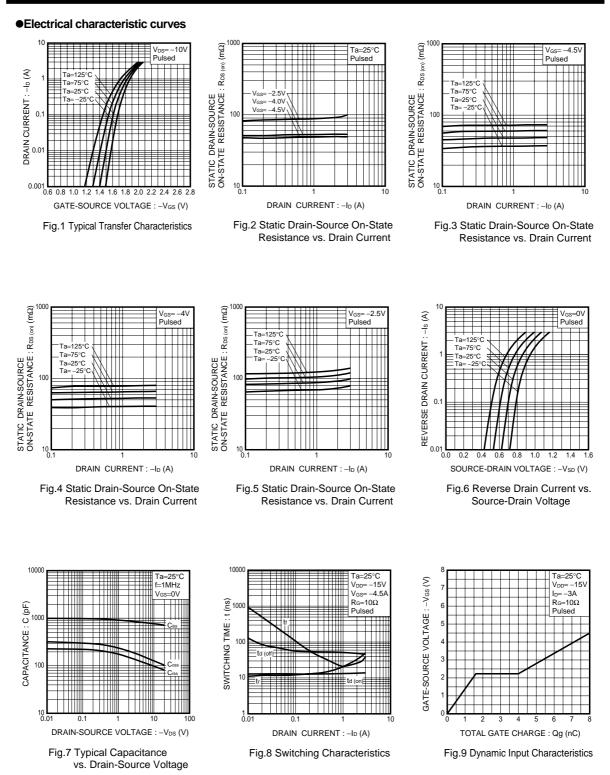
*1 Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Gate-source leakage	Igss	-	-	±10	μΑ	V _{GS} =±12V, V _{DS} =0V		
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	-20	-	-	V	I _D = -1mA, V _{GS} =0V		
Zero gate voltage drain current	IDSS	-	-	-1	μA	VDS=-20V, VGS=0V		
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	V_{DS} = -10V, ID= -1mA		
Static drain-source on-state resistance	RDS (on)	-	50	70	mΩ	I _D = -3.0A, V _{GS} = -4.5V *		
		-	55	77	mΩ	I _D = -3.0A, V _{GS} = -4V *		
		-	90	125	mΩ	I _D = -1.5A, V _{GS} = -2.5V *		
Forward transfer admittance	Y _{fs}	2.0	-	_	S	V _{DS} = -10V, I _D = -1.5A *		
Input capacitance	Ciss	_	760	_	pF	V _{DS} = -10V		
Output capacitance	Coss	_	125	_	pF	V _{GS} =0V		
Reverse transfer capacitance	Crss	_	100	_	pF	f=1MHz		
Turn-on delay time	td (on)	-	12	-	ns	ID= -1.5A *		
Rise time	tr	-	25	_	ns	VDD≒ –15V ∗ VGs= –4.5V		
Turn-off delay time	t _{d (off)}	-	50	_	ns	$R_{L}=10\Omega$		
Fall time	tr	-	22	_	ns	Rgs=10Ω *		
Total gate charge	Qg	_	8.0	_	nC	V _{DD} ≒−15V RL≒5Ω		
Gate-source charge	Qgs	-	1.5	_	nC	V_{GS} = -4.5V R _{GS} =10 Ω		
Gate-drain charge	Q _{gd}	_	2.5	_	nC	I _D =-3A		
*Pulsed								
Body diode characteristics (source-drain characteristics)								
Forward voltage	VSD	-	-	-1.2	V	Is= -0.8A, V _{GS} =0V		

RTL030P02

Transistors



Transistors

Measurement circuits

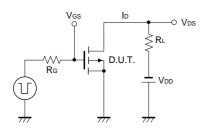


Fig.10 Switching Time Measurement Circuit

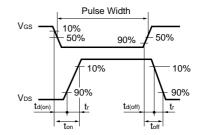


Fig.11 Switching Waveforms

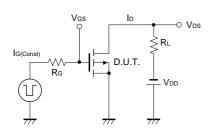


Fig.12 Gate Charge Measurement Circuit

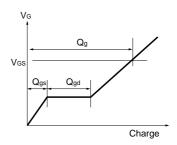


Fig.13 Gate Charge Waveforms

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