

## Transistors

## 4V Drive Nch MOS FET

## RHP030N03

## ●Structure

Silicon N-channel MOS FET

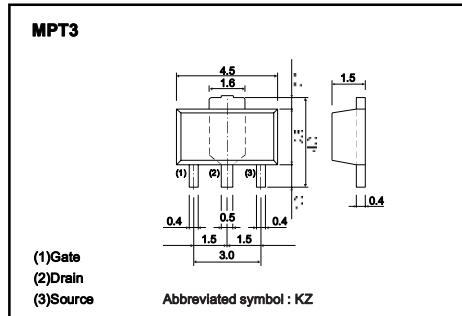
## ●Features

- 1) Low On-resistance.
- 2) 4V drive.

## ●Applications

Switching

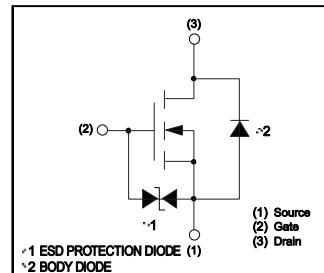
## ●External dimensions (Unit : mm)



## ●Packaging specifications

Type	Package	Taping
	Code	T100
	Basic ordering unit (pieces)	1000
RHP030N03		○

## ●Inner circuit

●Absolute maximum ratings ( $T_a=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-source voltage	$V_{DSS}$	30	V
Gate-source voltage	$V_{GSS}$	$\pm 20$	V
Drain current	Continuous $I_D$	3	A
	Pulsed $I_{DP}$	10	A
Reverse drain current	Continuous $I_{DR}$	3	A
	Pulsed $I_{DRP}$	10	A
Total power dissipation	$P_D$	500	mW
		2	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Range of storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*1  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ 

\*2 When mounted on a 40x40x0.7mm ceramic board

## ●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th(ch-a)}$	250 62.5 *	$^\circ\text{C/W}$

\* When mounted on a 40x40x0.7mm ceramic board

## Transistors

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GS</sub>	—	—	±10	µA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	30	—	—	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DS</sub>	—	—	1	µA	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	—	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub>	—	90	120	mΩ	I <sub>D</sub> = 3A, V <sub>GS</sub> = 10V
		—	160	210	mΩ	I <sub>D</sub> = 3A, V <sub>GS</sub> = 4V
Forward transfer admittance	Y <sub>fs</sub>	2.0	—	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 3A
Input capacitance	C <sub>iss</sub>	—	160	—	pF	V <sub>DS</sub> = 10V
Output capacitance	C <sub>oss</sub>	—	90	—	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	—	27	—	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub>	—	7	—	ns	V <sub>DD</sub> = 15V I <sub>D</sub> = 1.5A
Rise time	t <sub>r</sub>	—	11	—	ns	V <sub>GS</sub> = 10V
Turn-off delay time	t <sub>d (off)</sub>	—	15	—	ns	R <sub>L</sub> =10Ω R <sub>G</sub> =10Ω
Fall time	t <sub>f</sub>	—	4.5	—	ns	
Total gate charge	Q <sub>g</sub>	—	6.5	—	nC	V <sub>DD</sub> = 15V
Gate-source charge	Q <sub>gs</sub>	—	1.0	—	nC	V <sub>GS</sub> = 10V
Gate-drain charge	Q <sub>gd</sub>	—	1.5	—	nC	I <sub>D</sub> = 3A

Pulsed

## Appendix

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