

RD74LVC16244B

16-bit Buffers / Line Drivers with 3-state Outputs

REJ03D0527-0100 Rev.1.00 Mar. 14, 2005

Description

The RD74LVC16244B has sixteen line drivers with three state outputs in a 48 pin package. This device is a non inverting buffer and has two active low enables ($1\overline{G}$ to $4\overline{G}$). Each enable independently controls four buffers. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- All outputs V_{OUT} (Max.) = 5.5 V (@ V_{CC} = 0 V or output off state)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ $V_{CC} = 3.3$ V, Ta = 25°C)
- High output current $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$
 - $\pm 8 \text{ mA } (@V_{CC} = 2.3 \text{ V})$
 - $\pm 12 \text{ mA } (@V_{CC} = 2.7 \text{ V})$
 - ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC16244BTEL	TSSOP-48 pin	PTSP0048KA-A (TTP-48DBV)	Т	EL (1,000 pcs/reel)

Function Table

Inp	outs	
G	Α	Output Y
Н	X	Z
L	Н	Н
L	L	L

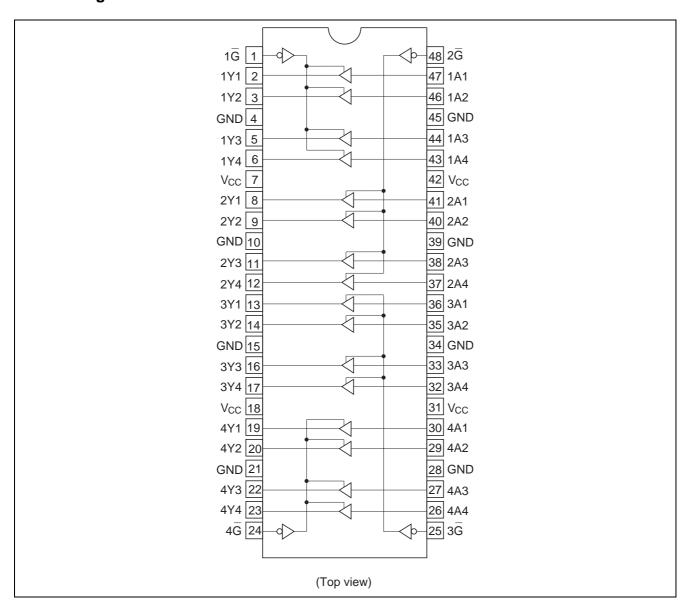
H: High level

L: Low level

X: Immaterial

Z: High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	-0.5 to 7.0	V	
Input diode current	I _{IK}	-50	mA	$V_1 = -0.5 \text{ V}$
Input voltage	Vı	–0.5 to 7.0	V	
Output diode current	I _{OK}	-50	mA	$V_{O} = -0.5 \text{ V}$
		50		$V_{O} = V_{CC} + 0.5 \text{ V}$
Output voltage	Vo	-0.5 to V_{CC} +0.5	V	Output "H" or "L"
		-0.5 to 7.0		Output "Z" or V _{CC} :OFF
Output current	Io	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Item Symbol Ratings		Unit	Conditions
Supply voltage	V _{CC}	1.5 to 5.5	V	Data hold
		1.65 to 5.5		At operation
Input / Output voltage	Vı	0 to 5.5	V	G, A
	Vo	0 to V _{CC}		Output "H" or "L"
		0 to 5.5		Output "Z" or V _{CC} : OFF
Operating temperature	Та	-40 to 85	°C	
Output current	I _{OH}	-4	mA	V _{CC} = 1.65 V
		-8		V _{CC} = 2.3 V
		-12		V _{CC} = 2.7 V
		-24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
	I _{OL}	4	mA	V _{CC} = 1.65 V
		8		V _{CC} = 2.3 V
		12		V _{CC} = 2.7 V
		24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
Input rise / fall time*1	t _r , t _f	20	ns/V	V _{CC} = 1.65 V to 2.7 V
		10		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

			Ta = -40) to 85°C		
Item	Symbol	V _{CC} (V)	Min Max		Unit	Test Conditions
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.65	_	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V _{CC} ×0.7	_		
	V _{IL}	1.65 to 1.95	_	V _{CC} ×0.35	V]
		2.3 to 2.7	_	0.7		
		2.7 to 3.6	_	0.8	1	
		4.5 to 5.5	_	V _{CC} ×0.3		
Output voltage	V _{OH}	1.65 to 5.5	V _{CC} -0.2	_	V	$I_{OH} = -100 \mu A$
		1.65	1.2	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.7	_		$I_{OH} = -8 \text{ mA}$
		2.7	2.2	_		I _{OH} = -12 mA
		3.0	2.4	_		
		3.0	2.2	_		I _{OH} = -24 mA
		4.5	3.8	_		
	V _{OL}	1.65 to 5.5	_	0.2	V	$I_{OL} = 100 \mu A$
		1.65	_	0.45		I _{OL} = 4 mA
		2.3	_	0.7		$I_{OL} = 8 \text{ mA}$
		2.7	_	0.4		I _{OL} = 12 mA
		3.0	_	0.55		I _{OL} = 24 mA
		4.5	_	0.55		
Input current	I _{IN}	0 to 5.5	_	±5.0	μΑ	V _{IN} = 5.5 V or GND
Output leak current	I _{OFF}	0	_	±5.0	μΑ	$V_{IN}/V_{OUT} = 5.5 \text{ V}$
Off state output current	loz	2.7 to 5.5	_	±5.0	μA	$V_{IN} = V_{CC}$ or GND $V_{O} = 5.5$ V or GND
Quiescent supply	I _{CC}	2.7 to 3.6	_	±10	μA	V _{IN} = 3.6 V to 5.5 V
current		2.7 to 5.5	_	10	1	$V_{IN} = V_{CC}$ or GND
	Δl _{CC}	2.7 to 3.6	_	500	μА	V_{IN} = one input at $(V_{CC}$ -0.6)V, other inputs at V_{CC} or GND

Switching Characteristics

		Ta :	= -40 to 8	5°C		From	То	
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	1.8±0.15	1.0	_	10.9	ns	А	Υ
	t_{PHL}	2.5±0.2	1.0	_	7.9			
		2.7	1.0	_	5.8			
		3.3±0.3	1.5	_	5.2			
		5.0±0.5	1.0	_	4.0			
Output enable time	t _{ZH}	1.8±0.15	1.0	_	12.6	ns	G	Υ
	t_{ZL}	2.5±0.2	1.0	_	9.6			
		2.7	1.0	_	8.2			
		3.3±0.3	1.5	_	7.5			
		5.0±0.5	1.0	_	5.5			
Output disable time	t _{HZ}	1.8±0.15	1.0	_	12.1	ns	G	Υ
	t_LZ	2.5±0.2	1.0	_	7.8			
		2.7	1.0	_	7.7			
		3.3±0.3	1.5	_	7.0			
		5.0±0.5	1.0	_	6.0			
Between output pins skew*1	t _{OSLH}	1.8±0.15	_	_	_	ns		
	toshl	2.5±0.2	_	_	_			
		2.7	_	_	_			
		3.3±0.3	_	_	1.0			
		5.0±0.5	_	_	1.0			
Input capacitance	C _{IN}	3.3	_	4.0	_	pF		
Output capacitance	Co	3.3		8.0		pF		

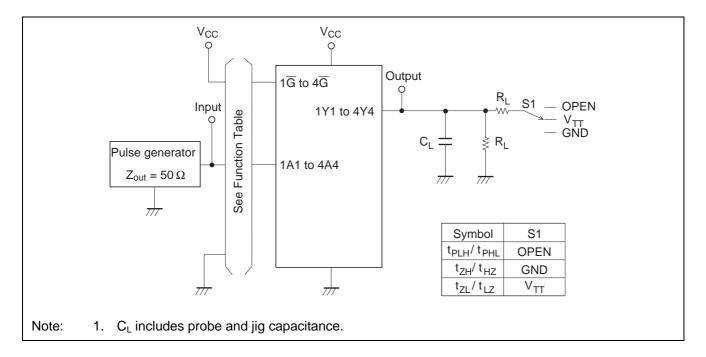
Note: 1. This parameter is characterized but not tested.

 $t_{\text{OSLH}} = \mid t_{\text{PLHm}} - t_{\text{PLHn}} \rvert, \, t_{\text{OSHL}} = \mid t_{\text{PHLm}} - t_{\text{PHLn}} \rvert$

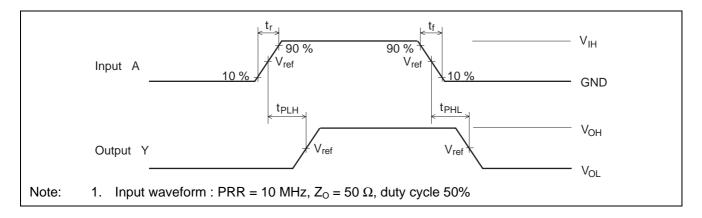
Operating Characteristics

			Ta = 25°C				
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	C _{PD}	1.8	_	25.0	_	pF	f = 10 MHz
Capacitance		2.5	_	25.0	_		
		3.3	_	27.0	_		
		5.0	_	32.0	_		

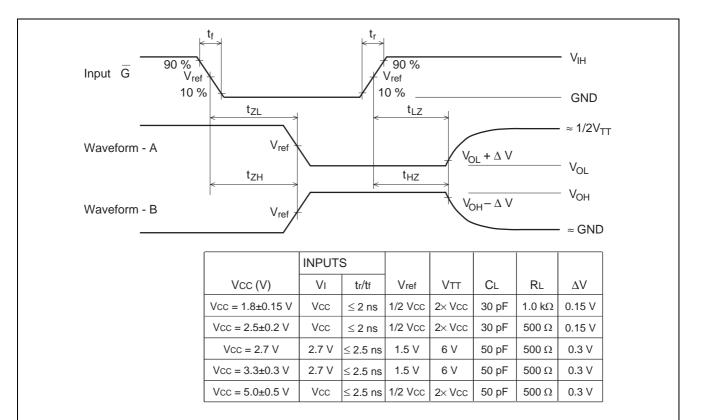
Test Circuit



Waveforms - 1



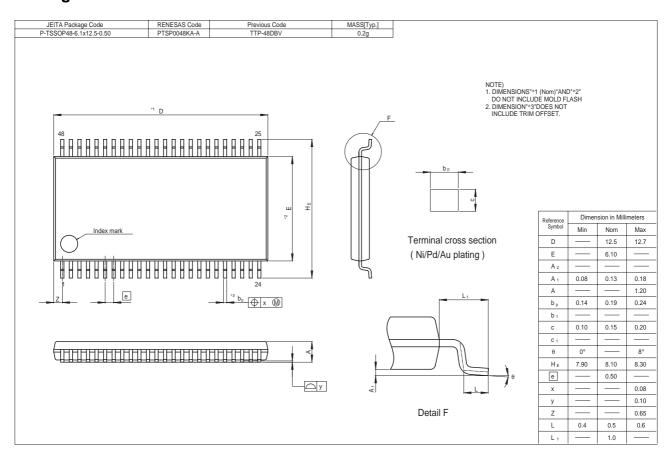
Waveforms - 2



Notes:

- 1. Input waveform : PRR = 10 MHz, Z_0 = 50 Ω , duty cycle 50%
- 2. Waveform A shows input conditions such that the output is "L" level when enable by the output control.
- 3. Waveform B shows input conditions such that the output is "H" level when enable by the output control.

Package Dimensions



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