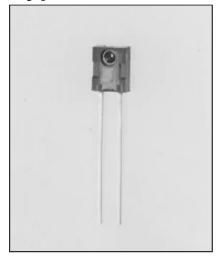
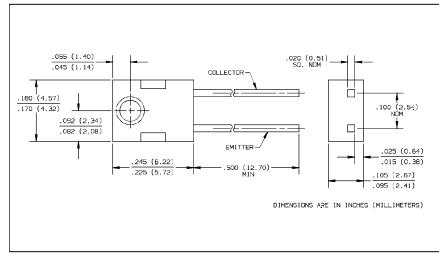


# NPN Phototransistor with Base-Emitter Resistor Types OP755A, OP755B, OP755C, OP755D





#### **Features**

- · Wide receiving angle
- · Variety of sensitivity ranges
- Side-looking package for space limited applications
- Base-emitter resistor provides ambient light protection

#### Description

The OP755 device consists of a NPN silicon phototransistor molded in blue tinted epoxy packages. The wide receiving angle provides relatively even reception over a large area. The sidelooking package is designed for easy PC board mounting of slotted optical switches or optical interrupt detectors. The series is mechanically and spectrally matched to the OP140 and OP240 series of infrared emitting diodes.

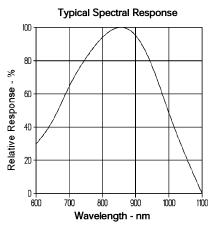
The phototransistor has an internal baseemitter resistor which provides protection from low level ambient lighting conditions. This feature is also useful when the media being detected is semitransparent to infrared light in interruptive applications.

### **Absolute Maximum Ratings** (T<sub>A</sub> = 25<sup>o</sup> C unless otherwise noted)

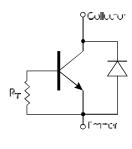
Collector-Emitter Voltage	30 V
Emitter Reverse Current	10 mA
Collector DC Current	30 mA
Storage and Operating Temperature Range	40• C to +100• C
Lead Soldering Temperature [1/16 inch (1.6 mm) from case for 5	
iron]	
Power Dissipation	200 mW <sup>(2)</sup>
Notes:	

- (1) RMA flux is recommended. Duration can be extended to 10 sec. max. When flow soldering. Max. 20 grams force may be applied to leads when soldering.
- (2) Derate linearly 2.0 mW/• C above 25• C.
- (3) Light source is an unfiltered GaAs LED with a peak emission wavelength of 935 nm and a radiometric intensity level which varies less than 10% over the entire lens surface of the phototransistor being tested.
- (4) The knee point irradiance is defined as the irradiance required to increase I<sub>C(ON)</sub> to 50 A.

#### **Typical Performance Curves**



#### **Schematic**



## Types OP755A, OP755B, OP755C, OP755D

**Electrical Characteristics** (T<sub>A</sub> = 25° C unless otherwise noted)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
	On-State Collector Current					$V_{CE} = 5 \text{ V}, E_e = 1.0 \text{ mW/cm}^{2(3)}$
I <sub>C(ON)</sub>	OP755A	1.80		5.50		
	OP755B	1.20		3.40	mA	
	OP755C	0.70		2.25		
	OP755D	0.70		5.50		
E <sub>KP</sub>	Knee Point Irradiance		.2		mW/cm <sup>2</sup>	$V_{CE} = 5 V^{(4)}$
I <sub>CEO</sub>	Collector-Emitter Dark Current			100	nA	$V_{CE} = 10 \text{ V}, E_e = 0$
I <sub>ECO</sub>	Emitter-Reverse Current			100	• A	V <sub>CE</sub> = 0.4 V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown	30			V	I <sub>C</sub> = 100 • A
VCE(SAT)	Collector-Emitter Saturation Voltage			0.4	V	$I_C = 100 \cdot A, E_e = 1 \text{ mW/cm}^{2(3)}$

#### **Typical Performance Curves**

