



Features

- Single power supply can operate at 2.4 V through 6.0 V.
- Built-in oscillating Resistor bonding optionally. 4 frequencies are provided optionally chipwise: 5, 6, 7, 8 KHz. External oscillation resistor could be used.
- Current output pair can drive buzzer or speaker (8 ohm or 16 ohm) without a transistor.
- 6 bit MPCM resolution per pixel.
- The voice content can be separated to 16 sections.
- Duration of each section can be different and is multiples of 100 h.
- Duration of each section with appended memory-less mute is up to 10.9 seconds (10000h).
- 3 straight trigger pins are provided TGA, TGB, TGC.
- Each access a sentence.
- Each sentence is composed of one or more sections.
- Lower key priority is provided.
- Last key priority is provided.
- Up to 128 table entries for all 16 sentences.
- Auto sleep functions are built in.
- CDS interface is provided.
- OKY function is provided optionally chipwise.
- Play all OKY is provided optionally chipwise.
- Random Play OKY is provided optionally chipwise.
- Playnext OKY is provided optionally chipwise.
- Continuous S.W.A.I. OKY is provided optionally chipwise.
- Home S.W.A.I. OKY is provided optionally chipwise.
- Up to 2 output pins are provided.
- Either 500us or 20ms debounce is provided.
- Either Sink or drive LED is provided optionally chipwise.
- Dynamic flash LED when playing audio is provided optionally chipwise at every LED pin.
- Fix flash LED when playing audio is provided optionally chipwise at every LED pin.
- On LED when playing audio is provided optionally chipwise at every LED pin.
- Variant flash speed on entry when playing audio is provided optionally chipwise.
- High busy when playing audio is provided optionally chipwise at LBS pin.
- Low busy when playing audio is provided optionally chipwise at LBS pin.
- Low Stop after playing audio is provided optionally chipwise at LBS pin.
- High Stop after playing audio is provided optionally chipwise at LBS pin.
- OKY's sentences could be different from those from TGS'.
- 8 different pitches are provided entrywise.
- Edge trigger is provided optionally chipwise.
- Level trigger is provided optionally chipwise.
- Holdable output by TG is provided optionally chipwise.
- Retriggerable TG by itself is provided optionally chipwise.
- Retriggerable TG by others is provided optionally chipwise.
- High trigger is provided optionally chipwise.
- Low trigger is provided optionally chipwise.

Block Diagram

