# Quad Array for ESD Protection

This quad monolithic silicon voltage suppressor is designed for applications requiring transient overvoltage protection capability. It is intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment, and other applications. Its quad junction common anode design protects four separate lines using only one package. These devices are ideal for situations where board space is at a premium.

#### **Features**

- SC–88A Package Allows Four Separate Unidirectional Configurations
- Low Leakage < 1 μA @ 3 V
- Breakdown Voltage: 6.1 V 7.2 V @ 1 mA
- Low Capacitance (90 pF typical)
- ESD Protection Meeting IEC1000-4-2
- Pb-Free Package is Available\*

#### **Mechanical Characteristics:**

- Void Free, Transfer-Molded, Thermosetting Plastic Case
- Corrosion Resistant Finish, Easily Solderable
- Package Designed for Optimal Automated Board Assembly
- Small Package Size for High Density Applications

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Power Dissipation @ 20 μs @T <sub>A</sub> ≤ 25°C (Note 1)	P <sub>pk</sub>	150	W
Steady State Power – 1 Diode (Note 2)	P <sub>D</sub>	385	mW
Thermal Resistance, Junction-to-Ambient Above 25°C, Derate	$R_{\theta JA}$	325 3.1	°C/W mW/°C
Maximum Junction Temperature	T <sub>Jmax</sub>	150	°C
Operating Junction and Storage Temperature Range	T <sub>J</sub> T <sub>stg</sub>	-55 to +150	°C
ESD Discharge MIL STD 883C – Method 3015–6 IEC1000–4–2, Air Discharge IEC1000–4–2, Contact Discharge	V <sub>PP</sub>	16 16 9	kV
Lead Solder Temperature (10 s duration)	T <sub>L</sub>	260	°C

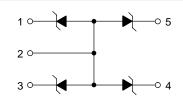
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- 1. Non-repetitive current per Figure 1. Derate per Figure 2.
- Only 1 diode under power. For all 4 diodes under power, P<sub>D</sub> will be 25%. Mounted on FR-4 board with min pad.



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SC-88A/SOT-323 CASE 419A

#### **MARKING DIAGRAM**



61 = Device Code

M = Date Code

= Pb–Free Package

(Note: Microdot may be in either location)

# ORDERING INFORMATION

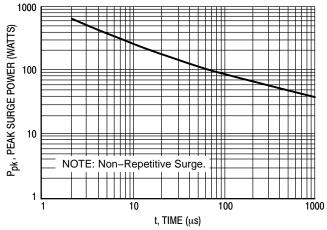
Device	Package	Shipping <sup>†</sup>
MSQA6V1W5T2	SC-88A	3000/Tape & Reel
MSQA6V1W5T2G	SC-88A (Pb-Free)	3000/Tape & Reel

- †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.
- \*T2 Suffix Devices are Packaged with Pin 1 Opposing Sprocket Hole.

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **ELECTRICAL CHARACTERISTICS**

	Breakdown Voltage V <sub>BR</sub> @ 1 mA (Volts)		Leakage Current I <sub>RM</sub> @ V <sub>RWM</sub> = 3 V	Capacitance @ 0 V Bias	Max V <sub>F</sub> @ I <sub>F</sub> = 200 mA	
Device	Min	Nom	Max	irin © irvin – σ i (μΑ)	(pF)	(V)
MSQA6V1W5	6.1	6.6	7.2	1.0	90	1.25



t, IIME (μs)
Figure 1. Pulse Width

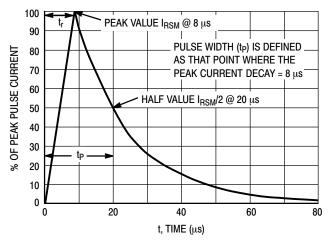


Figure 2. 8 x 20 μs Pulse Waveform

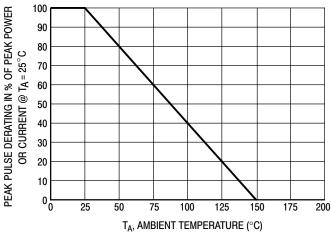


Figure 3. Pulse Derating Curve

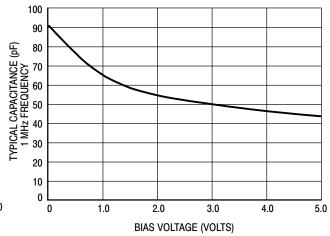
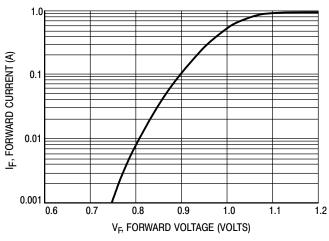


Figure 4. Capacitance



100 (SdWe) 10 2.5 μs SQUARE WAVE 2.5 μs SQUARE WAVE 30 V<sub>C</sub>, CLAMPING VOLTAGE (VOLTS)

Figure 5. Forward Voltage

Figure 6. Clamping Voltage versus Peak Pulse Current (Reverse Direction)

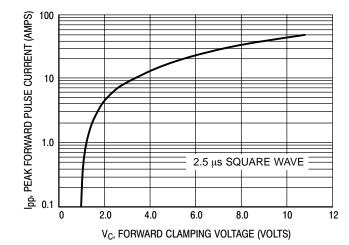
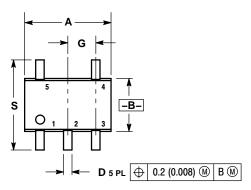
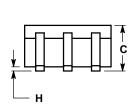


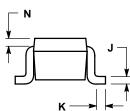
Figure 7. Clamping Voltage versus Peak Pulse Current (Forward Direction)

#### PACKAGE DIMENSIONS

SC-88A / SOT-353 / SC-70 CASE 419A-02 **ISSUE J** 







#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: INCH.
- 419A-01 OBSOLETE. NEW STANDARD 419A-02
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026 BSC		0.65 BSC		
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20 REF		
S	0.079	0.087	2.00	2.20	

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