International
IOR Rectifier

PD-96925B

IRHYS67230CM

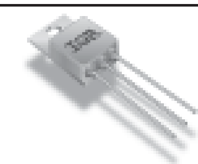
200V, N-CHANNEL

R6 TECHNOLOGY

RADIATION HARDENED POWER MOSFET THRU-HOLE (Low-Ohmic TO-257AA)

Product Summary

Part Number	Radiation Level	$R_{DS(on)}$	I_D
IRHYS67230CM	100K Rads (Si)	0.13Ω	16A
IRHYS63230CM	300K Rads (Si)	0.13Ω	16A



Low-Ohmic
TO-257AA

International Rectifier's R6™ technology provides superior power MOSFETs for space applications. These devices have improved immunity to Single Event Effect (SEE) and have been characterized for useful performance with Linear Energy Transfer (LET) up to 90MeV/(mg/cm²). Their combination of very low $R_{DS(on)}$ and faster switching times reduces power loss and increases power density in today's high speed switching applications such as DC-DC converters and motor controllers. These devices retain all of the well established advantages of MOSFETs such as voltage control, ease of paralleling and temperature stability of electrical parameters.

Features:

- Low $R_{DS(on)}$
- Fast Switching
- Single Event Effect (SEE) Hardened
- Low Total Gate Charge
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Eyelets
- Electrically Isolated
- Light Weight

Absolute Maximum Ratings — Pre-Irradiation

	Parameter		Units
I_D @ V_{GS} = 12V, T_C = 25°C	Continuous Drain Current	16	A
I_D @ V_{GS} = 12V, T_C = 100°C	Continuous Drain Current	10	
I_{DM}	Pulsed Drain Current ①	64	
P_D @ T_C = 25°C	Max. Power Dissipation	75	W
	Linear Derating Factor	0.6	W/°C
V_{GS}	Gate-to-Source Voltage	±20	V
E_{AS}	Single Pulse Avalanche Energy ②	83	mJ
I_{AR}	Avalanche Current ①	16	A
E_{AR}	Repetitive Avalanche Energy ①	7.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	9.0	V/ns
T_J T_{STG}	Operating Junction Storage Temperature Range	-55 to 150	°C
	Lead Temperature	300 (0.063 in. /1.6 mm from case for 10s)	
	Weight	4.3 (Typical)	g

For footnotes refer to the last page

01/19/07

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	—	V	V_{GS} = 0V, I_D = 1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.19	—	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	0.13	Ω	V_{GS} = 12V, I_D = 10A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	V_{DS} = V_{GS}, I_D = 1.0mA
g_{fs}	Forward Transconductance	11	—	—	S (℧)	V_{DS} = 15V, I_{DS} = 10A ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	10	μA	V_{DS}= 160V ,V_{GS}=0V
		—	—	25		V_{DS} = 160V, V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V_{GS} = 20V
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		V_{GS} = -20V
Q_g	Total Gate Charge	—	—	42	nC	V_{GS} =12V, I_D = 16A
Q_{gs}	Gate-to-Source Charge	—	—	10		V_{DS} = 100V
Q_{gd}	Gate-to-Drain ('Miller') Charge	—	—	20		
$t_{d(on)}$	Turn-On Delay Time	—	—	15	ns	V_{DD} = 100V, I_D = 16A
t_r	Rise Time	—	—	40		V_{GS} =12V, R_G = 7.5Ω
$t_{d(off)}$	Turn-Off Delay Time	—	—	35		
t_f	Fall Time	—	—	15		
$L_S + L_D$	Total Inductance	—	6.8	—	nH	Measured from Drain lead (6mm / 0.25in. from package) to Source lead (6mm /0.25in. from package)
C_{iss}	Input Capacitance	—	1660	—	pF	V_{GS} = 0V, V_{DS} = 25V
C_{oss}	Output Capacitance	—	206	—		f = 1.0MHz
C_{rss}	Reverse Transfer Capacitance	—	2.6	—		
R_g	Internal Gate Resistance	—	1.75	—	Ω	f = 1.0MHz, open drain

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	16	A	
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	64		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	T_j = 25°C, I_S = 16A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	—	300	ns	T_j = 25°C, I_F = 16A, di/dt ≤ 100A/μs
Q_{RR}	Reverse Recovery Charge	—	—	3.2	μC	V_{DD} ≤ 25V ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$.				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	1.67	°C/W	
R_{thJA}	Junction-to-Ambient	—	—	80		Typical Socket Mount

Note: Corresponding Spice and Saber models are available on International Rectifier Web site.

For footnotes refer to the last page

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ Tj = 25°C, Post Total Dose Irradiation ⑤⑥

	Parameter	Up to 300K Rads (Si)[1]		Units	Test Conditions
		Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	V	$V_{GS} = 0V$, $I_D = 1.0mA$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0		$V_{GS} = V_{DS}$, $I_D = 1.0mA$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	nA	$V_{GS} = 20V$
I_{GSS}	Gate-to-Source Leakage Reverse	—	-100		$V_{GS} = -20V$
I_{DSS}	Zero Gate Voltage Drain Current	—	10	µA	$V_{DS}=160V$, $V_{GS}=0V$
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-3)	—	0.134	Ω	$V_{GS} = 12V$, $I_D = 10A$
$R_{DS(on)}$	Static Drain-to-Source On-State ④ Resistance (Low Ohmic TO-257)	—	0.13	Ω	$V_{GS} = 12V$, $I_D = 10A$
V_{SD}	Diode Forward Voltage ④	—	1.2	V	$V_{GS} = 0V$, $I_D = 16A$

1. Part numbers IRHYS67230CM and IRHYS63230CM

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area

Ion	LET	Energy	Range	VDS (V)			
	(MeV/(mg/cm^2))	(MeV)	(µm)	@VGS= 0V	@VGS= -5V	@VGS= -10V	@VGS= -15V
Xe	43	2441	205	200	200	200	190
Xe	59	825	66	200	200	200	190
Au	90	1480	80	170	170	--	--

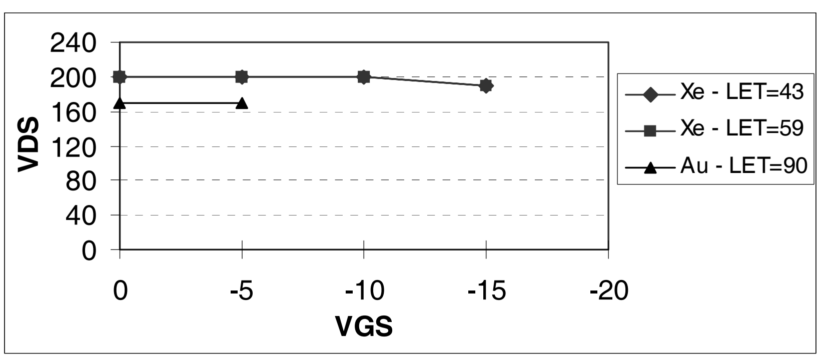


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

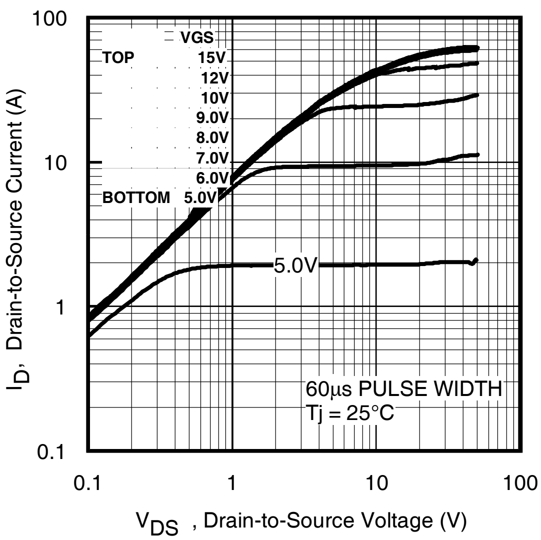


Fig 1. Typical Output Characteristics

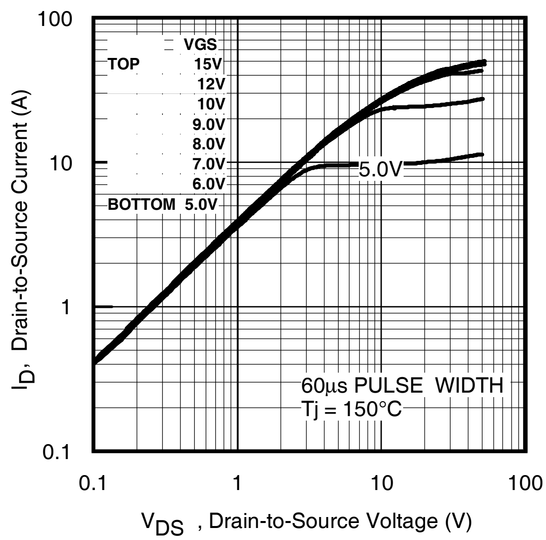


Fig 2. Typical Output Characteristics

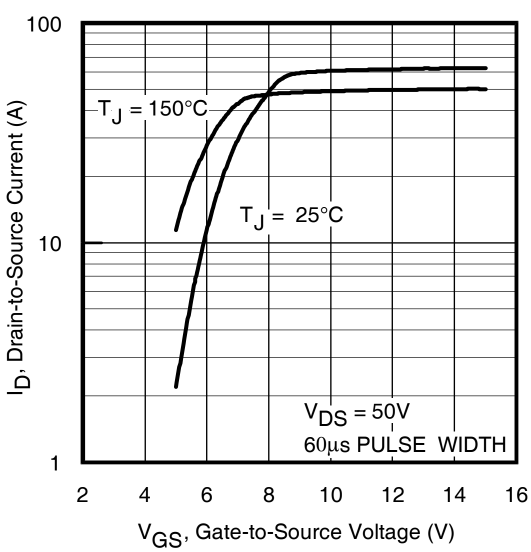


Fig 3. Typical Transfer Characteristics

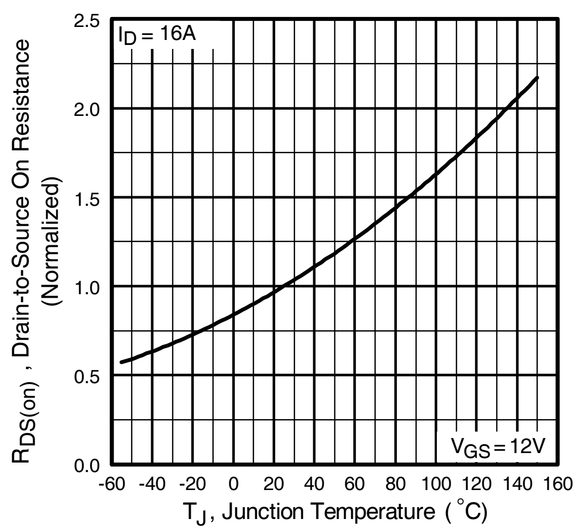


Fig 4. Normalized On-Resistance Vs. Temperature

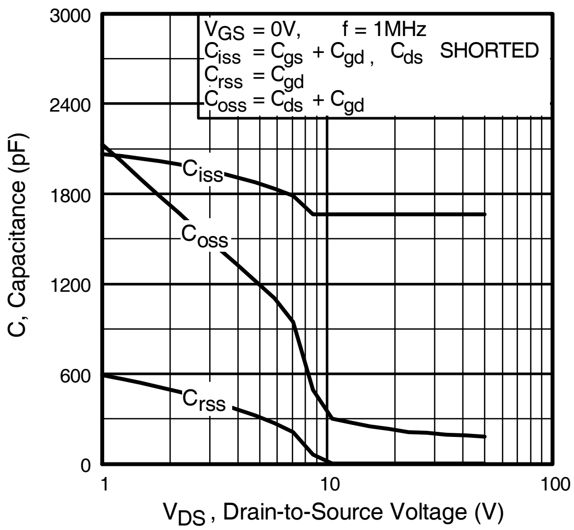


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

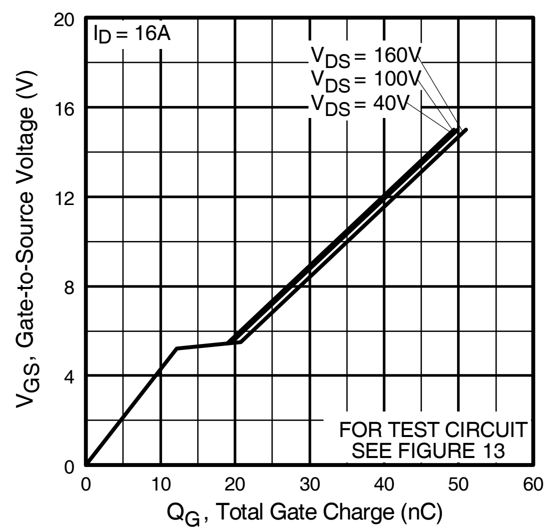


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

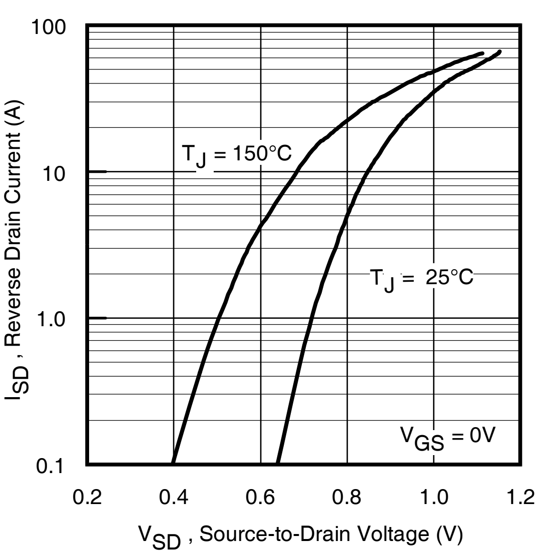


Fig 7. Typical Source-Drain Diode Forward Voltage

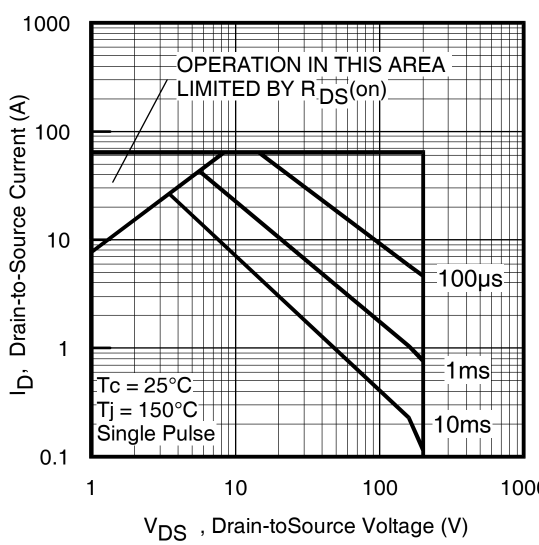


Fig 8. Maximum Safe Operating Area

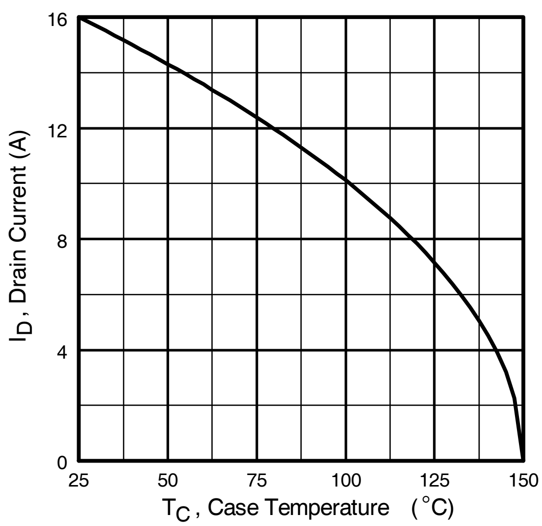


Fig 9. Maximum Drain Current Vs. Case Temperature

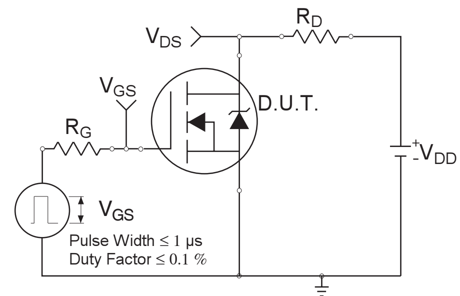


Fig 10a. Switching Time Test Circuit

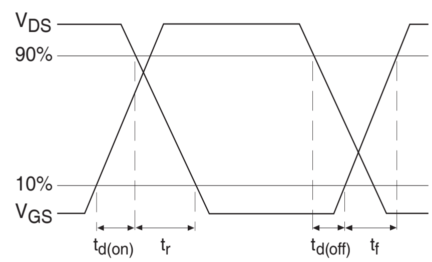


Fig 10b. Switching Time Waveforms

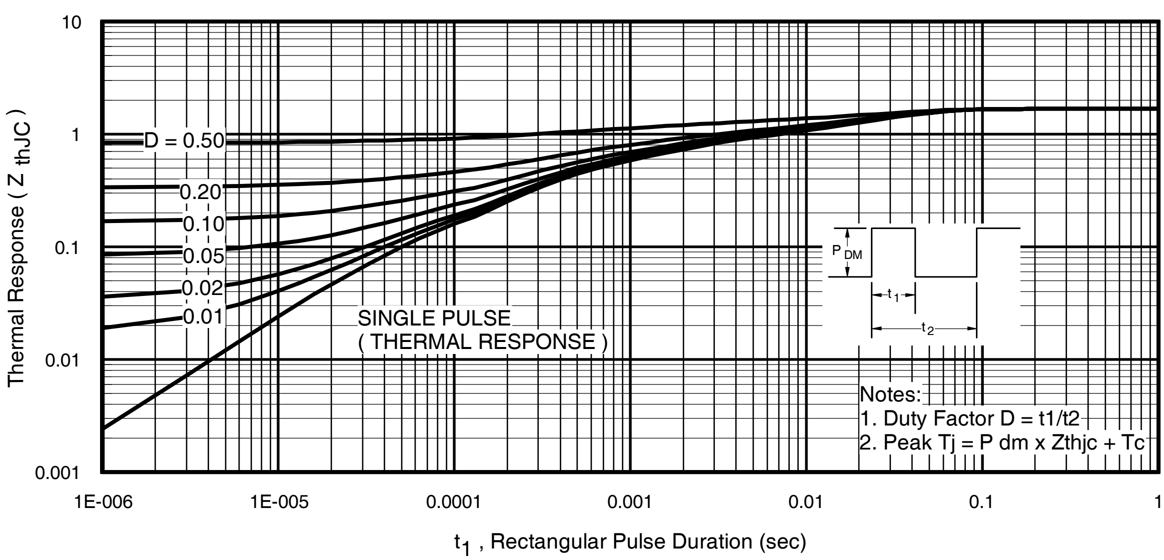


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

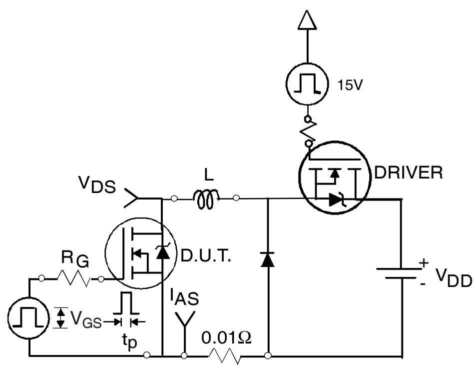


Fig 12a. Unclamped Inductive Test Circuit

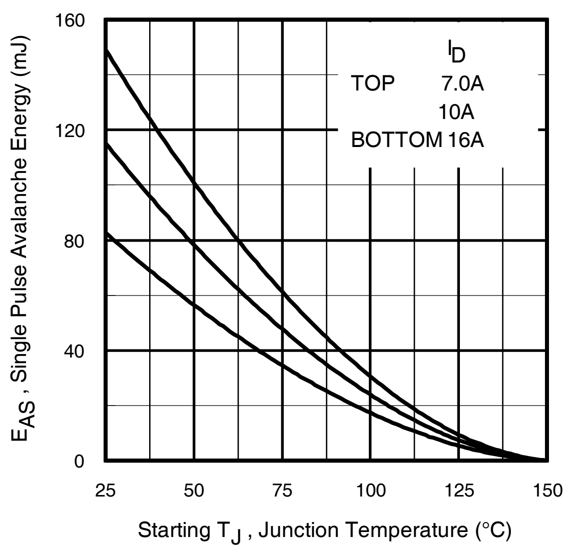


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

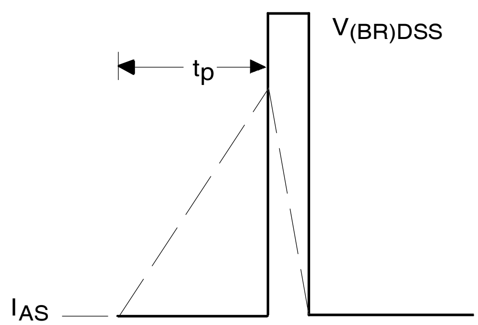


Fig 12b. Unclamped Inductive Waveforms

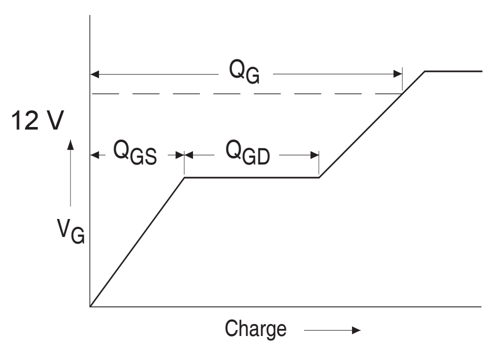


Fig 13a. Basic Gate Charge Waveform

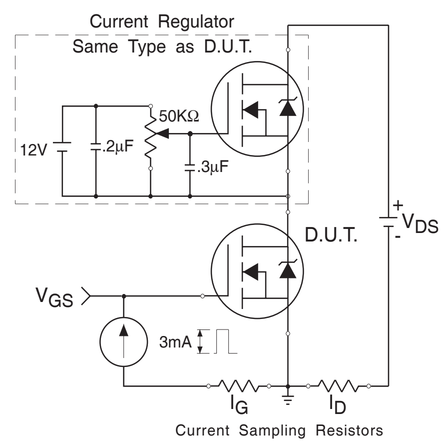


Fig 13b. Gate Charge Test Circuit

Footnotes:

① Repetitive Rating; Pulse width limited by maximum junction temperature.

② V_{DD} = 25V, starting T_J = 25°C, L= 0.65mH Peak I_L = 16A, V_{GS} = 12V

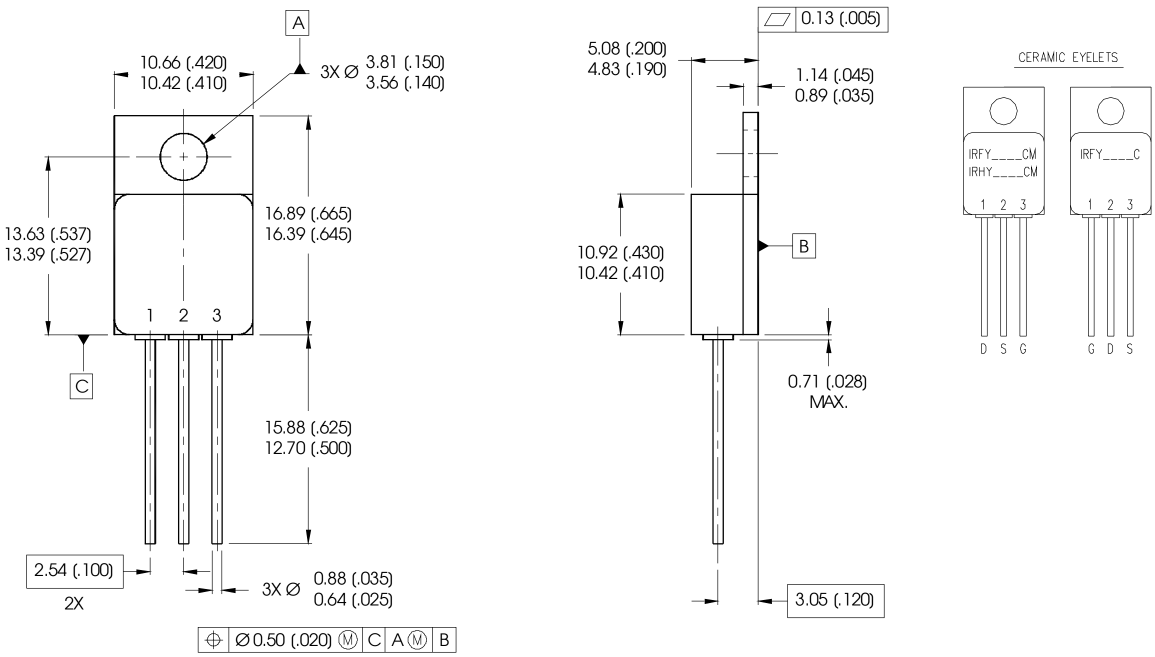
③ $I_{SD} \leq$ 16A, di/dt $\leq$ 750A/µs, $V_{DD} \leq$ 200V, $T_J \leq$ 150°C

④ Pulse width $\leq$ 300 µs; Duty Cycle $\leq$ 2%

⑤ **Total Dose Irradiation with V_{GS} Bias.** 12 volt V_{GS} applied and V_{DS} = 0 during irradiation per MIL-STD-750, method 1019, condition A.

⑥ **Total Dose Irradiation with V_{DS} Bias.** 160 volt V_{DS} applied and V_{GS} = 0 during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — Low-Ohmic TO-257AA



NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE TO-257AA.

LEAD ASSIGNMENTS

1 = DRAIN
2 = SOURCE
3 = GATE

International
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