Dual digital transistors IMH22

●Features

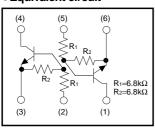
In addition to the features of regular digital transistors.

- 1) Low saturation voltage, typically VCE (sat) =40mV at Ic / IB=50mA / 2.5mA, makes these transistors ideal for muting circuits.
- 2) These transistors can be used at high current levels, Ic=600 mA.
- 3) Two DTC663E chips in a SMT package.

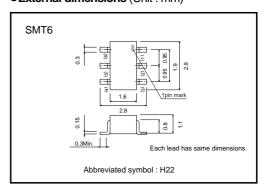
●Structure

NPN digital transistor (Built-in resistor type)

●Equivalent circuit



●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

| Parameter | Symbol | Limits | Unit |
|----------------------|--------|-------------|------|
| Supply voltage | Vcc | 20 | V |
| Input voltage | VIN | -20 to +20 | V |
| Output current | lc | 600 | V |
| Power dissipation | Pd | 300(TOTAL) | mW * |
| Junction temperature | Tj | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

^{* 200}mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|------------------------|---------------------|------|------|------|------|---|
| Input voltage | V _{I(OFF)} | _ | _ | 0.5 | ٧ | Vcc=5V , Io=100μA |
| input voltage | VI(ON) | 2 | _ | _ | | Vo=0.3V , Io=10mA |
| Output voltage | V _{O(ON)} | _ | _ | 150 | mV | I _O /I _I =50mA/2.5mA |
| Input current | lı | _ | _ | 1.3 | mA | V _I =5V |
| Output current | I _{O(OFF)} | _ | _ | 0.5 | μΑ | Vcc=20V , Vi=0V |
| DC current gain | G ₁ | 250 | _ | _ | _ | V ₀ =5V , I ₀ =50mA |
| Input ersistance | R ₁ | 4.76 | 6.8 | 8.84 | kΩ | _ |
| Resistance ratio | R2/R1 | 0.8 | 1 | 1.2 | _ | _ |
| Transition frequency | f⊤ | _ | 150 | _ | MHz | VcE=10V, IE=-50mA, f=100MHz * |
| Output "ON" resistance | Ron | _ | 0.9 | _ | Ω | V _I =5V, R _L =1kΩ, f=1kHz |

^{*}Transition frequency of the device.

●Packaging specifications and hFE

| Туре | Package | SMT6 |
|-------|------------------------------|--------|
| | Packaging type | Taping |
| | Code | T110 |
| | Basic ordering unit (pieces) | 3000 |
| IMH22 | | 0 |

•Electrical characteristic curves

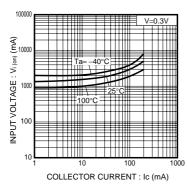


Fig.1 Input voltage vs. output current (ON characteristics)

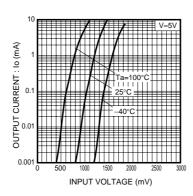


Fig.2 Output current vs. input voltage (OFF characteristics)

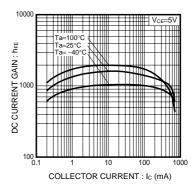


Fig.3 DC current gain vs. output current characteristics

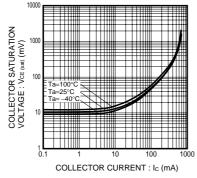


Fig.4 Output voltage vs. output curent characteristics

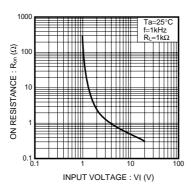


Fig.5 "ON" characteristics vs. input voltage characteristics

●Ron measurement circuit

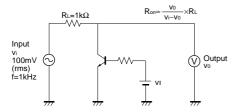


Fig.4 Output "ON" resistance (Ron) measurement circuit

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