

Current Transducer HY7-P

For the electronic measurement of currents: DC, AC, pulsed..., with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).



CE

Primary nomina r.m.s. current $\mathbf{I}_{PN}(A)$	Il Primary current measuring range I _P (A)	Primary conductor (mm)	Type	
7.5	± 22.5	Ø1.0	HY 7-P	
V _C	Supply voltage (± 5 %)		± 15	٧
I _c	Current consumption		± 10	mΑ
Î	Overload capability (1 ms)		50 x I _{PN}	
V _d	R.m.s. voltage for AC isolation test, 50/60Hz, 1 min		2.5	kV
V _b	R.m.s. rated voltage, safe separation		500 ¹⁾	V
R _{IS}	Isolation resistance @ 500 VDC		> 1000	$M\Omega$
V OUT	Output voltage @ $\pm \mathbf{I}_{PN}$, $\mathbf{R}_{1} = 10 \text{ k}\Omega$, $\mathbf{T}_{\Delta} = 25^{\circ}\text{C}$		± 4	V
R _{OUT}	Output internal resistance	A	100	Ω
R	Load resistance		> 1	kΩ

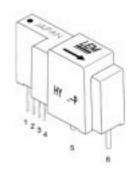
Accu	racy - Dynamic performance data			
X	Accuracy @ \mathbf{I}_{PN} , $\mathbf{T}_{A} = 25^{\circ}$ C (without offset)		< ± 1	%
E ,	Linearity 2) (0 ± I _{PN})		< ± 1	% of I _{PN}
V OF	Electrical offset voltage, T _A = 25°C		$< \pm 40$	m̈ν̈
E _L V _{OE} V _{OH}	Hysteresis offset voltage $@ \mathbf{I}_p = 0;$			
3	after an excursion of 1 x I _{PN}		< ± 15	mV
\mathbf{V}_{OT}	Thermal drift of V _{OF}	typ.	± 1.5	mV/K
0.		max.	± 3	mV/K
TCE _G	Thermal drift of the gain (% of reading)		$< \pm 0.1$	%/K
t,	Response time @ 90% of I _P		< 3	μs
di/dt	di/dt accurately followed		> 50	A/µs
f	Frequency bandwidth ³⁾ (- 3 dB)		DC 50) kHz

General data						
T _A	Ambient operating temperature	- 10 + 80	°C			
T _s	Ambient storage temperature	- 25 + 85	°C			
m	Mass	< 14	g			
	Standards 4)	EN 50178				

Notes: 1) Pollution class 2, overvoltage category III

- 2) Linearity data exclude the electrical offset.
- ³⁾ Please refer to derating curves in the technical file to avoid excessive core heating at high frequency
- ⁴⁾ Please consult characterisation report for more technical details and application advice.

 $I_{PN} = 7.5 A$



Features

- Hall effect measuring principle
- Galvanic isolation between primary and secondary circuit
- Isolation voltage 2500 V~
- Compact design for PCB mounting
- Low power consumption
- Extended measuring range (3 x ▮_{DN})
- Insulated plastic case recognized according to UL 94-V0.

Advantages

- Easy mounting
- Small size and space savings
- Only one design for wide current ratings range
- High immunity against external interference

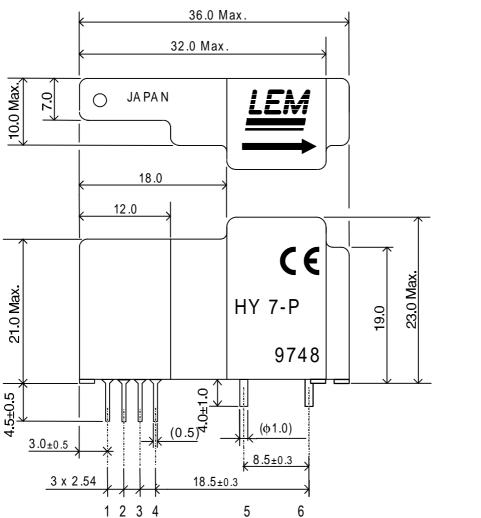
Applications

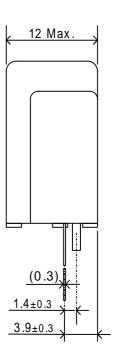
- General purpose inverters
- Switched-Mode Power Supplies (SMPS)
- AC motor speed control
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Power supplies for welding applications.

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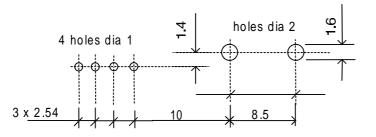


HY 7-P Dimensions (in mm)





PCB MOUNTING DIMENSIONS (in mm ± 0.1 , hole -0, +0.2) HY 7-P



PIN ARRANGEMENT

- 1 +15V
- 2 15V
- 3 OUTPUT
- 4 0V
- 5 PRIMARY IN
- 6 PRIMARY OUT

LEM reserves the right to change limits and dimensions.