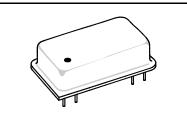


- SAW Frequency Stabilization
- Fundamental-Mode Oscillation at 750.0 MHz
- 0.8" x 0.5" x 0.25" Metal Dip Case

This general-purpose oscillator is stabilized by surface-acoustic-wave (SAW) technology. Fundamental oscillation at 750.0 MHz eliminates all internally generated spurious outputs except integral harmonics of 750.0 MHz. The compact size of the rugged, metal, hermetically-sealed case makes this oscillator suitable for a variety of applications.

HO1301

750.0 MHz SAW Oscillator



Dip 14-8 Case

Absolute Maximum Ratings

Ratir	Value	Units		
DC Supply Voltage		0 to +13	VDC	
Ambient Temperature	Powered	-40 to +70	°C	
	Storage	-40 to +85		

Electrical Characteristics

Characteristic		Sym	Notes	Minimum	Typical	Maximum	Units
Operating Frequency	Absolute Frequency	f _O	1, 7	749.750	750.0	750.250	MHz
	Tolerance from 750.0 MHz	Δf_{O}	1, 1			±250	kHz
RF Output Power		Po	3, 6	+7	+11	+13	dBm
Spurious Outputs	Second Harmonics					-15	
	Third and Higher Harmonics		3, 6, 7			-20	dBc
	Nonharmonic				<-80	-60	
RF Impedance	Nominal Impedance	Z _O	3		50		Ω
	Operating Load VSWR	G_L	3, 5			1.5:1	
DC Power Supply	Operating Voltage	V _{CC}	3, 6	7.9	8.4	8.9	VDC
	Operating Current	I _{CC}	3, 0		30	40	mA
Operating Ambient Temperature		T _A	3, 6	-20		+60	°C
Lid Symbolization (YY=Year, WW=Week)							

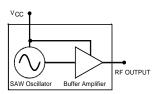


CAUTION: Electrostatic Sensitive Device. Observe precautions for handling. COCOM CAUTION: Approval by the U.S. Department of Commerce is required prior to export of this device.

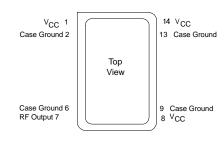
Notes:

- One or more of the following United States patents apply: 4,616,197; 4,610,681; and 4 761 616
- Unless noted otherwise, all specifications are listed at T_A = +25°C ±2°C, V_{CC} = nominal voltage ±0.01 VDC, and load impedance = 50 Ω with VSWR ≤ 1.5:1.
- 3. The design, manufacturing process, and specification of this device are subject to change without notice.
- Applies to oscillator only and not to sidebands caused by external electrical or mechanical sources. (Dedicated external voltage regulation with low-frequency filtering for the DC power supply and proper circuit board layout are recommended for optimum spectral purity.)
- For specified maximum operating load VSWR (any angle) at F_O. (No instability or damage will occur for any passive load impedance.)
- 6. For any combination of V_{CC} and T_A within the specified operating ranges.
- 7. Applies for any combination of Note 5 and 6 conditions.

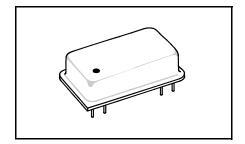
BLOCK DIAGRAM



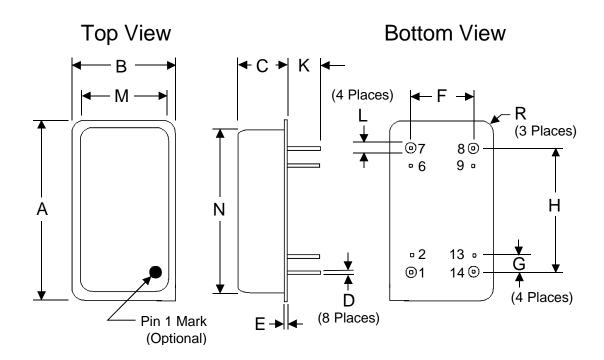
ELECTRICAL CONNECTIONS



DIP14-8 Metal Dual-Inline Package with 8 leads in a 14-lead DIP configuration



Dimension	mm		Inches		
Dimension	MIN	MAX	MIN	MAX	
Α	_	20.45	_	0.805	
В	ı	12.83	1	0.505	
С	_	6.35	_	0.250	
D	0.40	0.51	0.016	0.020	
E	0.64 Nominal		0.025 Nominal		
F	7.62 Nominal		0.300 Nominal		
G	2.54 Nominal		0.100 Nominal		
Н	15.24 Nominal		0.600 Nominal		
К	5.97	6.73	0.235	0.265	
L	1.30	_	0.051	_	
М	_	11.18	_	0.440	
N	_	18.80	_	0.740	
R	1.75	2.26	0.069	0.089	



 RF Monolithics, Inc.
 Phone: (972) 233-2903
 Fax: (972) 387-9148

 RFM Europe
 Phone: 44 1963 251383
 Fax: 44 1963 251510

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