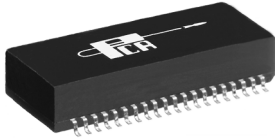


# 10/100Base-TX Module for Multi-Port Applications

## EPF8105S



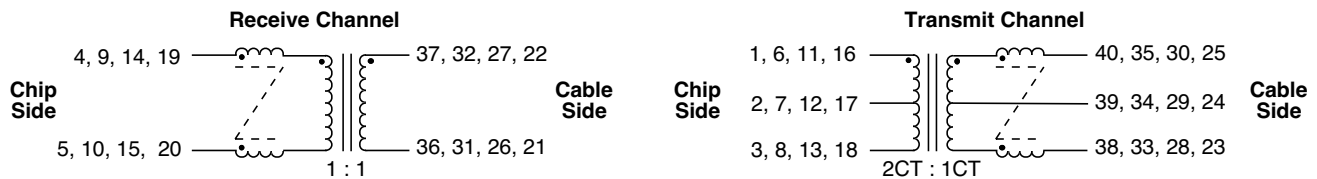
- Recommended for use with Level One LXT974 Chip
- For use with Transceiver requiring 2:1 Transmit Turn Ratio
- Significantly Improved Common Mode Attenuation
- Guaranteed to operate with 8 mA DC Bias at 70°C
- Complies with or exceeds IEEE 802.3, 10/100Base-TX Standards

### Electrical Parameters @ 25° C

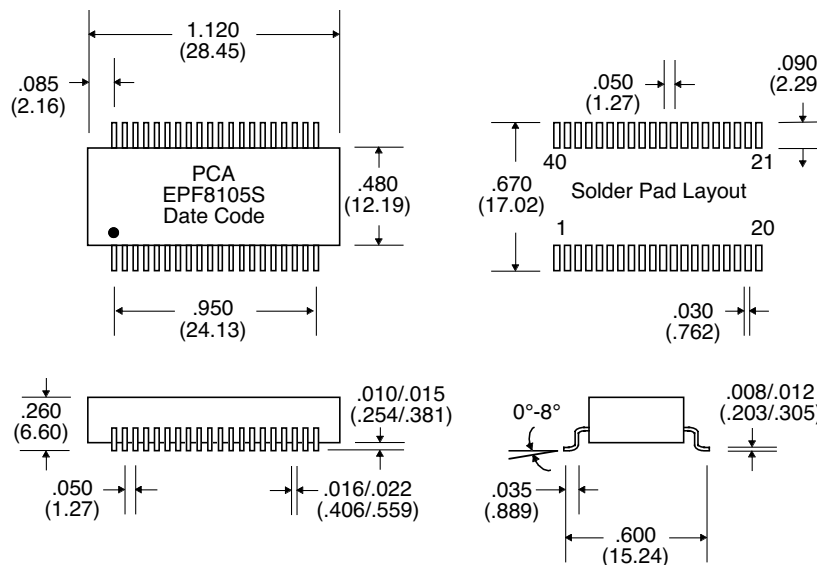
OCL (@ 70°C)	Insertion Loss (dB Max.)			Return Loss (dB Min.)			CMR (dB Min.)		Diff. to CMR (dB Min.)		Crosstalk (dB Min.)	
	1-60 MHz	80 MHz	100 MHz	1-30 MHz	60 MHz	80 MHz	30-100 MHz	200 MHz	1-60 MHz	200 MHz	60 MHz	100 MHz
100 KHz, 0.1 Vrms 8 mA DC Bias												
<b>Cable Side</b>	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv	Xmit/Rcv		
350 $\mu$ H	-1/-1	-2/-1	-2.5/-1	-18/-18	-12/-12	-10/-10	-30/-20	-15/-15	-40/-25	-30/15	-40	-30

• Isolation : 1500 Vrms • Impedance : 100  $\Omega$  • Rise Time : 3.0 nS Max. •

### Schematic



### Package



Unless Otherwise Specified Dimensions are in Inches /mm  $\pm$ .010 / .25