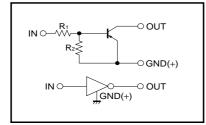
Digital transistor (built-in resistors) DTA144VKA/DTA144VSA

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on / off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

Equivalent circuit



• Absolute maximum ratings (Ta=25°C)

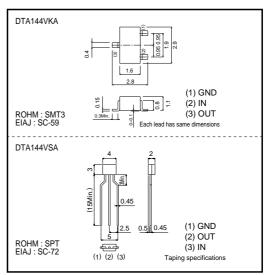
Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	-50	V	
Input voltage		Vi -40 to		V	
		lo	-30	mA	
Output current		Iс(мах.) —100			
Power dissipation	DTA144VKA	Pd	200	mW	
	DTA144VSA	Pa	300		
Junction temperature	•	Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

Packaging, marking and packaging specifications

Туре	DTA144VKA	DTA144VSA
Package	SMT3	SPT
Marking	E56	-
Packaging code	T146	TP
Basic ordering unit (pieces)	3000	5000

ROHM

•External dimensions (Unit : mm)



Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	-1	V	Vcc= -5V , Io= -100µA
	VI(on)	-6	-	-		Vo= -0.3V , Io= -2mA
Output voltage	VO(on)	-	-0.1	-0.3	V	lo= -10mA , li= -0.5mA
Input current	h	-	-	-0.16	mA	Vi= -5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc= -50V , VI=0V
DC current gain	Gi	33	-	-	-	Io= -5mA , Vo= -5V
Input resistance	R1	32.9	47	61.1	kΩ	_
Resistance ratio	R2/R1	0.17	0.21	0.26	-	_
Transition frequency	f⊤	-	250	-	MHz	Vce= -10V , Ie=5mA , f=100MHz *

* Transition frequency of the device.

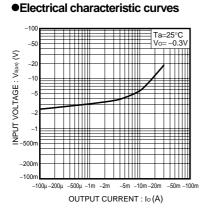
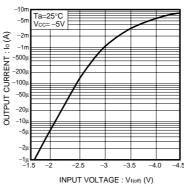
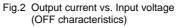
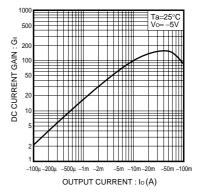
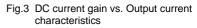


Fig.1 Input voltage vs. Output current (ON characteristics)









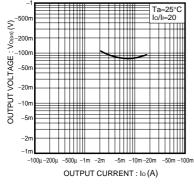


Fig.4 Output voltage vs. Output current characteristics

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