

**SANYO**

No.799D

**D T A 0 5**

Silicon Planar Type

0.5A Bidirectional Thyristor

**Features**

- Low AC power control
- Small size and light weight : 0.2g
- Peak OFF-state voltage : 100 to 400V
- RMS ON-state current : 0.5A
- TO-92 package

**Absolute Maximum Ratings at Ta = 25°C**

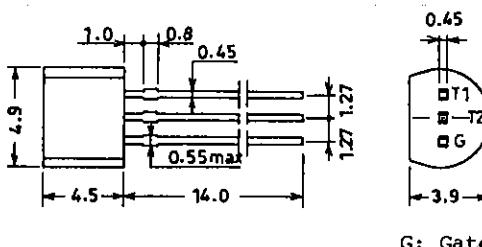
	V <sub>DRM</sub>	DTA05B 100	DTA05C 200	DTA05E 400	unit V
Repetitive Peak OFF-State Voltage					
RMS ON-State Current	I <sub>T</sub> (RMS)	Ta = 20°C, single-phase full-wave Peak 1 cycle, 50Hz	→	→	0.5 A
Surge ON-State Current	I <sub>TSM</sub>	→	→	6 A	
Amperes Squared-Seconds	$\int I^2 T \cdot dt$	→	→	0.18 A <sup>2</sup> s	
Peak Gate Power Dissipation	P <sub>GM</sub>	→	→	1 W	
Average Gate Power Dissipation	P <sub>G(AV)</sub>	→	→	0.1 W	
Peak Gate Current	I <sub>GM</sub>	→	→	0.5 A	
Peak Gate Voltage	V <sub>GM</sub>	→	→	6 V	
Junction Temperature	T <sub>j</sub>	→	→	110 °C	
Storage Temperature	T <sub>stg</sub>	→	→	-25 to +110 °C	

**Electrical Characteristics at Ta = 25°C**

	I <sub>DRM</sub>	T <sub>j</sub> = 110°C, V <sub>D</sub> = V <sub>DRM</sub>	min	typ	max	unit
Repetitive Peak OFF-State Current			0.1		mA	
ON-State Voltage	V <sub>T</sub>	I <sub>T</sub> = 2A	2.0		V	
Holding Current	I <sub>H</sub>	R <sub>L</sub> = 100Ω	25		mA	
Gate · Trigger Current ※ (I)	I <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	15		mA	
" (II)	I <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	15		mA	
" (III)	I <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	—			
" (IV)	I <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	15		mA	
Gate · Trigger Voltage ※ (I)	V <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	2.3		V	
" (II)	V <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	2.3		V	
" (III)	V <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	—			
" (IV)	V <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	2.3		V	
Gate · Nontrigger Voltage	V <sub>GD</sub>	T <sub>c</sub> = 25°C, V <sub>D</sub> = V <sub>DRM</sub>	0.2		V	
Thermal Resistance	R <sub>th(j-a)</sub>		250		°C/W	

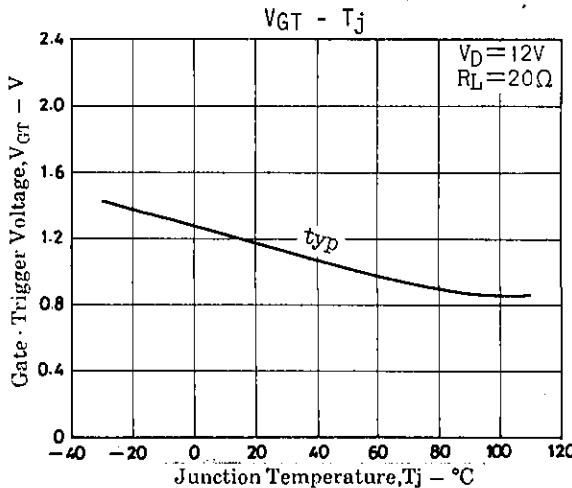
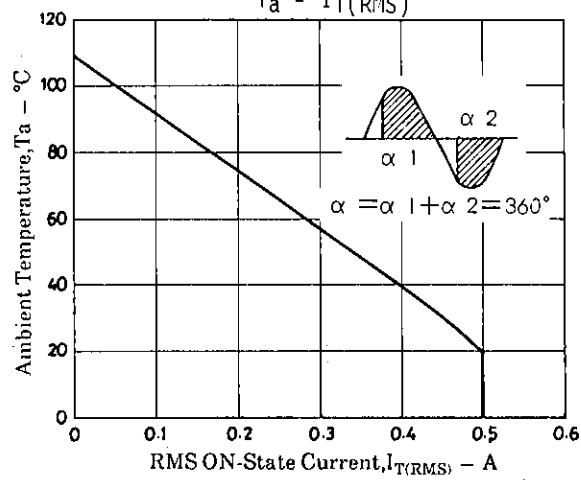
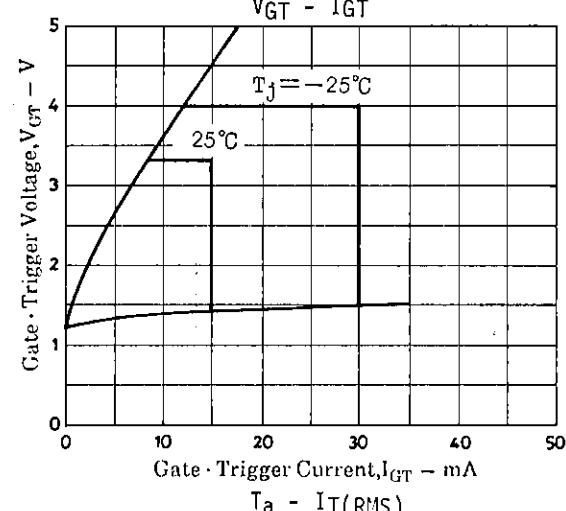
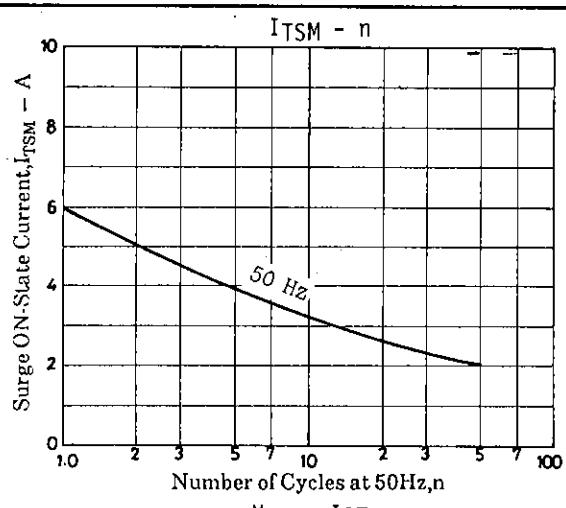
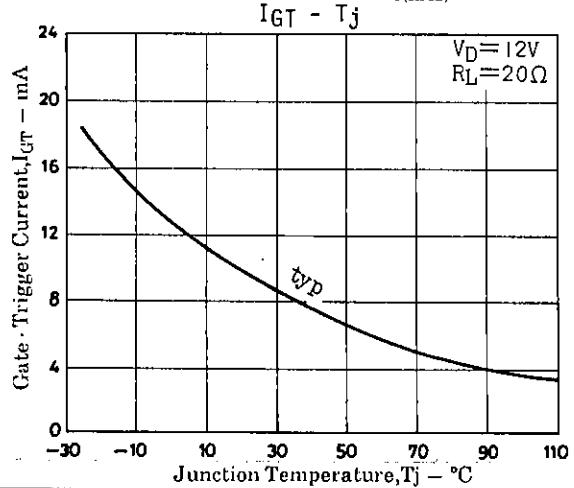
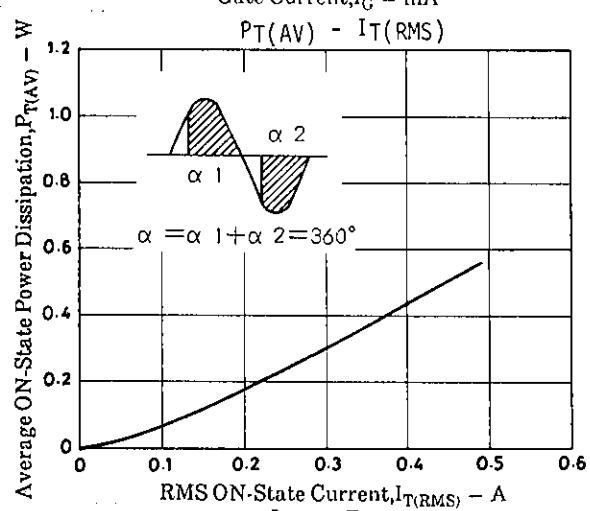
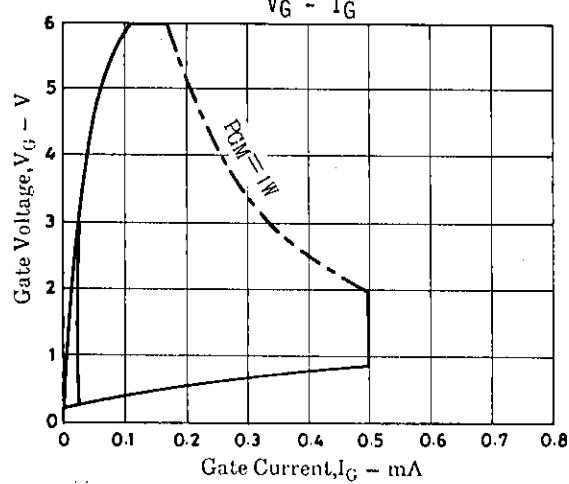
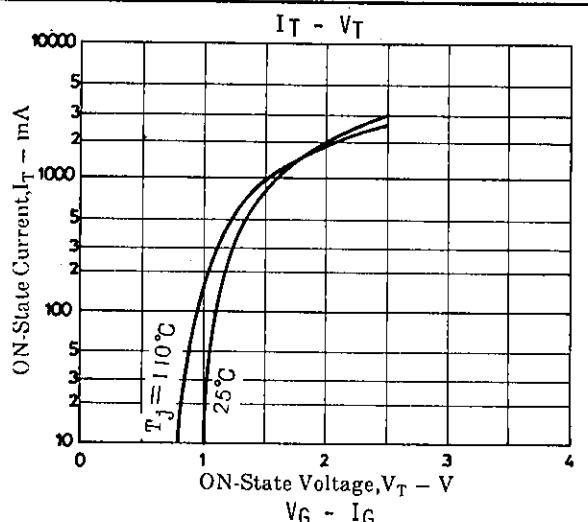
※ : The gate trigger mode is shown below.

Trigger mode	T2	T1	G
I	+	-	+
II	+	-	-
III	-	+	+
IV	-	+	-

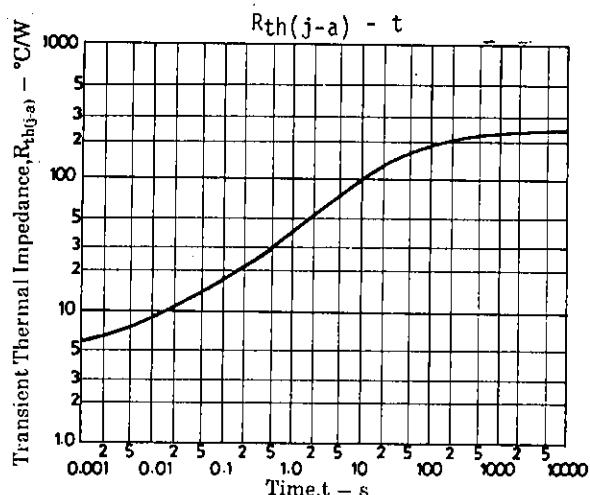
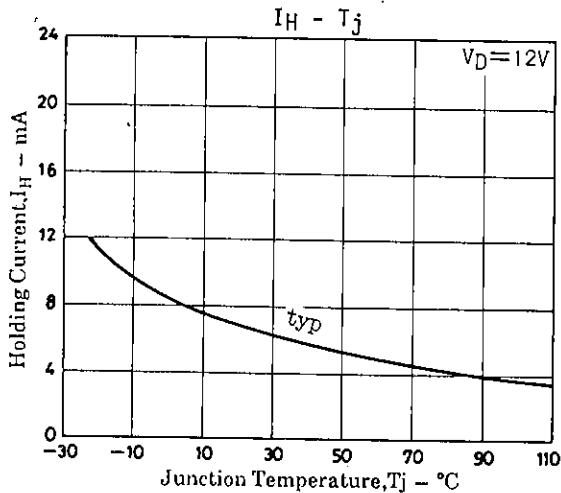
**Package Dimensions 1097A**  
(unit: mm)

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# DTA05



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