

Continental Device India Limited







SOT-23 Formed SMD Package

CSA1162

$LOWFREQUENCY\,GENERAL\,PURPOSE\,AMPLIFIER\,TRANSISTOR$

P-N-P transistor

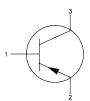
Marking

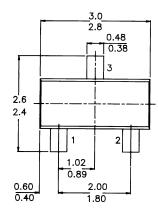
CSA1162Y-3E CSA1162GR(G)-3F

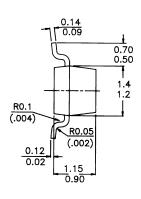
PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm

Pin configuration

- 1 = BASE
- 2 = EMITTER
- 3 = COLLECTOR







ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	50 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	50 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5 V
Collector current (d.c.)	$-I_C$	max.	150 mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max.	150 mW
Junction temperature	T_{i}	max.	150 ° C
D.C. current gain	,		
$-I_C = 2 \text{ mA}; -V_{CE} = 6V$	$h_{\!F\!E}$	min.	70
		max.	400

RATINGS (at $T_A = 25$ °C unless otherwise specified)

Limiting values			
Collector-base voltage (open emitter)	$-V_{CBO}$	max.	50 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	50 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5 V
Collector current (d.c.)	$-I_C$	max.	150 mA
Base current	$-I_B$	max.	30 mA

Total power dissipation at $T_{amb} = 25^{\circ}C$ Storage temperature Junction temperature	P _{tot} Tstg T _j	max. –50 to max.	150 mW +150 ° C 150 ° C
CHARACTERISTICS (at $T_A = 25^{\circ}C$ unless otherwise Collector-emitter breakdown voltage	specified)		
$-I_C = 1 \text{ mA; } I_B = 0$	-V _{(BR)CE}	O min	50 V
Collector cut-off current $-V_{CB} = 50 \ V; I_E = 0$	-I _{CBO}	max.	100 nA
Emitter cut-off current $V_{EB} = 5 \ V; I_C = 0$	I_{EBO}	max.	100 nA
Saturation voltage $-I_C = 100 \text{ mA}$; $-I_B = 10 \text{ mA}$	-V _{CEsat}	max.	0.3 V
D.C. current gain $I_C = 2 \text{ mA; } -V_{CE} = 6 \text{ V}$	h_{FE}	min. max.	70 400
	Y	min. max.	120 240
	GR(G)	min. max.	200 400
Transition frequency $V_{CE} = 10 \ V; I_C = 1 \ mA$	f_T	min.	80 MHz
Collector output capacitance $V_{CB} = 10 \ V; I_E = 0; f = 1 \ MHz$	C_{ob}	max.	7 pF
Noise figure $V_{CE} = 6 \text{ V; } I_C = 0.1 \text{ mA}$ $f = 1 \text{ kHz; } R_g = 10 \text{ kw}$	N_F	max.	10 dB

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-579 6150 Fax + 91-11-579 9569, 579 5290
e-mail sales@cdil.com www.cdil.com