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Four Output Driver for White LEDs

Features

- 2.9V to 6V input voltage range
- Powers display backlight and/or flash WLED
- Low external parts count, requires no inductor and ballast resistors
- Low EMI and reflected ripple
- Adaptive charge pump ratio (1x or 1.5x) maximizes efficiency at both high and low input voltage
- Precision regulation for each output with 2% current matching at 20mA
- Programmable LED current via ISET pin
- Typical 500 KHz fixed switching frequency
- Supports up to 300mA, drives four LEDs regulated to 50mA each
- Analog and PWM intensity control
- Less than 10µA shutdown current
- Over-current and over-temperature protection
- Undervoltage lockout
- Soft-start limits start-up inrush current
- 16 lead TQFN package
- Optional RoHS compliant lead free package

Applications

- Drives white LEDs for STN/TFT Color LCD backlighting
- Cell phones, PDAs
- Digital Still Cameras
- Flash for DSC

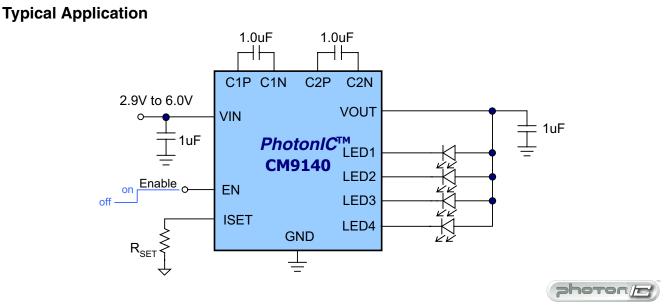
Product Description

The CM9140 is an adaptive fractional switched capacitor (charge pump) regulator optimized for driving four white LEDs. Each LED's driver current is matched to within 2% for uniform intensity. It supports an input voltage range of 2.9V to 6V, with undervoltage lockout. A failure detection circuit prevents the loss of power when one or more LEDs fail (short or open). Internal over-temperature and over-current management provide short circuit protection.

The CM9140 regulates up to 300mA of output current to drive WLEDs, allowing up to 50mA per LED channel. The maximum LED current is programmed with an external resistor. The EN input allows for Analog and PWM brightness control. The CM9140 can also be used for a camera flash. In full shutdown mode, the CM9140 draws only 10μ A.

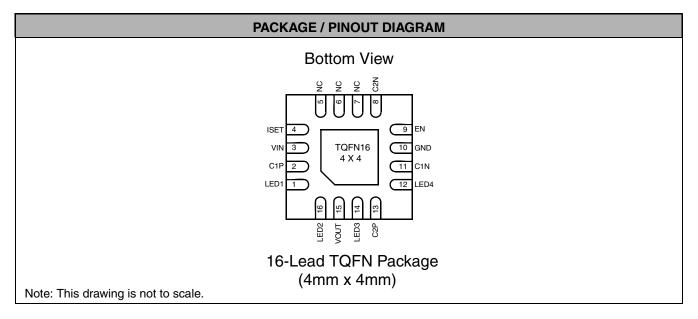
The CM9140 automatically selects the most efficient charge pump ratio based on the operating voltage requirement of the white LEDs. The proprietary design architecture maintains high efficiency (> 80%), and at low V_{IN} provides longer battery life. With a high V_{IN}, or when the adapter is powered, it provides cool reliable operation.

The CM9140 is available in a compact, 16-lead TQFNpackage. It can operate over the industrial temperature range of -40°C to 85°C.





Package Pinout



Ordering Information

PART NUMBERING INFORMATION				
		Lead-free Finish		
Leads	Package	Ordering Part Number ¹	Part Marking	
16	TQFN	CM9140-01QE		

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

Specifications

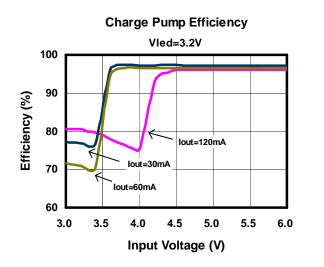
ABSOLUTE MAXIMUM RATINGS				
PARAMETER	RATING	UNITS		
ESD Protection (HBM)	±2	kV		
Pin Voltages V _{IN} to GND V _{OUT} to GND ISET, EN to GND All other pins to GND	[GND - 0.3] to +6.0 [GND - 0.3] to +7.0 [GND - 0.3] to +5.0 [GND - 0.3] to +5.0	V V V		
Storage Temperature Range	-65 to +150	°C		
Operating Temperature Range	-40 to +85	°C		
Lead Temperature (Soldering, 10s)	300	°C		

Specifications (cont'd)

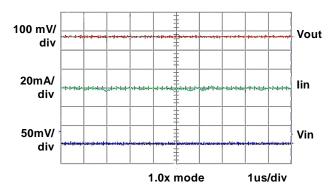
ELECTRICAL OPERATING CHARACTERISTICS							
V_{IN} = 3.6V; All outputs are on. Typical values are at T_A = 25°C.							
SYMBOL	PARAMETER CONDITIONS		MIN	ТҮР	МАХ	UNIT S	
V _{IN}	Supply Voltage Range		2.9		6.0	V	
V _{UVLO}	Undervoltage Lockout	All outputs are no load.	1.7	1.8	1.9	V	
Ι _Q	Quiescent Current	1x mode		500		μΑ	
I _{SD}	Shutdown Supply Current	V _{EN} < 0.4V		2	10	μA	
VOUT Charge F	Pump					I	
V _{OUT}	Output Voltage	$I_{OUT} = 0$ mA to 120mA, $V_{IN} = 3.0$ to 5.5V	4.2		5.5	V	
I _{LED TOT}	Total ILED Current	ΣI_{LED1} thru I_{LED4} +photoflash			300	mA	
ILED							
	Accuracy of ISET	V _{IN} = 3.0V to 5.5V		1		%	
	Matching current between LED1 to LED4	V _{IN} = 4.0V, I _{LED 1,2,3,4} = 20mA		2	5	%	
	I _{LED} per driver	Device total I _{LED} < 200mA			50	mA	
EN, ISET						I	
V _{IH}	High Level Input Voltage		1.8				
V _{IL}	Low Level Input Voltage				0.4		
Protection	•	-	ı				
	Over-current Limit			400		mA	
	Over-temperature Limit			135		°C	
	Over-temperature Hysteresis			15		°C	

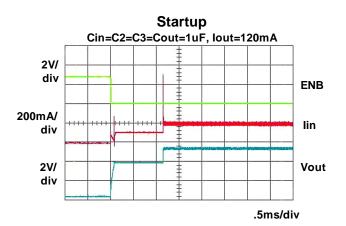
PRELIMINARY CM9140

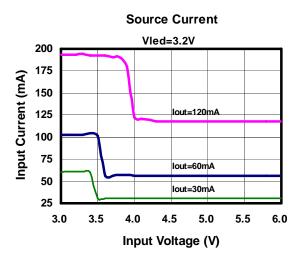
Typical Performance Curves



Typical Waveforms Cin=C2=C3=Cout=1uF, lout=120mA

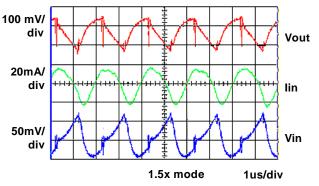


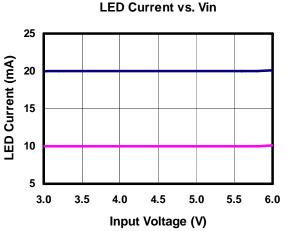




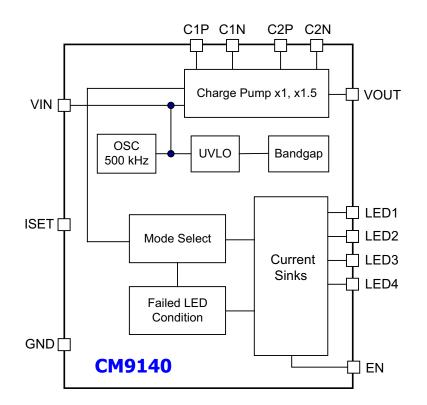
Typical Waveforms

Cin=C2=C3=Cout=1uF, lout=120mA





Functional Block Diagram



Pin Descriptions

PIN DESCRIPTIONS				
LEAD(s)	NAME	DESCRIPTION		
1	LED1	Cathode of LED1 pin.		
2	C1P	This pin is the plus side of charge pump bucket capacitor C1. Connect a 1.0μ F ceramic capacitor with a voltage rating of 10V or greater between C1N and C1P.		
3	VIN	Positive supply voltage input pin. This voltage should be between 2.9V and 6V. This pin requires a 1.0μ F or larger ceramic capacitor to ground.		
4	ISET	Enable pin and Current set pin for drivers, active low. To set the LED current, a resistor, R_{SET} , is connected between this pin and ground. The regulated LED current is 1000x the current flowing in R_{SET} , and is approximately: $I_{LED} = \frac{0.66V - (LogicLow)}{R_{SET}} \times 1000$ If this resistor is tied to directly ground (and enable function not used) Logic Low = 0, otherwise subtract the voltage drop of the device that drives this pin low.		

Pin Descriptions (cont'd)

PIN DESCRIPTIONS				
5	NC			
6	NC			
7	NC			
8	C2N	This pin is the minus side of charge pump bucket capacitor C2. Connect a 1.0μ F ceramic capacitor between C2N and C2P.		
9	EN	PWM/Analog input pin. Can be used as second Enable pin, active high. Should tied high when not used.		
10	GND	Ground terminal pin.		
11	C1N	This pin is the minus side of charge pump bucket capacitor C1. Connect a 1.0μ F ceramic capacitor between C1N and C1P.		
12	LED4	Cathode of LED4 pin.		
13	C2P	This pin is the plus side of charge pump bucket capacitor C2. Connect a 1.0μ F ceramic capacitor between C2N and C2P.		
14	LED3	Cathode of LED3 pin.		
15	VOUT	Charge pump output voltage pin, which connects to the anodes of all LEDs. A 1μ F capacitor to ground is recommended.		
16	LED2	Cathode of LED2 pin.		

Application Information

The CM9140 is a switched capacitor, charge pump voltage converter ideally suited for driving white LEDs to backlight LCD color displays in portable devices. The CM9140 charge pump is the perfect driver for portable applications such as cellular phones, digital still cameras, PDAs and any application where small space, compact overall size, low system cost and minimal EMI are critical.

The CM9140 requires only two external switched (bucket) capacitors, plus an input and an output capacitor, providing for a compact, low profile design. In many applications, these can all be conveniently the same value of 1.0μ F, available in a compact 0805 surface mount package.

The adaptive conversion ratio selects the most efficient operating mode. When V_{IN} is higher than the needed V_{OUT} ($V_{LED}+V_{CURRENT_SINK}$), the 1x mode is set. When the input voltage is below the LED forward voltage and a voltage boost is needed, the 1.5x mode is automatically selected. The 1.5x mode uses a frac-

tional charge pump to convert the nominal Li-ion battery voltage (3.6V) by 1.5 times and regulates the LED current to the low dropout current sources.

The current regulated sources maintain constant LED drive in the presence of supply voltage fluctuations. All LEDs are driven with the same current even when they have slightly different forward voltages. The individual current sources sense the current through each LED and match this current to less than 2% for uniform brightness across the color LCD display.

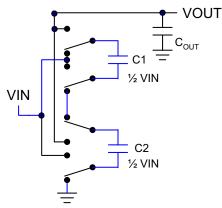
The CM9140 drives up to four WLEDS. The maximum current programmed by R_{SET} determines the maximum intensity; the display can be further dimmed by PWM control applied to its EN pin.

CM9140 Operation

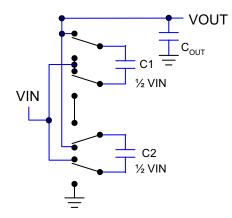
When a voltage is applied to the VIN pin, the CM9140 initiates a softstart cycle, typically lasting 100 μ S. Softstart limits the inrush current while the output capaci-

tors are charged. Following softstart, the CM9140 next determines the best conversion ratio (1x or 1.5x).

The 1.5x mode employs a fractional charge pump. The charge pump uses two phases from the internal oscillator to drive switches that are connected to the bucket capacitors, C1 and C2, as shown in Figure 1. In the first switch position, the bucket capacitors are connected in series and each are charged from VIN to a voltage of V_{IN}/2. The next phase changes the switch positions so that C1 and C2 are in parallel, and places them on top of VIN. The resulting voltage across C_{OUT} is then V_{IN}+1/2V_{IN} = 1.5 x V_{IN}.



Charge C1 and C2 to ½ VIN each



Transfer 1/2 VIN charge to top of VIN



The CM9140 has over-temperature and over-current protection circuitry to limit device stress and failure during short circuit conditions. An overcurrent condition will limit the output current (approximately 400~600mA)

and will cause the output voltage to drop, until automatically resetting after removal of the excessive current. Over-temperature protection disables the IC when the junction is about 135 °C, and automatically turns on the IC when the junction temperature drops by approximately 15 °C.

Efficiency

A conventional charge pump with a fixed gain of 2x will usually develop more voltage than is needed to drive paralleled white LEDs from Li-Ion sources. This excessive gain develops a higher internal voltage, reducing the system efficiency and increasing battery drain in portable devices. A fractional charge pump with a gain of 1.5x is better suited for driving white LEDs in these applications.

The CM9140 charge pump automatically switches between the two conversion gains, 1x and 1.5x, allowing high efficiency levels over a wide operating input voltage range. The 1x mode allows the voltage to pass directly through to the output when sufficient input voltage is available. As the battery discharges to the point where any one current source no longer has sufficient voltage headroom to maintain a constant current regulation, the 1.5x charge pump is enabled.

At nominal loads, the switching losses and quiescent current are negligible. If these losses are ignored for simplicity, the efficiency, η , for an ideal 1.5x charge pump can be expressed as the output power divided by the input power:

$$\eta = \frac{P_{LED}}{P_{IN}}$$

For an ideal 1.5x charge pump, I_{IN} 1.5 x I_{OUT} , and the efficiency may be expressed as;

$$V_{OUT} = (V_{LED} + V_{CURRENT_SINK})$$

$$\frac{P_{LED}}{P_{IN}} \approx \left(\frac{(V_{OUT}) \times I_{OUT}}{V_{IN} \times 1.5 \times I_{OUT}}\right) = \frac{V_{OUT}}{1.5 \times V_{IN}}$$

For
$$(V_{LED} + V_{CURRENT_SINK}) = 3.9V$$
, $\eta \approx \frac{3.9V}{1.5 \times V_{IN}}$

Many charge pumps are fixed 2x designs. The ideal 2x charge pump efficiency can be similarly expressed;

$$\frac{P_{OUT}}{P_{IN}} \approx \frac{3.9V}{2.0 \times V_{IN}}$$

In 1x mode, when the input voltage is above the output voltage, the ideal efficiency is simply V_{OUT}/V_{IN} .

The typical conversion efficiency plots for these modes, with some losses, are shown in Figure 2.

VLED=3.5V 90 1X Efficiency (%) 75 – 1X-1.5X dual mode 60 45 1.5X 2X 30 3.0 3.5 4.0 4.5 5.0 5.5 6.0 Input Voltage (V)

Figure 2. Ideal charge pump efficiency

As can be seen, the CM9140, with 1x and 1.5x modes, has better efficiency in this application than a fixed 2x charge pump. At low battery voltages, the higher efficiency of the CM9140 charge pump's 1,5x gain reduces the battery drain. At higher input voltages, typically seen when the system is running off an AC adapter, the CM9140, operating the 1x mode, has better efficiency than single mode 1.5x or 2x charge pumps, lowering the power dissipation for cooler circuit operation and long life.

While the charge pump efficiency is easily determined, the system efficiency is more difficult due to the current source outputs, which complicate measuring the output power. The forward voltage of the white LEDs will vary, and the constant current sources will adjust to maintain the current. When comparing systems, it is best to compare the input current for a specified LED drive current. The 1x mode has better efficiency than the 1.5x mode. Selecting LEDs with low forward voltage (V_{LED}) increases the time spent in the 1x mode as the battery discharges, extending the operating time.

Failed LED Detection

If a LED is shorted, the CM9140 will continue to operate and drive the remaining LEDs at the programmed current. If a LED opens, the other LEDs will still be regulated at the programmed current.

LED Current Set (ISET)

An external resistor programs a reference current, setting the maximum driver current. This resistor must be tied to a good analog ground. If it is pulled to ground through a switch, for example, from the host controller output, the voltage drop across that switch should not exceed 10mV.

The voltage at the ISET pin is provided by a .66V bandgap reference. The LED current is approximately 1000x the current set by the R_{SET} resistor, according to the following formula:

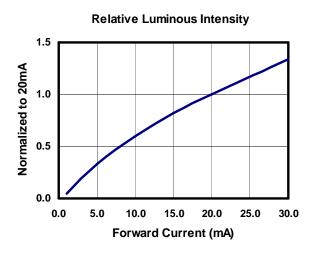
$$R_{SET} = \frac{0.66V - (LogicLow)}{I_{LED}} \times 1000$$

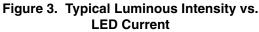
Logic Low is the voltage on device driving this pin to ground. If the resistor is tied to ground directly, Logic low = 0. For 20mA LED current, $R_{SET} \cong 33$ k. When this pin is driven high or open, the device will enter a sleep mode with $V_{OUT} = 4.5V$ and, with no load, $I_{QUIESCENT} \cong 500\mu$ A.

Analog Control of Display Intensity

Typically, portable devices control the backlight display intensity in response to ambient light conditions, or lower the intensity after a short standby interval to converse battery charge. The luminous intensity of white LEDs is proportional to the amount of forward current through them, but the color wavelength emitted is also dependent upon the forward current. In applications where color shift is not critical, brightness can be controlled by adjusting the diode's current. A typical white LED Intensity vs. forward current curve is shown in Figure 3.

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The ISET pins of the CM9140 can be used to connect an analog DC signal for analog dimming of the white LEDs, as shown in Figure 4 This requires an additional resistor, R, and a DC source voltage, Vc.

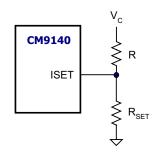


Figure 4. Analog LED current adjust

A control voltage, V_C , applied to the resistor divider will decrease the current for all LEDs. The maximum LED current occurs with 0V on V_C , which is set by R_P is the parallel combination of R and R_{SET} .

$$R_{\rm P} = \frac{0.66 \rm V}{\rm I_{\rm LED} max} \times 1000$$

Choose the maximum control voltage, V_C , which sets zero LED current, and then determine the resistor ratio.

Ratio =
$$\frac{0.66V}{Vc - 0.66V}$$

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The resistors can be determined from the equations below.

$$R = \frac{(R \times Ratio) + Rp}{Ratio}$$

Rset = Ratio \times R

For example, a V_C max of 2.5V and a maximum current setting of 20mA, R=125k, R_{SET} =44.8k. Figure 5 shows the control curve.

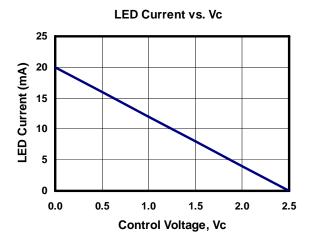


Figure 5. LED Current Control Curve

The circuit in Figure 6 is an example of logic dimming control, which changes the LED forward current in discrete steps. The NMOS source is an open drain (or open collector if bipolar) device, either the output of a host controller, or a discrete device. Open drain, or open collector devices sink current in their active, low voltage state (logic 0), and are high impedance in their high voltage, non-active state (logic 1). The open drain must not be pulled high with an external resistor, but instead connected only to the current setting resistors.

The parallel combination of R and R_{SET} determine the full intensity current. When the drain goes high, R_{SET} determines the lower intensity current.

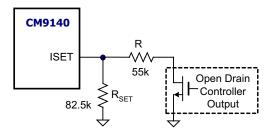
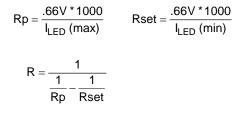


Figure 6. Logic Signal Dimming

For example, to reduce the luminosity intensity by half, using the LED curve from Figure 3, the current setting needs to be changed from 20mA to about 8mA. The values in Figure 6 will accomplish this, are where obtained using the following equations;



Additional parallel resistors can be added in the same way.

PWM Control of Display Intensity

Typically, portable devices control the backlight display intensity in response to ambient light conditions, or lower the intensity after a short standby interval to converse battery charge. The CM9140 allows the output to lower the LED brightness by applying a pulsing (PWM) signal to EN, as shown in Figure 7. The waveforms are shown in Figure 8.

The white in white LEDs is typically bichromatic, produced by a blue or UV LED that excites yellow phosphors. The two colors combine and the human eye sees these them as white light. The forward current of the LED influences the chromaticity, with higher LED current increasing the blue content of the color.

Using a PWM signal allows the LEDs to be dimmed without substantially shifting their color balance due to chromaticity shifts related to changing white LED forward current. The PWM signal causes the LEDs to operate either at the full ISET current, or at zero current. Only the time averaged current changes. Above a minimum frequency, the human eye will perceive the change in duty cycle as a change in brightness.

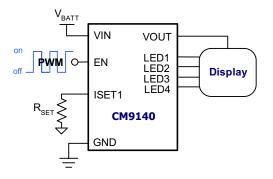


Figure 7. PWM applied to EN

The recommended frequency is between 100 Hz and 200 Hz, with a duty cycle greater than 20%. If a frequency of less then 100 Hz is used, flicker might be seen in the LEDs. The frequency should also be greater than the refresh rate of the TFT display. Higher frequencies will cause a loss of brightness control linearity. In addition, higher frequency can cause chromaticity shifts because the fixed rise and fall times of the PWM signal will shift the forward current.

The PWM signal will cause the average LED current to be reduced. The average current is determined by the PWM duty cycle, which can vary from 0% to 100%. Decreased Duty Cycle will linearly lower LED brightness, 0% Duty Cycle will turn off the display LEDs.

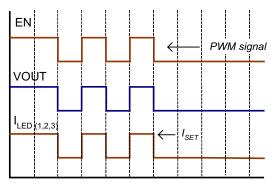


Figure 8. PWM Signal Dimming

Application Information (cont'd) CM9140 Design Examples Cell Phone

Some mobile phone LCD displays (both STN and mini-TFT) use white LEDs for backlighting. Lightguides are used to distribute the light uniformly behind the LCD. A typical application is shown in Figure 9. The display's intensity can be lowered by a PWM signal applied to the EN pin, as determine by the ambient light conditions.

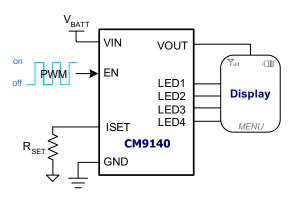


Figure 9. Display Backlight

Camera Flash

The CM9140 can support a camera flash in digital still cameras as well as in camera equipped smart phones and PDAs. In this case the flash LEDs are supplied 3 x 50 mA = 150 mA. See Figure 10.

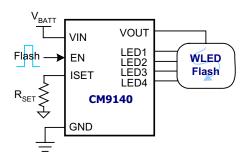


Figure 10. Flash Application

Capacitor Selection

For proper performance, use surface-mount, low ESR ceramic capacitors for all four positions. X7R or X5R ceramic dielectric provides good stability over the operating temperature and voltage range.

The capacitance and ESR of the external bucket capacitors will directly affect the output impedance and efficiency of the converter. A ceramic 1μ F capacitor is recommended.

Reflected input ripple depends on the impedance of the V_{IN} source, such as the PCB traces and the Li-ion battery, which have elevated impedance at higher frequencies. The input capacitor located near the converter input reduces this source impedance and ripple. Any ESR from the capacitor will result in steps and spikes in the ripple waveform, and possibly produce EMI. Much of the ripple voltage is due to moving current charge in and out of the capacitor and the capacitor's impedance at the charge pump frequency. If ripple voltage or current on the battery bus is an application issue, add a small input inductor between the battery and the capacitor, or just increase the capacitor.

For a given output current, increasing the output capacitance reduces output ripple in the 1.5x mode. Increasing the output capacitor will also increase startup current and time. In most LED applications, high frequency output ripple is not a concern because it will not cause intensity variations that are visible to the human eye.

Layout Guide

The charge pump is rapidly charging and discharging the external capacitors, so external traces to the capacitors should be made wide and short to minimize inductance and high frequency ringing. The four capacitors should be located as close as practical to the charge pump, particularly C1 and C2, which have the highest dv/dt. Use a solid ground plane, and connect the ground side of C_{IN} , C_{OUT} and the package GND as close as practical.

CM9140

Mechanical Details

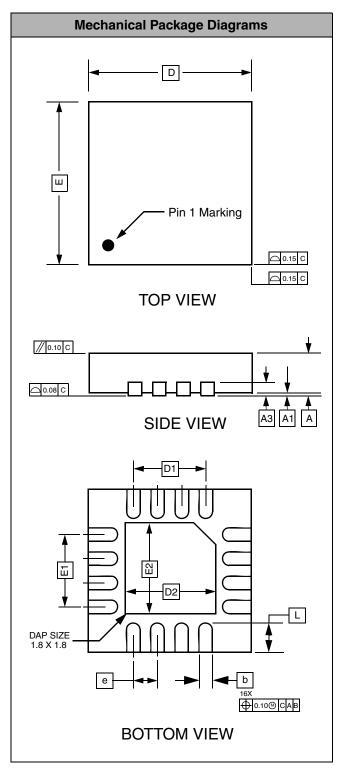
TQFN-16 Mechanical Specifications

The CM9140 is supplied in a 16-lead, 4.0mm x 4.0mm TQFN package. Dimensions are presented below.

For complete information on the TQFN16, see the California Micro Devices TQFN Package Information document.

	PAC	KAGE	DIME	NSIO	NS		
Package	TQFN-16 (4x4)						
Leads			1	16			
Dim.	Millimeters			Inches			
	Min	Nom	Max	Min	Nom	Max	
Α		0.80	0.84		0.031	0.033	
A1	0.00		0.04	0.00		0.002	
A3	0.20 REF			.008			
b	0.25		0.33	0.010		0.013	
D	4.0 BSC			0.157			
D1		1.95 RE	F		0.077		
D2	2.05		2.15	0.081		0.085	
E	4.0 BSC			0.157			
E1		1.95 RE	F	0.077			
E2	2.05		2.15	0.081	0.081 0		
е		0.65 TYI	Р.	0.026			
L	0.55		0.65	0.022		0.026	
# per tube	xx pieces*						
# per tape and reel	xxxx pieces						
Controlling dimension: millimeters							

* This is an approximate number which may vary.



Package Dimensions for 16-Lead TQFN