

CM8560

3A SINK & SOURCE ADJUSTABLE LINEAR BUS TERMINATOR

GENERAL DESCRIPTION

The CM8560 is a low cost linear regulator designed to provide a desired output voltage or termination voltage for various applications by converting voltage supplies ranging from 1.6V to 6.0V. The desired output voltage could be programmable by two external voltage divider resistors.

The CM8560 is capable of sourcing or sinking up to 3A of current while regulating an output VOUT voltage to within 2% (DDR-I), 3% (DDR-II) or less.

The CM8560 provides low profile 5-lead TO-263 package to save system space.

FEATURES

- Ideal for DDR-I and DDR-II
- 5-Lead TO-263 packages
- Source and sink up to 3A, no heat sink required
- Integrated power MOSFETs
- Programmable output voltage by external resistors
- Output voltage could go down to 0.6V
- Iccq at VCCA less than 500uA
- Current limit protection and Short Circuit protection
- Thermal shutdown protection
- Shutdown for standby or suspend mode operation
- Minimum external components

APPLICATIONS

- Mother Board
- PCI/AGP Graphics
- Game/ Play Station
- Set Top Box

PIN CONFIGURATION

- ♦ IPC
- SCSI-III Bus terminator

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VCCA

REFEN

GND

νουτ

VIN



CM8560 3A Sink & Source Adjustable Linear Bus Terminator

PIN DESCRIPTION

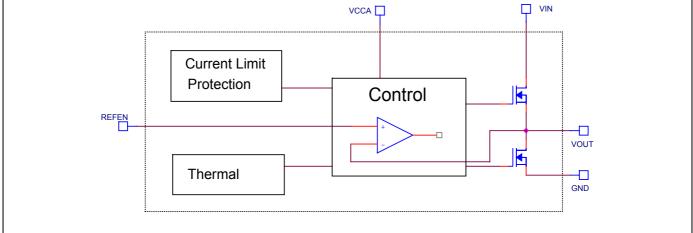
Pin No.	Symbol	Description		Operating Rating				
		Description	Min.	Тур.	Max.	Unit		
1	VIN	Input Power		2.5/1.8		V		
2	VOUT	Output Voltage			6	V		
3	GND	Ground						
4	REFEN	Reference Voltage Input and Chip Enable			VCCA-2.5	V		
5	VCCA	Voltage supply for internal circuits				V		

ORDERING INFORMATION

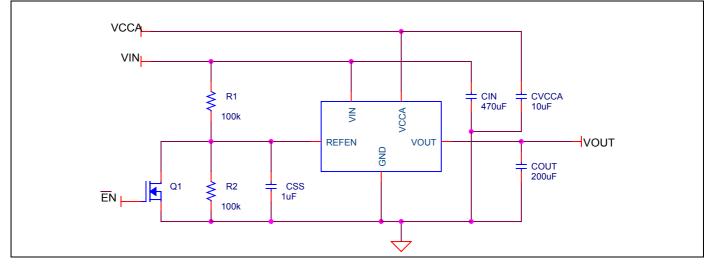
Part Number	Temperature Range	Package
CM8560IN263	-40℃ to 85℃	5-Lead TO-263 (N263)
CM8560GIN263*	-40℃ to 85℃	5-Lead TO-263 (N263)

*Note: Add suffix "G" for Pb Free Product

BLOCK DIAGRAM



APPLICATION CIRCUITS





ABSOLUTE MAXIMUM RATINGS

ELECTRICAL CHARACTERISTICS (Unless otherwise stated, these specifications apply T_A=25°C; VIN=+2.5V and VCCA=+3.3V, VREFEN=1.25V) maximum ratings are stress ratings only and functional device operation is not implied. (Note 1)

	D		CM8560				
Symbol	Parameter	Test Conditions	Min.	Typ. Max.		Unit	
Vos	Output Offset Voltage	I _{OUT} =0A (Note 2)	-20		20	mV	
I _{OP}	Operating Current at VIN	No load, Cout=200uF			1	mA	
	Load Regulation (DDR I/II)	I _L : 0A -> 3A		0.8/1.2	2/3	%	
$ \Delta V_{LOAD} $		I _L : 0A -> -3A		0.8/1.2	2/3	%	
I _{CCQ}	Quiescent Current at V _{CCA}	At Room Temp.		190	230	μA	
I _{SHDN}	Current in Shutdown Mode	REFEN< $0.2V$, R _L = 10 Ohm		90	110	μA	
V _{IN}	Input Voltage Range (Note 3)	No Load	1.35/1	2.5/1.8	6	V	
V _{CCA}	Input Voltage Range (Note 3)	R _L = 10 Ohm	3.75		6	V	
SHORT CIRC	UIT PROTECTION						
I _{LIMIT}	Current Limit			5		А	
I _{SC,VIN}	Short Current	Sinking	2			А	
I _{SC,GND}	Short Current	Sourcing	3			А	
OVER THERM	IAL PROTECTION						
THSD	Thermal Shutdown Temperature	3.75V<=VCCA<=6V	125	150		°C	
	Thermal Shutdown Hysteresis		25	30	35	°C	
REFEN FUNC	TION						
	REFEN Threshold	VREFEN < VIN VREFEN < VCCA – 2.5V	0.4	0.5	0.6	V	

Note 1: Limits are guaranteed by 100% testing, sampling, or correlation with worst case test conditions

Note 2: VOS = VREFEN – VOUT

Note 3: Keep VCCA >= VIN and VCCA >= VREFEN + 2.5V on operation power on and power off sequences

Note 4: Guaranteed by design, not 100% test



FUNCTIONAL DESCRIPTION

The CM8560 is a linear regulator that is capable of sinking and sourcing 3A of current without an external heat sink.

The CM8560 integrates power MOSFETs that are capable of source and sink 3A of current while maintaining excellent voltage regulation. The output voltage can be regulated within 3% or less by using the external feedback. Separate voltage supply inputs have been added to fit applications with various power supplies for the databus and power buses.

INPUTS

The input voltage pins (VIN) determine the output voltages (VOUT). At CM8560, the desired output voltage could be programmable by two external voltage divider resistors. VIN is suggested to connect to VDDQ of memory module for better tracking with memory VDDQ.

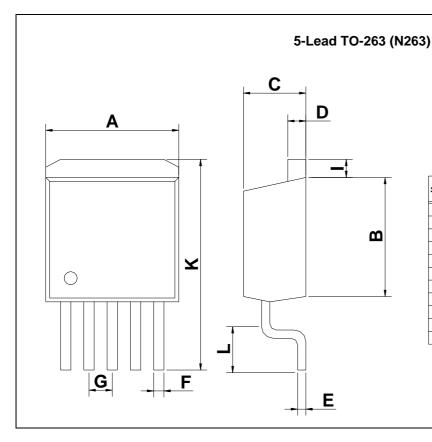
OTHER SUPPLY VOLTAGES

VCCA provide the voltage supply to the logic section and internal error amplifiers of CM8560.

OUPUTS

The output voltage pins (VOUT) are tied to the databus, address, or clock lines via an external inductor. Output voltage is determined by the VIN.

PACKAGE DIMENSION



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SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	10.03		10.67	0.395		0.420	
В	8.25		9.17	0.325		0.361	
С	4.34		4.59	0.171		0.181	
D	1.14		1.40	0.045		0.055	
Е	0.33		0.432	0.013		0.017	
F	0.737		0.889	0.029		0.035	
G	1.57		1.83	0.062		0.072	
Ι			1.65			0.065	
К	14.60		16.13	0.575		0.635	
L	2.29		2.79	0.090		0.110	
v	1.14		1.40	0.045		0.055	



IMPORTANT NOTICE

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