

# PACIFIC DISPLAY DEVICES

## **LCD Component Data Sheet**

Model Number: 20210

20 Character by 2 Line Alphanumeric LCD Assembly With Embedded Controller

#### **CONTENTS**

1.	GENERAL INFORMATION	
	1.1 Product Overview	2
	1.2 Part Numbering System	2
	1.3 Absolute Maximum Ratings	3
	1.4 Circuit Block Diagram	3
	1.5 Mechanical Characteristics	3
	1.6 Input Signal Function	4
	1.7 LCM Contrast Control and Bias	4
	1.8 LCD Dimensions	5
2.	ELECTRICAL / OPTICAL CHARACTERISTICS	
	2.1 DC Electrical Characteristics	6
	2.2 AC Electrical Characteristics	6
	2.3 Optical Characteristics	8
	2.4 LED Backlight Characteristics	10
	2.5 EL Panel Backlight Characteristics	10
3.	OPERATING PRINCIPALS AND METHODS	
	3.1 LCD Controller Display and Control Functions	11
	3.2 Display Data RAM Address Map	12
4.	RELIABILITY	14
5.	PRECAUTIONS FOR USING LCD MODULES	15

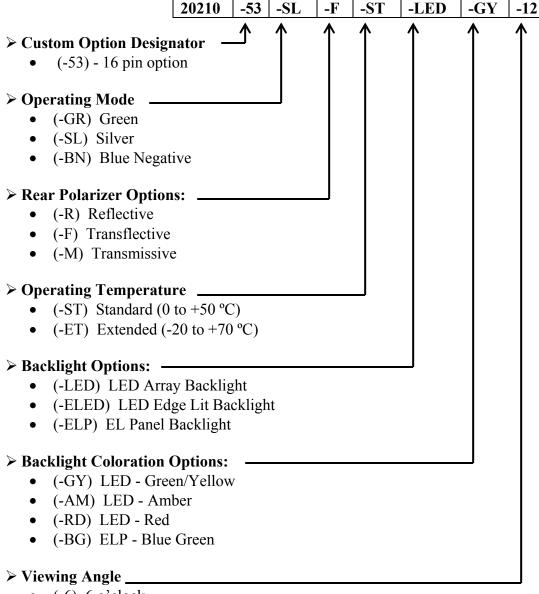
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#### 1. GENERAL INFORMATION

#### 1.1 Product Overview

- 20 Character x 2 line Alphanumeric Dot Matrix LCD Module
- LCD Controller: Embedded S6A0069 or equivalent alpha-numeric controller
- Multiplexing driving: 1/16 duty, 1/4 bias
- Operating Mode: Super Twisted Nematic (STN) technology
- LCD Module Service Life: 100,000 hours minimum

#### 1.2 Part Numbering System

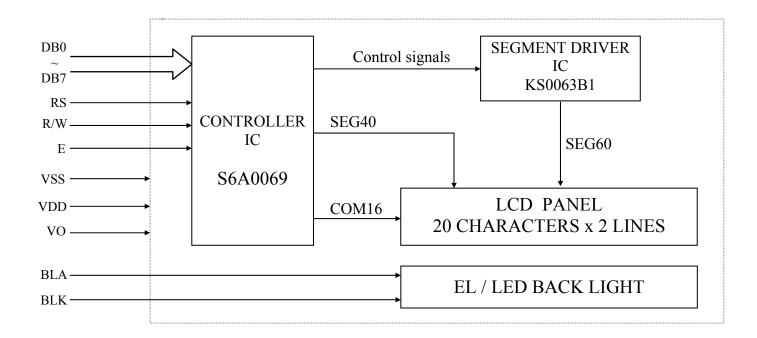


- (-6) 6 o'clock
- (-12) 12 0'clock

## 1.3 Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	$ m V_{DD}$	-0.3	7.0	V
Supply voltage for LCD	$V_{DD} - V0$		$V_{DD} + 0.3$	V
Input voltage	VI	-0.3	$V_{DD} + 0.3$	V
Standard Operating temperature	TOP (-ST)	0	50	°C
Standard Storage temperature	TST (-ST)	-10	60	°C
Extended Operating temperature	TOP (-ET)	-20	70	°C
Extended Storage temperature	TST (-ET)	-30	80	°C
Soldering Temp	Tsolder	20	60	°C

## 1.4 Circuit Block Diagram



#### 1.5 Mechanical Characteristics

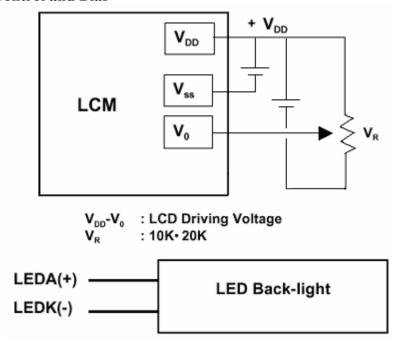
Item	Contents	Unit
Module size (W×H×T)	180 x 40.0 x 15.0 Max (w/ LED Backlight)	mm
	180 x 40.0 x 9.5 Max (w/ ELP or No LED Backlight)	
Viewing area (W×H)	149.0 x 23.0	mm
Active area (W×H)	142.80 x 20.64	mm
Character matrix (W×H)	5 × 8	dots
Character size (W×H)	$6.00 \times 9.66$	mm
Dot size (W×H)	$1.12 \times 1.12$	mm
Dot pitch (W×H)	$1.22 \times 1.22$	mm

## 1.6 Input Signal Function

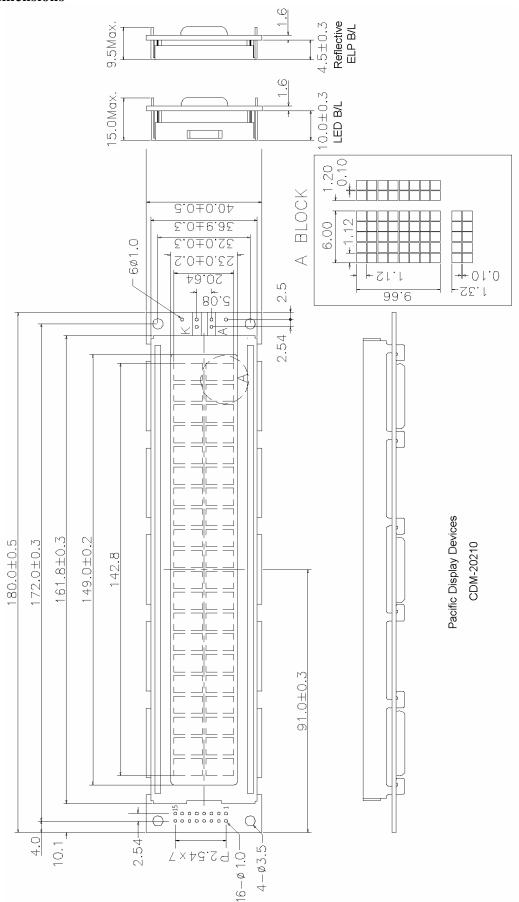
Pin NO.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply voltage for logic
3	VO		Input voltage for LCD
4	RS	H/L	H: Data signal, L: Instruction signal
5	R/W	H/L	H: Read mode, L: Write mode
6	Е	$H, H \rightarrow L$	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15*	BLA	4.2V	Back light anode
16*	BLK	0V	Back light cathode

<sup>\*</sup> These pins are only available on the -53 option

#### 1.7 LCM Contrast Control and Bias



#### 1.8 LCM Dimensions



#### MODEL NO: 20210

#### 2. ELECTRICAL / OPTICAL CHARACTERISTICS

## **2.1 DC Electrical Characteristics** $(V_{DD} = +5V\pm10\%, V_{SS} = 0V, Ta = 25^{\circ}C)$

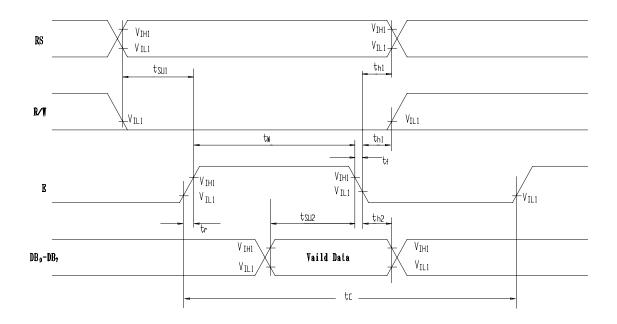
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Supply voltage for logic	$V_{ m DD}$		4.5	5.0	5.5	V
Supply current for logic	$I_{\mathrm{DD}}$			1.4	5	mA
		0°C	4.35	4.55	4.75	V
Operating voltage for LCD*	$V_{\mathrm{DD}}$ - $V_{\mathrm{O}}$	25°C	4.10	4.30	4.50	V
		50°C	3.65	3.85	4.00	V
Input voltage 'H' level	$V_{ m IH}$		VDD - 2.2		VDD	V
Input voltage ' L ' level	$V_{ m IL}$		0		0.8	V

<sup>\*</sup>Note: -ET temp range will require up to 8V biasing and require a minimum external voltage supply of -3V @-20°C.

#### 2.2 AC Electrical Characteristics

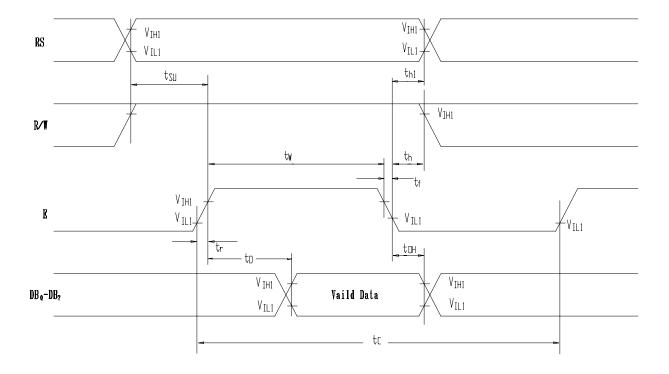
#### • Write Mode

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test pin
E cycle time	$t_{\rm C}$	500			ns	Е
E rise time	$t_{\rm r}$			25	ns	Е
E fall time	$t_{ m f}$			25	ns	Е
E pulse width (High, Low)	$t_{\mathrm{W}}$	220			ns	Е
R/W and RS set-up time	$t_{\mathrm{SU1}}$	40			ns	R/W, RS
R/W and RS hold time	$t_{h1}$	10			ns	R/W, RS
Data set-up time	$t_{ m SU2}$	60			ns	$DB_0 \sim DB_7$
Data hold time	$t_{h2}$	10			ns	$DB_0 \sim DB_7$



## • Read Mode

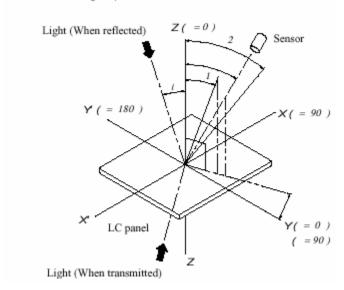
Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test pin
E cycle time	$t_{\mathrm{C}}$	500			ns	Е
E rise time	$t_{\rm r}$			25	ns	Е
E fall time	$t_{\mathrm{f}}$			25	ns	Е
E pulse width	$t_{ m W}$	220			ns	Е
R/W and RS set-up time	$t_{ m SU}$	40			ns	R/W, RS
R/W and RS hold time	$t_{\rm h}$	10			ns	R/W, RS
Data output delay time	$t_{\mathrm{D}}$			120	ns	$DB_0 \sim DB_7$
Data hold time	$t_{ m DH}$	20			ns	$DB_0 \sim DB_7$



## **2.3 Optical Characteristics** $(V_{OP} = 4.7V, Ta = 25^{\circ}C)$

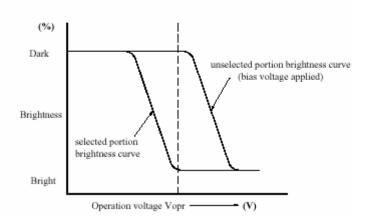
Item	Symbol	Condition	Min	Тур	Max	Unit
Response time	Tr			100	200	ms
	Tf			210	340	ms
Frame Frequency	fFRM			60		Hz
Contrast ratio	Cr			7		
Viewing angle range	θ1	25° C	50		130	Deg
Viewing angle range	θ2	25° C	30		30	Deg

#### \* Definition of angles $\varphi$ and $\theta$

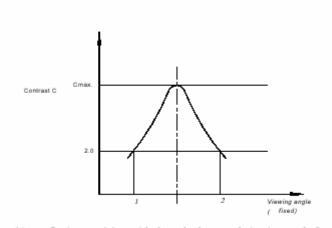


#### \*Definition of contrast C

$$C = \frac{B1}{B2} = \frac{\text{Brightness of selected portion}}{\text{Brightness of unselected portion}}$$

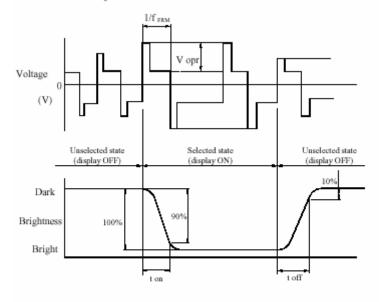


#### \* Definition of viewing angles $\theta 1$ and $\theta 2$



Note : Optimum vision with the naked eye and viewing angle  $\boldsymbol{\theta}$  at Cmax above are not always the same.

#### \* Definition of response time



Vopr : Operating voltage (V)

ton: Response time (rsie) (ms)

fFRM: Frame frequency (Hz)

toff: Response time (fall) (ms)

#### MODEL NO: 20210

## 2.4 LED Backlight Characteristics

### ■ Yellow-Green LED Operating Characteristics (5V - Array Lit)

Item	Symbol	Conditions		Standard			
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	$V_{\mathrm{f}}$	Ta= 25 °C	3.8	4.1	4.4	VDC	
Forward Current	$I_F$	Ta= 25 °C		500	711	mA	

#### ■ Life (Array or Edge Lit)

Ite	m	Conditions	Stan	dard	Unit
Ite	Ш	Conditions	Min.	Max.	Unit
Lit	e e	Ta= 25 °C	100,000		hrs

## 2.5 EL Panel Backlight Characteristics

## ■ Blue-Green EL Panel Operating Characteristics,

Item	Conditions		Standard					
Item	Conditions	Min.	Тур.	Max.	Unit			
Current Consumption	100VAC RMS, 400Hz Ta: 25°C		9.2	24.0	mA			
EL Drive Voltage			100	150	VAC RMS			
EL Drive Frequency			400	1000	Hz			
Operating Temperature		-35°C		+50°C	°C			
Storage Temperature		-40°C		+60°C	°C			
Luminance	100VAC RMS, 400Hz	40	50		cd/m <sup>2</sup>			
Luminance Half-Life	Ta: 25°C	3500			Hrs			

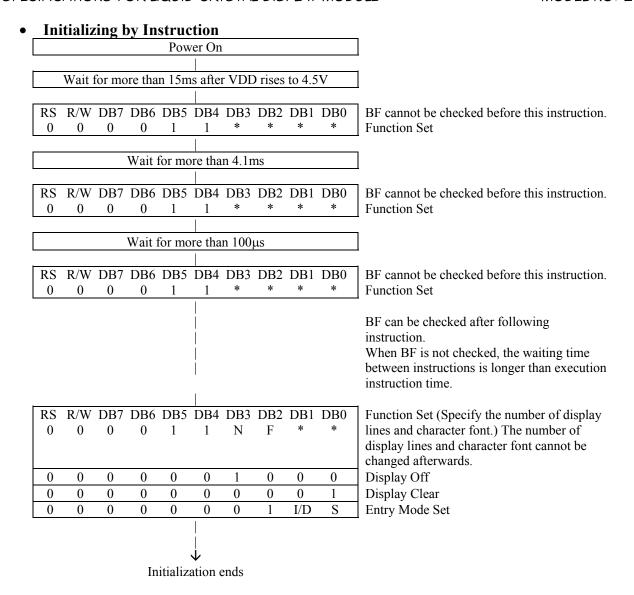
<sup>\*</sup>Note: half life is defined as Luminance being reduced by 50%

## 3. OPERATING PRINCIPALS AND METHODES

• Control and Display Command

Command	• Control	ana i	Dispie	ij Co								Exe Time	
Return Home		RS	R/W	DB <sub>7</sub>		DB <sub>5</sub>	DB <sub>4</sub>	DB <sub>3</sub>	DB <sub>2</sub>	$DB_1$		f=250khz	Remark
Eatry Mode Set													
Display On/Off													
Display On Off	Entry Mode Set	L	L	L	L	L	L	L	Н	I/D	SH	42µs	• I/D : Set cursor move direction
$Shift = \begin{array}{c c c c c c c c c c c c c c c c c c c $													
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$													
Display On 'Off													
Display On/Off													SH
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$													
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Display On/Off	L	L	L	L	L	L	Н	D	С	В	42μs	• Display
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$ \begin{array}{c c c c c c c c c c c c c c c c c c c $													• Cursor
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Shift													L Cursor off
Shift $\begin{bmatrix} L \\ L \end{bmatrix} \begin{bmatrix} L \\ L $													
Shift L L L L H S/C R/L X X 42 $\mu$ s													
Set Function  L L L L H DL N F X X 42μs  L H 8 bits interface  DL L 4 bits interface  L L 4 bits interface  L L 1 line display  L 1 line display  L 1 line display  Set Cg Ram Address  Set Cg Ram Address L L L H DD RAM address  42μs  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  Read Busy Flag & Address  CG RAM address  42μs  DD RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  Read Busy Flag & Address  CG RAM address  42μs  DD RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  PREAD DEAL READ DEAL R													L Blinking off
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Set Function  L L L H DL N F X X 42μs  H 8 bits interface DL L 4 bits interface  L 1 line display  L l line display  L l line display  Explain the setting sent and received after this setting  RAD DR AM address  Set Dd Ram Address Read Busy Flag & Address Address  Read Busy Flag & Address Address  L H BF Address Counter used for both DD & CG RAM address  Address  Address  Oμs  BF H Busy  Ready  Ready  Reads BF indication internal operating is being performed a Reads addr counter contents													
Set Function  L L L H DL N F X X 42μs  H 8 bits interface DL L 4 bits interface  L 1 line display  L l line display  L l line display  Explain the setting sent and received after this setting  RAD DR AM address  Set Dd Ram Address Read Busy Flag & Address Address  Read Busy Flag & Address Address  L H BF Address Counter used for both DD & CG RAM address  Address  Address  Oμs  BF H Busy  Ready  Ready  Reads BF indication internal operating is being performed a Reads addr counter contents													H Right shift
Set Function  L L L L H DL N F X X 42μs  L 4 bits interface  L 4 bits interface  L 4 bits interface  L 1 line display  L 1 line display  L 1 line display  L 5 X 7 dots  Set Cg Ram Address  L L L H CG RAM address (corresponds to cursor address)  Set Dd Ram Address  Read Busy Flag & Address  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM address  Read Busy Flag & Address  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM address  Page 4  DD RAM address  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RAM Data is sent and received after this setting  CG RA													R/L
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Set Cg Ram Address  L L L H CG RAM address (corresponds to cursor address)  Set Dd Ram Address Read Busy Flag & Address  L H BF Address Counter used for both DD & CG RAM address  Address  Read Busy Flag & Address  CG RAM Data is sent and received after this setting  DD RAM Data is sent and received after this setting  Physical Read Busy Flag & Address Counter used for both DD & CG RAM address  Oμs  F L BF Address Counter used for both DD & CG RAM address  Address  Oμs  F L Ready  Reads BF indication internal operating is being performed - Reads addr counter contents													
Set Cg Ram Address  L L L H CG RAM address (corresponds to cursor address)  Set Dd Ram Address  Read Busy Flag & Address  L H BF Address Counter used for both DD & CG RAM address  Address  Read Busy Flag & Address  Address  BF H Busy  L Ready  Reads BF indication internal operating is being performed - Reads addr counter contents													H 5 X 10 dots
Set Cg Ram Address  L L L H CG RAM address (corresponds to cursor address)  Set Dd Ram Address  Read Busy Flag & Address  L H BF Address Counter used for both DD & CG RAM address  Oμs  H Busy  Ready  Reads BF indication internal operating is being performed - Reads addr counter contents													F
Set Dd Ram Address  Read Busy Flag & Address  Address  L H BF Address Counter used for both DD & CG RAM address  Address  DD RAM Data is sent and received after this setting  Oμs  H Busy  L Ready  Reads BF indication internal operating is being performed - Reads addr counter contents													E 3 A 7 dots
Set Dd Ram Address  Read Busy Flag & Address  L H BF Address Counter used for both DD & CG RAM address  Oμs L Ready  Reads BF indication internal operating is being performed  Reads addr counter contents	Set Cg Ram Address	L	L	L	Н							42μs	CG RAM Data is sent and received after this setting
Read Busy Flag & L H BF Address Counter used for both DD & CG RAM address  Oμs  H Busy  L Ready  - Reads BF indication internal operating is being performed - Reads addr counter contents		L	L	Н								42μs	DD RAM Data is sent and
Address BF indication internal operating is being performed — Reads addr counter contents	Read Busy Flag &	L	Н	BF								0μs	H Busy
Reads BF indication internal operating is being performed  Reads addr counter contents	/1001C33					00	, III DD 0	CORA	rivi audit	J. 33			
- Reads addr counter contents													- Reads BF indication internal
	Write Data	Н	L									46μs	Write data into DD or CG RAM
Read Data     H     H     Read Data     46μs     Read data from DD or CG RAM       X : Don't care		Н	Н				Read	Data				46μs	Read data from DD or CG RAM

X : Don't care



#### MODEL NO: 20210

## 3.2 Display Data RAM Address Map

Upper 4 Lower Bits 4 Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			0	อ	P		P				_	7	<b>≡</b> .	α	p
xxxx0001	(2)		I	1	A	Q	a	9			0	7	手	4	ä	q
xxxx0010	(3)		III	2	В	R	b	۳			r	1	IJ	×	ß	0
xxxx0011	(4)		#	3	C	5	C	S.			7	Ż	Ŧ	ŧ	W	8
ххжх0100	(5)		\$	4	D	T	d	t.				I	ŀ	þ	μ	Ω
хххх0101	(6)		7	5	E	U	e	u			•	7	<b>+</b>	1	σ	ü
ххжх0110	(7)		8.	6	F	Ų	f	V			P	Ħ	_	3	ρ	Σ
ххжх0111	(8)		7	7	G	W	9	W			7	#	Z	7	9	π
xxxx1000	(1)		(	8	H	X	h	X			4	7	末	IJ.	<b>.</b> ,	$\overline{\mathbf{x}}$
xxxx1001	(2)		)	9	I	Y	i	y			Ð	፞፞፞፞፞፞፞	J	ıЬ	7	J
xxxx1010	(3)		*		J	Z	j	Z			I		ι'n	ŀ	j	Ŧ
xxxx1011	(4)		+	•	K		k	{			7	<b>7</b>	L		×	Б
xxxx1100	(5)		7	<	L	¥	1				t	5)	J	7	4	Ħ
xxxx1101	(6)		_	=	М		M	}			ュ	Z	^	<u>_</u> ,	Ł	÷
xxxx1110	(7)			>	M	^	n	÷			3	t	<b>†</b>	*	ń	
xxxx1111	(8)		•	?	0	_	0	÷			ij	y	₹		Ö	

## **LCD Character Location map**

				-																
Characters	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
First line (H)	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10	11	12	13
Second line (H)	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53

## 4. RELIABILITY

		<b>Environmental Test</b>				
No ·	Test Item	Content of Test	Test Condition	Applicable Standard		
1	High temperature storage	Endurance test applying the high storage temperature for a long time.	80 °C 200 hrs			
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-30 °C 200 hrs			
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70 °C 200 hrs			
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	-20 °C 200 hrs			
5	High temperature / Humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	80 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023		
6	High temperature / Humidity operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	70 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023		
7	Temperature cycle	Endurance test applying the low and high temperature cycle.  -20°C 25°C 70°C 30min. 30min.	-20°C / 70°C 10 cycles			
		Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	$10\sim22$ Hz → 1.5mmp-p 22 $\sim500$ Hz → 1.5G Total 0.5hrs	MIL-202E-201A JIS-C5025 JIS-C7022-A-10		
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msedc 3 times of each direction	MIL-202E-213B		
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115 mbar 40 hrs	MIL-202E-105C		
		Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	$VS=800V$ , $RS=1.5$ k $\Omega$ $CS=100$ pF 1 time	MIL-883B- 3015.1		

<sup>\*\*\*</sup> Supply voltage for logic system = VDD. Supply voltage for LCD system = Operating voltage at 25°C

#### **LCD Panel Service Life**

#### **Definition of panel service life**

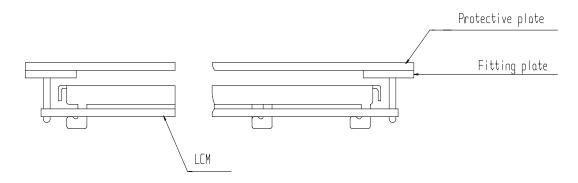
- 100,000 hours minimum at 25° C  $\pm$ 10%
- Contrast becomes 30% of initial value
- Current consumption becomes three times higher than initial value
- Remarkable alignment deterioration occurs in LCD cell layer
- Unusual operation occurs in display functions

#### 5. PRECAUTIONS FOR USING LCD MODULES

#### **Installing LCD Modules**

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ±0.1mm.

#### **Precaution for Handing LCD Modules**

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- 1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- 2) Do not alter, modify or change the shape of the tab on the metal frame.
- 3) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- 4) Do not damage or modify the pattern writing on the printed circuit board.
- 5) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- 6) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- 7) Do not drop, bend or twist LCM.

#### **Electro-Static Discharge Control**

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- 1) Make certain that you are grounded when handing LCM.
- 2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- 3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- 4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- 5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- 6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

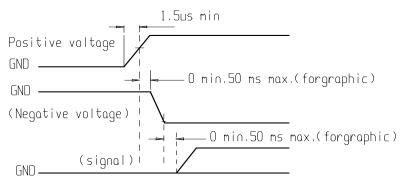
#### Precaution for soldering to the LCM

- 1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.
  - a) Soldering iron temperature :  $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$ .
  - b) Soldering time: 3-4 sec.
- 2) Solder: eutectic solder.

- MODEL NO: 20210
- 3) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 4) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 5) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### **Precautions for Operation**

- 1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- 2) Driving the LCD in the voltage above the limit shortens its life.
- 3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- 4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- 5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- 6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



#### Safety

• If the LCD panel breaks, be careful not to get the liquid crystal in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

#### Handling

- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.
- If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
- Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - o Water
  - Ketone
  - Aromatic solvents
- Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- Do not attempt to disassemble or process the LCD module.
- NC terminal should be open. Do not connect anything.
- If the logic circuit power is off, do not apply the input signals.

#### SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY MODULE

- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - o Be sure to ground the body when handling the LCD modules.
  - o Tools required for assembling, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
  - o The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### **Storage**

- When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps
- Store the module in a dark place where the temperature is  $25 \,^{\circ}\text{C} \pm 10 \,^{\circ}\text{C}$  and the humidity below 65% RH.
- Do not store the module near organic solvents or corrosive gases.
- Do not crush, shake, or jolt the module (including accessories).

#### **Cleaning**

- Do not wipe the polarizing plate with a dry cloth, as it may scratch the surface.
- Wipe the module gently with soft cloth soaked with a petroleum benzene.
- Do not use ketonic solvents (ketone and acetone) or aromatic solvents (toluene and xylene), as they may damage the polarizing plate.

#### Others:

- Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
  - Exposed area of the printed circuit board.
  - Terminal electrode sections.