



NPN COMPLEMENTARY SILICON HIGH VOLTAGE TRANSISTOR

CC5551 (9AW) TO-92 BCE

MARKING : NCC 5551

High Voltage NPN Transistor for General Purpose and Telephony Applications

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector -Emitter Voltage	V _{CEO}	160	V
Collector -Base Voltage	V _{CBO}	180	V
Emitter -Base Voltage	V _{EBO}	6.0	V
Collector Current Continuous	I _C	600	mA
Power Dissipation @Ta=25°C	P _D	625	mW
Derate Above 25°C		5.0	mw/°C
Power Dissipation @Tc=25°C	P _D	1.5	W
Derate Above 25°C		12	mw/°C
Junction Temperature	T _i	150	°C
Storage Temperature	T _{stq}	-55 to +150	°C
THERMAL RESISTANCE	U U		
Junction to Case	R _{th(j-c)}	125	°C/W
Junction to Ambient	R _{th(j-a) (1)}	357	°C/W

(1) $R_{th(j-a)}$ is measured with the device soldered into a typical printed circuit board

ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector -Emitter Voltage	V _{CEO}	I _C =1mA,I _B =0	160	-	-	V
Collector -Base Voltage	V _{CBO}	I _C =100μΑ.I _E =0	180	-	-	V
Emitter -Base Voltage	V_{EBO}	I _E =10μΑ, I _C =-0	6.0	-	-	V
Collector-Cut off Current	I _{CBO}	V_{CB} =160V, I _E =0	-	-	50	nA
		T _a =100°C				
		V _{CB} =160V, I _E =0	-	-	50	μA
Emitter-Cut off Current	I _{EBO}	V_{EB} =4V, I_{C} =0	-	-	50	nA
DC Current Gain	h _{FE*}	I _C =1mA,V _{CE} =5V	80	-	-	
		I _C =10mA,V _{CE} =5V	80	-	320	
		I _C =50mA,V _{CE} =5V	30	-	-	
Collector Emitter Saturation Voltage	V _{CE(Sat)} *	I _C =10mA,I _B =1mA	-	-	0.15	V
		I _C =50mA,I _B =5mA	-	-	0.2	V
Base Emitter Saturation Voltage	V _{BE(Sat)} *	I _C =10mA,I _B =1mA	-	-	1.0	V
		I _C =50mA,I _B =5mA	-	-	1.0	V

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ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Otherwise Specified)

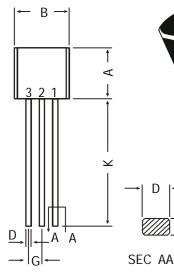
CC5551

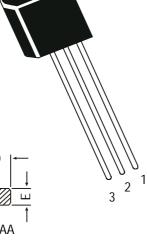
CC5551 (9AW)

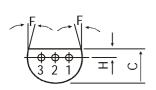
TO-92 BCE

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Dynamic Characteristics						
Small Signal Current Gain	h _{fe}	I _C =1mA, V _{CE} =10V f=1KHz	80	-	320	
Transition Frequency	f _t	V _{CE} =10V,I _C =10mA, f=100MHz	100	-	300	MHz
Output Capacitance	C _{ob}	V _{CB} =10V, I _E =0 f=1MHz	-	-	6.0	pF
Input Capacitance	C_{ib}	V _{EB} =0.5V, I _C =0 f=1MHz	-	-	20	pF
Noise Figure	N _F	V _{CE} =5V, I _C =250μA R _S =1kΩ, f=10Hz to 15.7kHz	-	-	8.0	dB









PIN CONFIGURATION

All diminsions in mm.

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER

DIM	MIN.	MAX.			
А	4.32	5.33			
В	4.45	5.20			
С	3.18	4.19			
D	0.41	0.55			
E	0.35	0.50			
F	5 DEG				
G	1.14	1.40			
Н	1.14	1.53			
Κ	12.70	_			

Ammo Pack Style MECHANICAL DATA Adhesive Tape on Top Side arrie ₫ĥ 4 FLAT SIDE LABEL wi t Ð ጠ Ē Æ 12 <u>t1</u> 7.77 Flat Side of Transistor and Adhesive Tape Visible 2000 pcs./Ammo Pack • Po

TO-92 Transistors on Tape and Ammo Pack

All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION				DEMADIZO	
	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS	
BODY WIDTH BODY HEIGHT BODY THICKNESS	A1 A T	4.0 4.8 3.9		4.8 5.2 4.2			
PITCH OF COMPONENT FEED HOLE PITCH	P Po	3.9	12.7 12.7	4.Z	±1 ±0.3	CUMULATIVE PITCH	
FEED HOLE CENTRE TO COMPONENT CENTRE	P2		6.35		±0.4	ERROR 1.0 mm/20 PITCH TO BE MEASURED AT	
DISTANCE BETWEEN OUTER LEADS COMPONENT ALIGNMENT TAPE WIDTH HOLD-DOWN TAPE WIDTH HOLE POSITION	F △h Wo W1		5.08 0 18 6 9	1	+0.6 -0.2 ±0.5 ±0.2 +0.7 -0.5	BOTTOM OF CLINCH	
HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT COMPONENT HEIGHT LENGTH OF SNIPPED LEADS FEED HOLE DIAMETER TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	W2 Ho H1 L Do t F2		0.5 16 4 2.54	23.25 11.0 1.2	±0.2 ±0.5 ±0.2 +0.4 -0.1	t1 0.3 - 0.6	
CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3			

NOTES

NOTES 1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm. 2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20

PITCHES.

PITCHES.
3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.
6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk TO-92 T&A	1K/polybag 2K/ammo box	200 gm/1K pcs 645 gm/2K pcs	3" x 7.5" x 7.5" 12.5" x 8" x 1.8"	5.0K 2.0K	17" x 15" x 13.5" 17" x 15" x 13.5"	80.0K 32.0K	23 kgs 12.5 kgs

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Data Sheet