



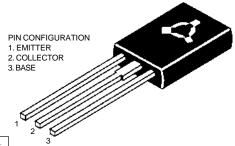
An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

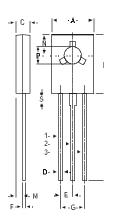
TO-126 (SOT-32) Plastic Package

C43C2

C43C2 PNP PLASTIC POWER TRANSISTOR

Complementary C42C series General Purpose Applications





DIM	MIN.	MAX.		
Α	7.4	7.8		
В	10.5	10.8		
С	2.4	2.7		
D	0.7	0.9		
Е	2.25 TYP.			
F	0.49	0.75		
G	4.5	TYP.		
L	15.7 T YP .			
М	1.27	TYP.		
N	3.75 TYP.			
Р	3.0	3.2		
Ş	2.5	TYP.		

ALL DIMENSIONS IN MM

ABSOLUTE MAXIMUM RATINGS

Collector-emitter voltage (V _{BE} =0)	$V_{C\!E\!S}$	max.	40 V
Collector-emitter voltage (open base)	$V_{C\!E\!O}$	max.	30 V
Collector current	I_C	max.	3 A
Total power dissipation up to $T_C = 25^{\circ}C$	P_D	max.	12.5 W
Junction temperature	T_{i}	max.	150 ℃
Collector-emitter saturation voltage	3		
$I_C = 1 A; I_B = 50 mA$	V_{CEsat}	max.	0.5 V
D.C. current gain			
$I_C = 200 \text{ mA}; V_{CE} = 1 \text{ V}$	$h_{\!F\!E}$	min.	40
		max.	120

RATINGS (at T_A =25°C unless otherwise specified)

Limiting values			
Collector-emitter voltage (V _{BE} =0)	$V_{C\!E\!S}$	max.	40 V
Collector-emitter voltage (open base)	$V_{C\!E\!O}$	max.	30 V
Emitter-base voltage (open collector)	V_{EBO}	max.	5.0 V
Collector current (DC)	I_C	max.	3.0 A

Collector current (Peak)*	I_{CM}	max.	5	\boldsymbol{A}
Base current	I_B	max.	2	\boldsymbol{A}
Total power dissipation up to $T_A = 25$ °C	P_D	max.	2.1	W
Total power dissipation up to $T_C = 25^{\circ}C$	P_D	max.	12.5	W
Junction temperature	T_j	max.	<i>150</i>	${}^{o}C$
Storage temperature	\check{T}_{Stg}	-65 to	+150	${}^{o}C$
THERMAL RESISTANCE				
From junction to case	R_{thj-c}	=	10	CW
From junction to ambient	R _{th j-a}	=		CW
CHADA CEEDICEICE	•			
CHARACTERISTICS				
$T_c = 25^{\circ}C$ unless otherwise specified				
Collector cutoff current				
$V_{BE} = 0$; $V_{CE} = Rated V_{CES}$	I_{CES}	max.	10	μA
Emitter cut-off current	023			•
$I_C = 0$; $V_{EB} = 5 V$	I_{EBO}	max.	100	μA
Breakdown sus. voltage				
$I_C = 100 \text{ mA}; I_B = 0$	$V_{CEO(sus)}^*$	min.	30	V
Saturation voltages	0_0 (000)			
$I_C = 1 A$; $I_B = 50 \text{ mA}$	V_{CEsat}^*	max.	0.5	V
$I_C = 1 A; I_B = 100 \text{ mA}$	V_{BEsat}^*	max.	1.3	V
D.C. current gain				
$I_C = 200 \text{ mA}; V_{CE} = 1 \text{ V}$	$h_{\!F\!E}^*$	min.	40	
		max.	120	
$I_C = 1 A$; $V_{CE} = 1 V$	$h_{\!F\!E}^*$	min.	20	
Transition frequency	12			
$I_C = 20 \text{ mA}; V_{CE} = 4 \text{ V}$	f_T	typ.	40	MHz
Collector capacitance	•			
$V_{CB} = 10 \ V; I_E = 0; f = 1 \ MHz$	C_{cbo}	max.	125	рF
Switching time				
Delay time + Rise time				
$I_C = 1 A; I_{B1} = I_{B2} = 0.1 A$	$t_{d} + t_{r}$	typ.	<i>50</i>	ns
Storage time + Fall time				
$V_{CC} = 30 \text{ V}; t_D = 25 \mu \text{sec}$	t_{S}	typ.	500	ns
P	t_f	typ.	<i>50</i>	ns
	•	J 1		

^{*} Pulsed test: $P_W = 300$ ms; duty cycle = 2%.

Customer Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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