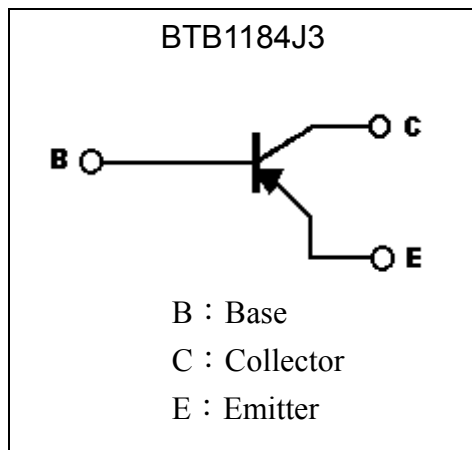
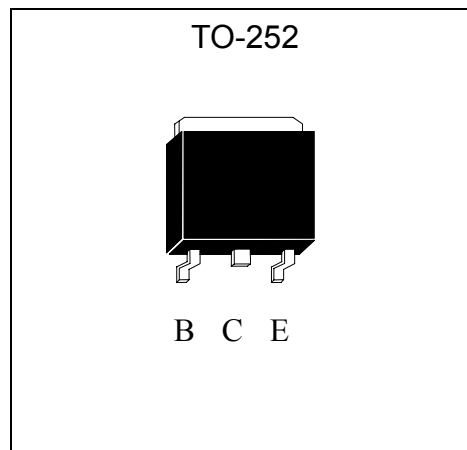


Low Vcesat PNP Epitaxial Planar Transistor

BTB1184J3

Features

- Low VCE(sat)
- Excellent current gain characteristics
- Complementary to BTD1760J3
- Pb-free package

Symbol

Outline

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V _{CB0}	-50	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EBO}	-6	V
Collector Current(DC)	I _C	-3	A
Collector Current(Pulse)	I _{CP}	-7 *1	
Power Dissipation (TA=25°C)	Pd(TA=25°C)	1	W
Power Dissipation (Tc=25°C)	Pd(Tc=25°C)	15 *2	
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55~+150	°C

Note : *1. Single Pulse Pw=10ms

*2 . Printed circuit board, 1.7mm thick, collector copper plating 10mm*10mm or larger.

**Characteristics (Ta=25°C)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	-50	-	-	V	I _C =-50μA, I _E =0
BV _{CEO}	-50	-	-	V	I _C =-1mA, I _B =0
BV _{EBO}	-6	-	-	V	I _E =-50μA, I _C =0
I _{CBO}	-	-	-1	μA	V _{CB} =-30V, I _E =0
I _{EBO}	-	-	-1	μA	V _{EB} =-4V, I _C =0
*V _{CE(sat)}	-	-	-1	V	I _C =-2A, I _B =-0.1A
*V _{BE(sat)}	-	-1	-1.5	V	I _C =-2A, I _B =-0.2A
*h _{FE1}	52	-	-	-	V _{CE} =-2V, I _C =-20mA
*h _{FE2}	82	-	560	-	V _{CE} =-3V, I _C =-500mA
*h _{FE3}	52	-	-	-	V _{CE} =-2V, I _C =-1A
f _T	-	80	-	MHz	V _{CE} =-5V, I _C =-0.1A, f=100MHz
Cob	-	35	-	pF	V _{CB} =-10V, f=1MHz

*Pulse Test : Pulse Width ≤380μs, Duty Cycle≤2%

Classification Of h_{FE2}

Rank	P	Q	R	S
Range	82~180	120~270	180~390	270~560

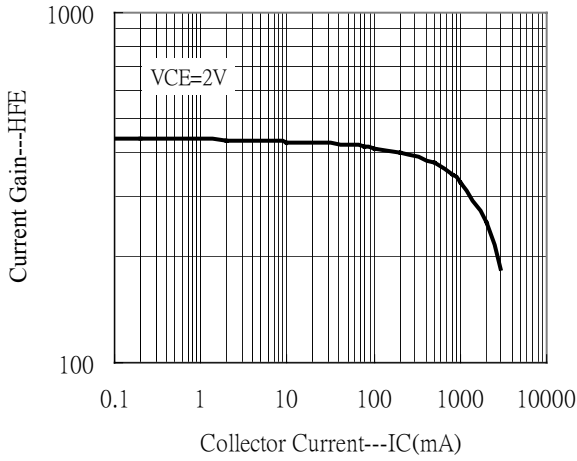
Ordering Information

Device	Package	Shipping	Marking
BTB1184J3	TO-252 (Pb-free)	2500 pcs / Tape & Reel	B1184

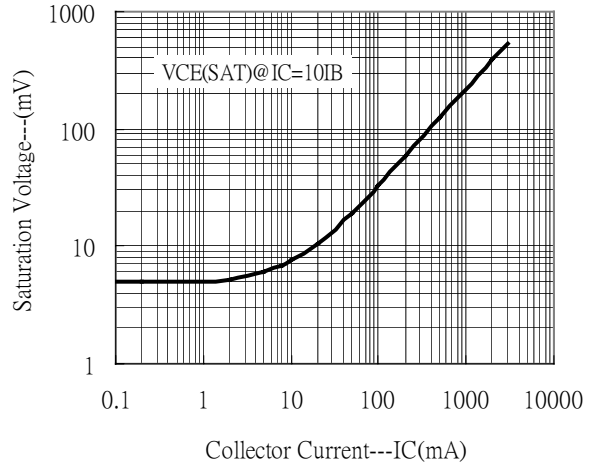


Characteristic Curves

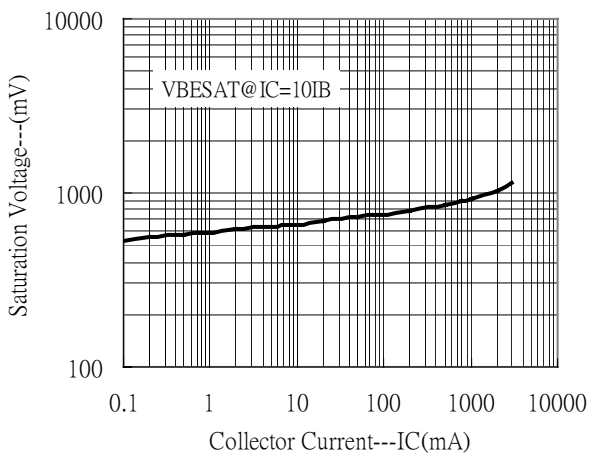
Current Gain vs Collector Current



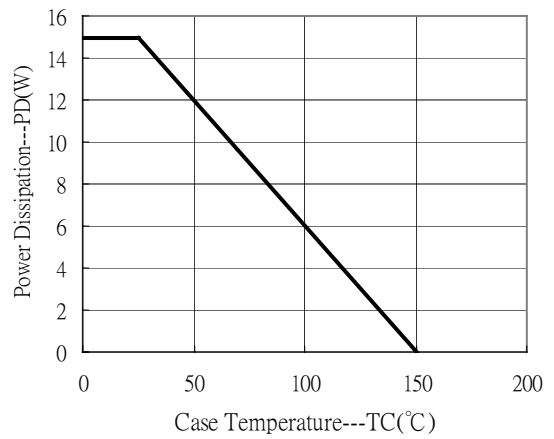
Saturation Voltage vs Collector Current



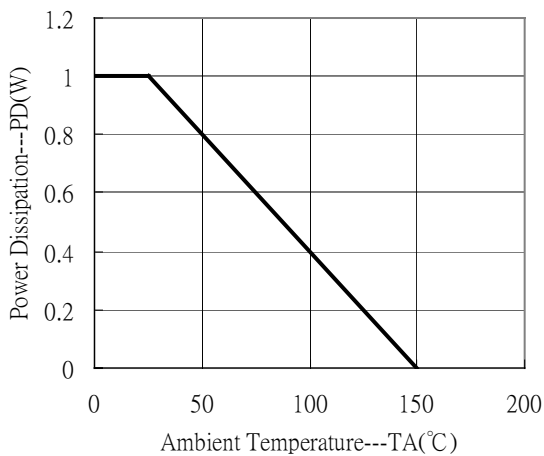
Saturation Voltage & Collector Current



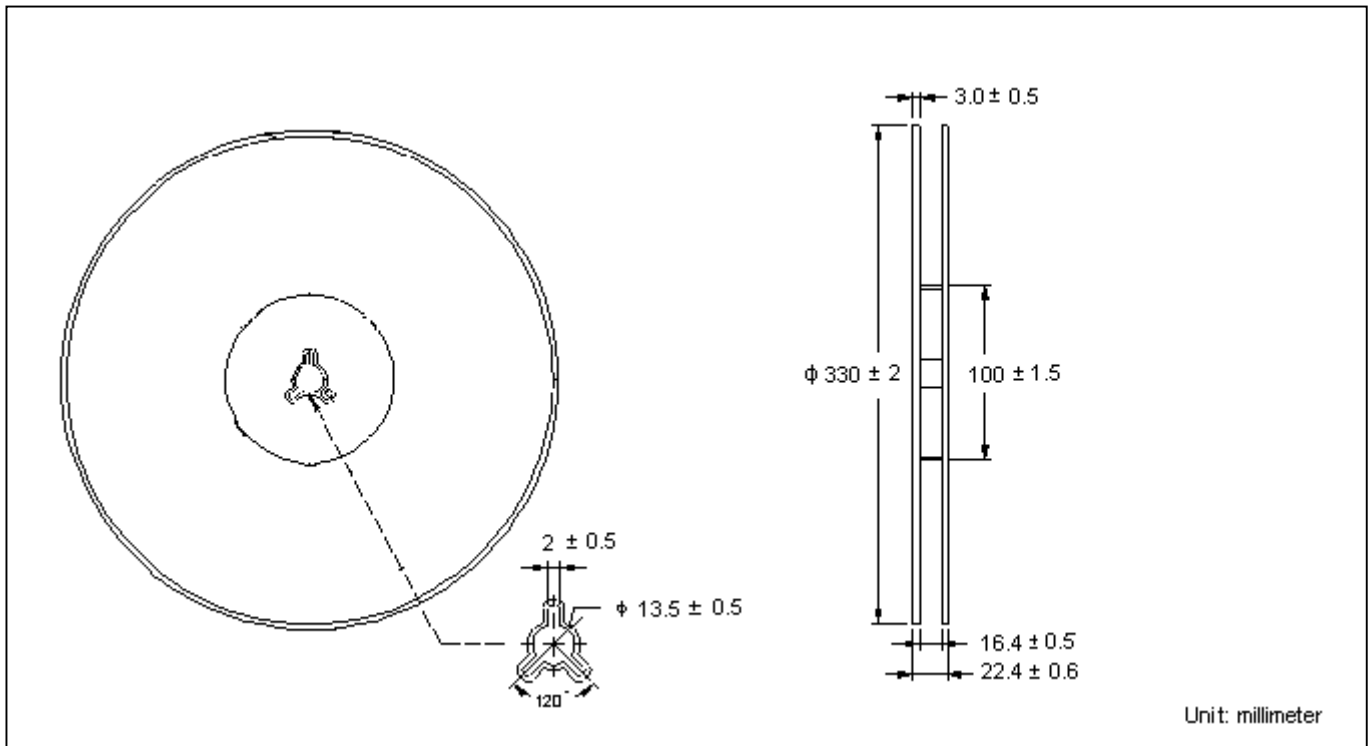
Power Derating Curve



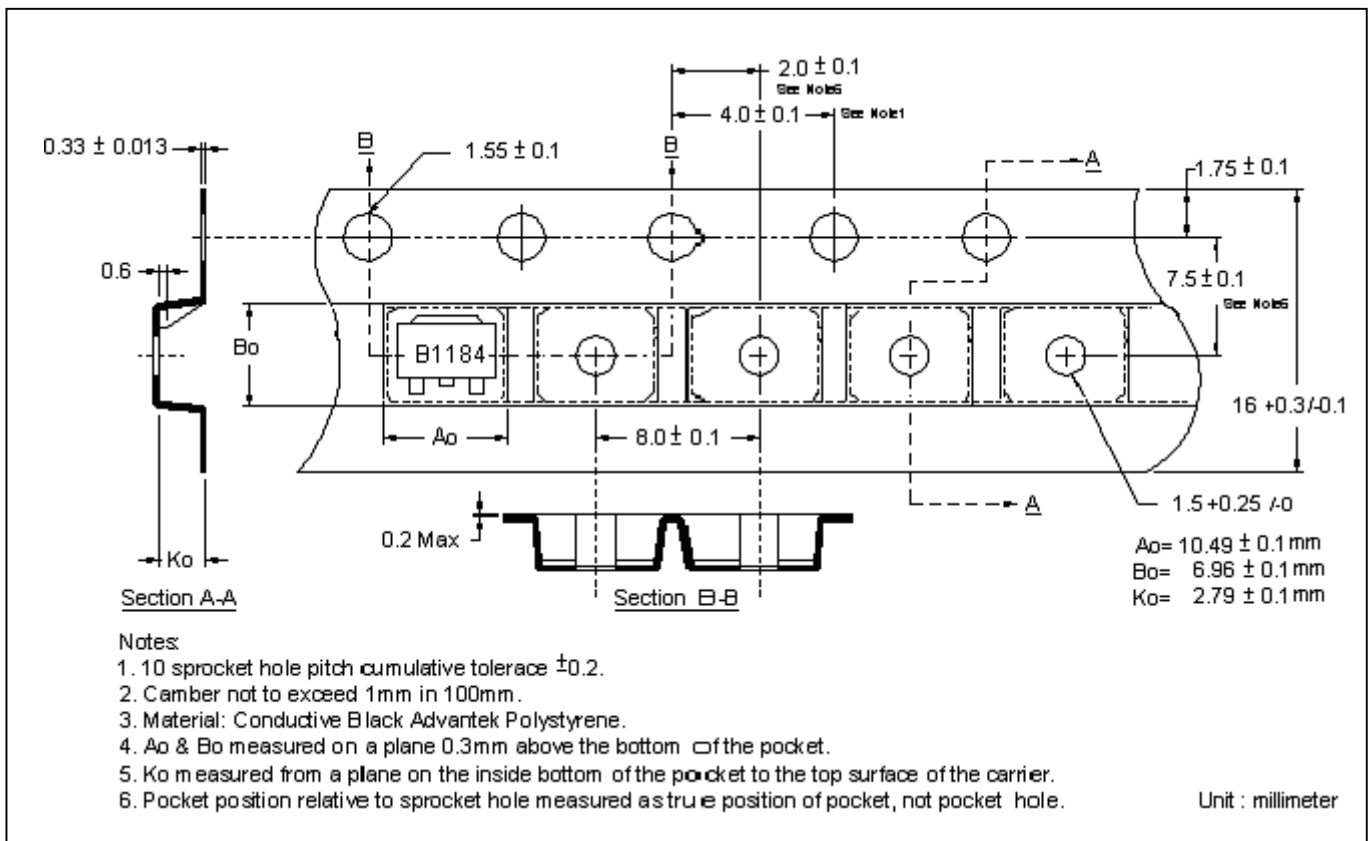
Power Derating Curve



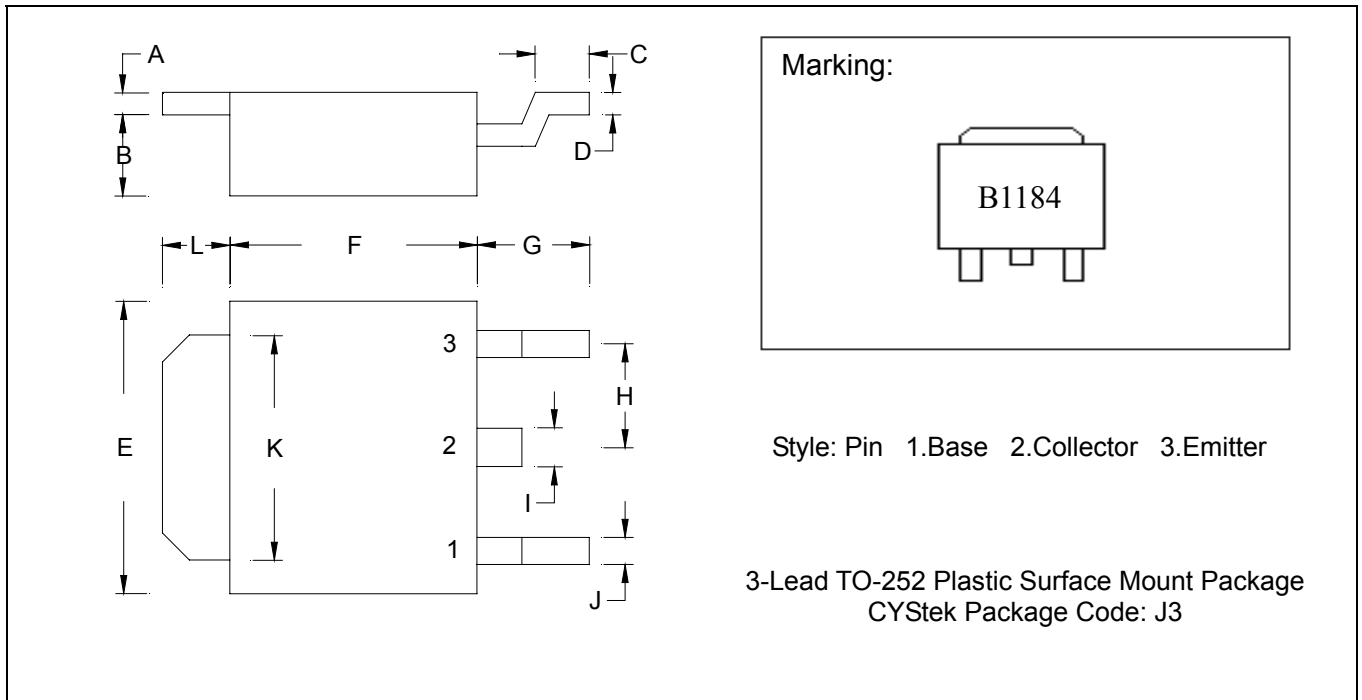
Reel Dimension



Carrier Tape Dimension



TO-252 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0177	0.0217	0.45	0.55	G	0.0866	0.1102	2.20	2.80
B	0.0650	0.0768	1.65	1.95	H	-	*0.0906	-	*2.30
C	0.0354	0.0591	0.90	1.50	I	-	0.0354	-	0.90
D	0.0177	0.0236	0.45	0.60	J	-	0.0315	-	0.80
E	0.2520	0.2677	6.40	6.80	K	0.2047	0.2165	5.20	5.50
F	0.2125	0.2283	5.40	5.80	L	0.0551	0.0630	1.40	1.60

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: 42 Alloy; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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