9009

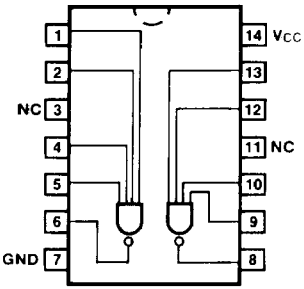
010132

NAND BUFFER

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE V_{CC} = +5.0 V, ±5%, T_A = 0°C to +75°C	MILITARY GRADE V_{CC} = +5.0 V ±10%, T_A = -55°C to +125°C	PKG TYPE
Ceramic DIP (D)	A	9009DC	9009DM	6A
Flatpak (F)	A	9009FC	9009FM	3I

CONNECTION DIAGRAM
PINOUT A



V_{CC} = Pin 14
GND = Pin 7

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	9XXX (U.L.) HIGH/LOW
Inputs Outputs	3.0/2.0 90/26 (99)/(25.5)

DC AND AC CHARACTERISTICS OVER COMMERCIAL TEMPERATURE RANGE: V_{CC} = +5.0 V ±5%

SYMBOL	PARAMETER		0°C Min	0°C Max	25°C Min	25°C Max	75°C Min	75°C Max	UNITS	CONDITIONS
V_{IH}	Input HIGH Voltage		1.9		1.8		1.6		V	Guaranteed Input HIGH Threshold
V_{IL}	Input LOW Voltage			0.85		0.85		0.85	V	Guaranteed Input LOW Threshold
V_{OL}	Output LOW Voltage			0.45		0.45		0.45	V	V_{CC} = 5.25 V, I_{OL} = 48 mA, V_{IN} = 5.25 V
										V_{CC} = 4.75 V, I_{OL} = 42.3 mA, Inputs at V_{IH}
I_{IH}	Input HIGH Current					120		120	µA	V_{CC} = 5.25 V, V_{IN} = 4.5 V Gnd on Other Inputs
I_{IL}	Input LOW Current			-3.2		-3.2		-3.2	mA	V_{CC} = 5.25 V, V_{IN} = .45 V 5.25 V on Other Inputs
				-2.82		-2.82		-2.82		V_{CC} = 4.75 V, V_{IN} = .45 V 5.25 V on Other Inputs
I_{CCH}	Power Supply Current (each gate)	ON		14.6		14.6		14.6	mA	V_{IN} = Open
I_{CCL}		OFF		3.4		3.4		3.4		V_{IN} = Gnd
t_{PLH}	Propagation Delay				3.0	17			ns	Figs. 3-1, 3-4
t_{PHL}					2.0	13				C_L = 15 pF

5

DC AND AC CHARACTERISTICS OVER MILITARY TEMPERATURE RANGE: V_{CC} = +5.0 V ±10%

SYMBOL	PARAMETER		-55°C Min	-55°C Max	25°C Min	25°C Max	125°C Min	125°C Max	UNITS	CONDITIONS
V_{IH}	Input HIGH Voltage		2.0		1.7		1.4		V	Guaranteed Input HIGH Threshold
V_{IL}	Input LOW Voltage			0.8		0.9		0.8	V	Guaranteed Input LOW Threshold
V_{OL}	Output LOW Voltage			0.4		0.4		0.4	V	V_{CC} = 5.5 V, I_{OL} = 52.8 mA V_{IN} = 5.5 V
										V_{CC} = 4.5 V, I_{OL} = 40.8 mA, Inputs at V_{IH}
I_{IH}	Input HIGH Current					120		120	μA	V_{CC} = 5.5 V, V_{IN} = 4.5 V Gnd on Other Inputs
I_{IL}	Input LOW Current			-3.2		-3.2		-3.2	mA	V_{CC} = 5.5 V V_{IN} = 0.4 V 5.5 V on Other Inputs
				-2.48		-2.48		-2.48		V_{CC} = 4.5 V V_{IN} = 0.4 V 5.5 V on Other Inputs
I_{CCH}	Power Supply Current (each gate)	ON		12.9		12.9		12.9	mA	V_{IN} = Open
I_{CCL}		OFF		3.2		3.2		3.2		V_{IN} = Gnd
t_{PLH}	Propagation Delay				4.0	15			ns	Figs. 3-1, 3-4
t_{PHL}					3.0	10				C_L = 15 pF