V _{RRM}	=	4500 V
I _{FAVM}	=	650 A
I _{FSM}	=	16 kA
V _{F0}	=	1.4 V
r _F	=	1 mΩ
V _{DClink}	=	2800 V

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Fast Recovery Diode 5SDF 07F4501

Doc. No. 5SYA1107-03 Sep. 01

- Patented free-floating silicon technology
- Low on-state and switching losses
- Optimized for use as freewheeling diode in GTO converters with high DC link voltages
- Standard press-pack housing, hermetically cold-welded
- Cosmic radiation withstand rating

Blocking

V _{RRM}	Repetitive peak reverse voltage	4500 V	Half sine wave	, t _P = 10 ms, f = 50 Hz	
I _{RRM}	Repetitive peak reverse current	≤ 50 mA	$V_{R} = V_{RRM}, T_{j} = 125^{\circ}C$		
V _{DClink}	Permanent DC voltage for 100 FIT failure rate	2800 V	100% Duty	Ambient cosmic radiation at sea level in open air.	
V _{DClink}	Permanent DC voltage for 100 FIT failure rate	V	5% Duty		

Mechanical data (see Fig. 12)

Е	Mounting force	min.		20 kN
F _m	Mounting force -	max.		24 kN
а	Acceleration: Device unclamped Device clamped			50 m/s ² 200 m/s ²
m	Weight			0.46 kg
Ds	Surface creepage distance		≥	33 mm
D _a	Air strike distance		≥	20 mm



On-state (see Fig. 2, 3)

I _{FAVM}	Max. average on-state current	650 A	Half sine wave, $T_c = 85^{\circ}C$
I _{FRMS}	Max. RMS on-state current	1000 A	
I _{FSM}	Max. peak non-repetitive	16 kA	tp = 10 ms Before surge:
	surge current	44 kA	$tp = 1 ms T_c = T_j = 125^{\circ}C$
∫l²dt	Max. surge current integral	1.28 10 ⁶ A ² s	tp = 10 ms After surge:
		0.8·10 ⁶ A ² s	tp = 1 ms $V_R \approx 0 V$
V_{F}	Forward voltage drop	\leq 2.7 V	I _F = 1250 A
V_{F0}	Threshold voltage	1.4 V	Approximation for $T_j = 125^{\circ}C$
۲ _F	Slope resistance	1 mΩ	I _F = 4002000 A

Turn-on (see Fig. 4, 5)

V_{fr}	Peak forward recovery voltage	\leq	74 V	di/dt = 500 A/µs, T _j = 125°C
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Turn-off (see Fig. 6 to 11)

l _{rr}	Reverse recovery current	\leq	600 A	di/dt = 300 A/µs, I _F = 700 A,	
Q _{rr}	Reverse recovery charge	\leq	1900 µC	$T_j = 125^{\circ}C, V_{RM} = 4500 V,$	
Err	Turn-off energy	\leq	1 J	$C_s = 3\mu F$ (GTO snubber circuit)	

Thermal (see Fig. 1)

Tj	Operating junction temperature range	-40125°C		
T _{stg}	Storage temperature range	-40125°C		
R_{thJC}	Thermal resistance junction to case	≤ 40 K/kW	Anode side cooled	
		\leq 40 K/kW	Cathode side cooled	F _m =
		≤ 20 K/kW	Double side cooled	20 24 kN
R_{thCH}	Thermal resistance case to heatsink	≤ 10 K/kW	Single side cooled	
		≤ 5 K/kW	Double side cooled	

Analytical function for transient thermal impedance.

$$Z_{\text{thJC}}(t) = \sum_{i=1}^{n} R_{i}(1 - e^{-t/\tau_{i}})$$

i	1	2	3	4		
R _i (K/kW)	11.83	4.26	1.63	2.28		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$						
F _m = 20… 24 kN Double side cooled						

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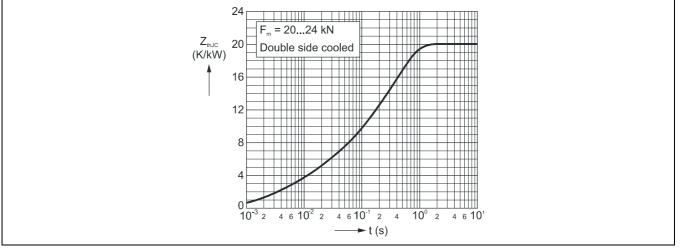


Fig. 1 Transient thermal impedance (junction-to-case) vs. time in analytical and graphical form (max. values).

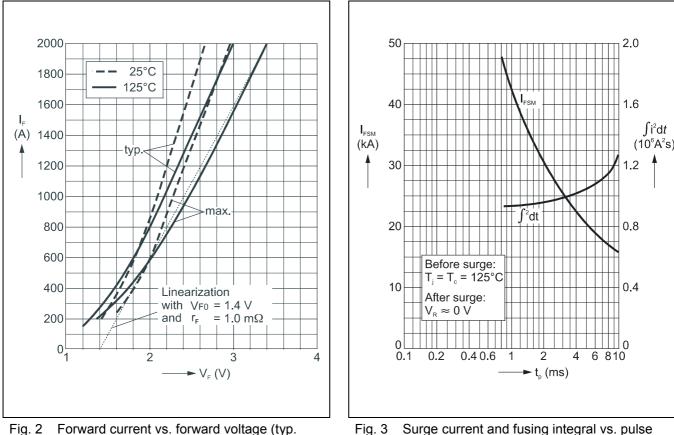
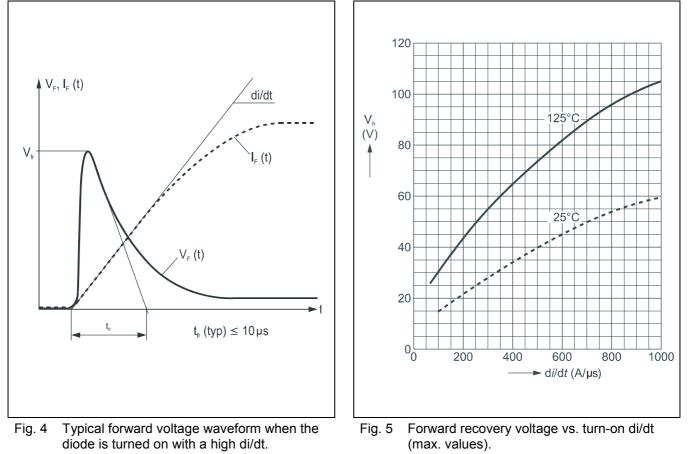


Fig. 2 Forward current vs. forward voltage (typ. and max. values) and linear approximation of max. curve at 125°C.

Fig. 3 Surge current and fusing integral vs. pulse width (max. values) for non-repetitive, half-sinusoidal surge current pulses.



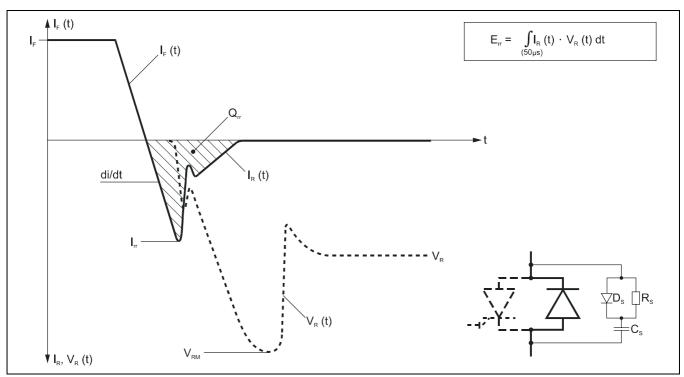
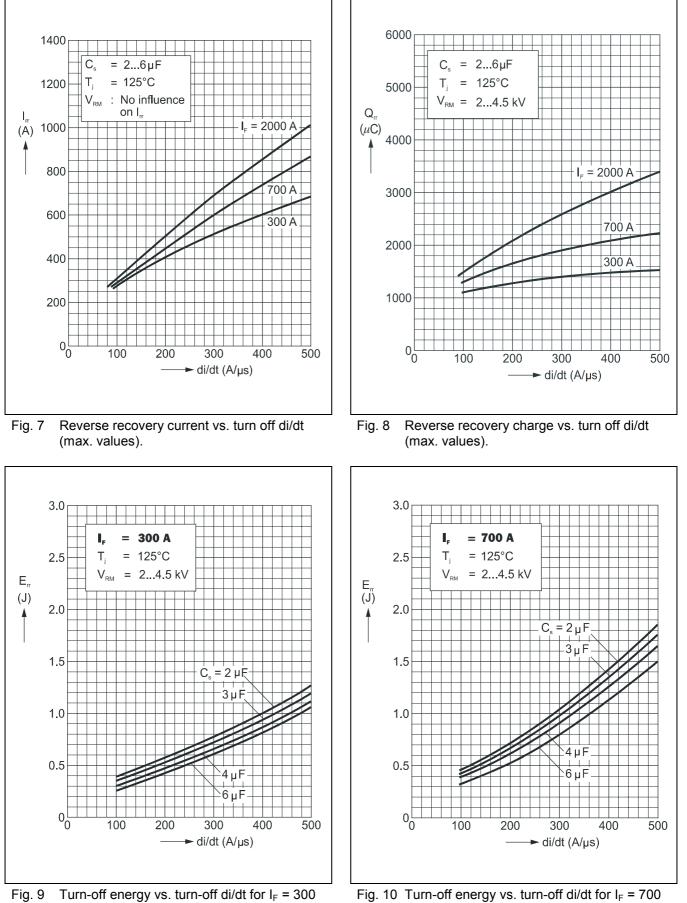


Fig. 6 Typical current and voltage waveforms at turn-off when the diode is connected to an RCD snubber, as often used in GTO circuits.



A (max. values).

Fig. 10 Turn-off energy vs. turn-off di/dt for I_F = 700 A (max. values).

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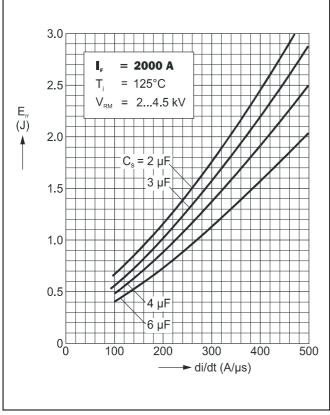


Fig. 11 Turn-off energy vs. turn-off di/dt for I_F = 2000 A (max. values).

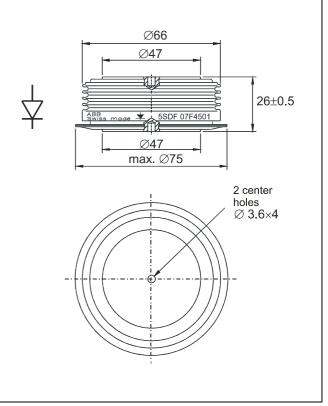


Fig. 12 Outline drawing. All dimensions are in millimeters and represent nominal values unless stated otherwise.

ABB Semiconductors AG reserves the right to change specifications without notice.



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