Silicon N/P Channel MOS FET High Speed Power Switching



ADE-208-729 (Z) 1st. Edition February 1999

Features

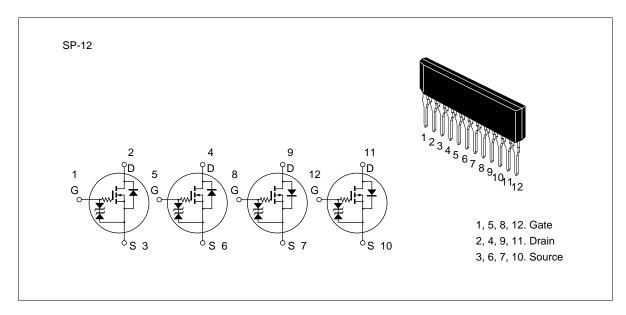
• Low on-resistance

N Channel: $R_{DS(on)} \leq 0.17 \ \Omega, V_{GS} = 10 \ V, I_{D} = 4 \ A$

 $P \ Channel: \quad R_{\text{DS(on)}} \leq 0.2 \ \Omega, \ V_{\text{GS}} = -10 \ V, \ I_{\text{D}} = -4 \ A$

- 4 V gate drive devices.
- High density mounting

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratin	gs	Unit
		Nch	Pch	
Drain to source voltage	V _{DSS}	60	-60	V
Gate to source voltage	V _{GSS}	±20	±20	V
Drain current	I _D	8	-8	А
Drain peak current	Note1 D(pulse)	32	-32	А
Body-drain diode reverse drain current	I _{DR}	8	-8	А
Channel dissipation	Pch (Tc = 25° C) ^{Note2}		28	W
Channel dissipation	Pch Note2	4.0		W
Channel temperature	Tch		150	°C
Storage temperature	Tstg		-55 to +150	°C

Note: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. 4 devices operation

Electrical Characteristics (Ta = 25° C)

(N Channel)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60		—	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20			V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—		±10	μΑ	$V_{\rm GS}=\pm 16~V,~V_{\rm DS}=0$
Zero gate voltage drain current	I _{DSS}	—		250	μΑ	$V_{\rm DS} = 50 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0		2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	$R_{DS(on)}$	—	0.13	0.17	Ω	$I_{\rm D}$ = 4 A, $V_{\rm GS}$ = 10 V ^{Note3}
resistance	R _{DS(on)}	—	0.19	0.24	Ω	$I_{\rm D} = 4$ A, $V_{\rm GS} = 4$ V ^{Note3}
Forward transfer admittance	y _{fs}	3.5	5.5		S	$I_{\rm D}$ = 4 A, $V_{\rm DS}$ = 10 V ^{Note3}
Input capacitance	Ciss	—	33	—	pF	V _{DS} = 10 V
Output capacitance	Coss	—	220	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	5.2	_	pF	f = 1 MHz
Gate series resistance	Rg	_	1.5	_	kΩ	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0$ f = 1 MHz
Turn-on delay time	t _{d(on)}	—	0.15	—	ns	V_{GS} = 10 V, I_{D} = 4 A
Rise time	t,	—	0.5	—	ns	$R_{L} = 7.5 \Omega$
Turn-off delay time	t _{d(off)}	—	3.2		ns	_
Fall time	t _f	—	1.4	_	ns	_
Body-drain diode forward voltage	V_{DF}	_	1.5		V	$I_{\rm F} = 8 {\rm A}, {\rm V}_{\rm GS} = 0$
Body–drain diode reverse recovery time	t _{rr}		850	—	ns	$I_{F} = 8 \text{ A}, V_{GS} = 0$ diF/ dt = 50 A/ μ s

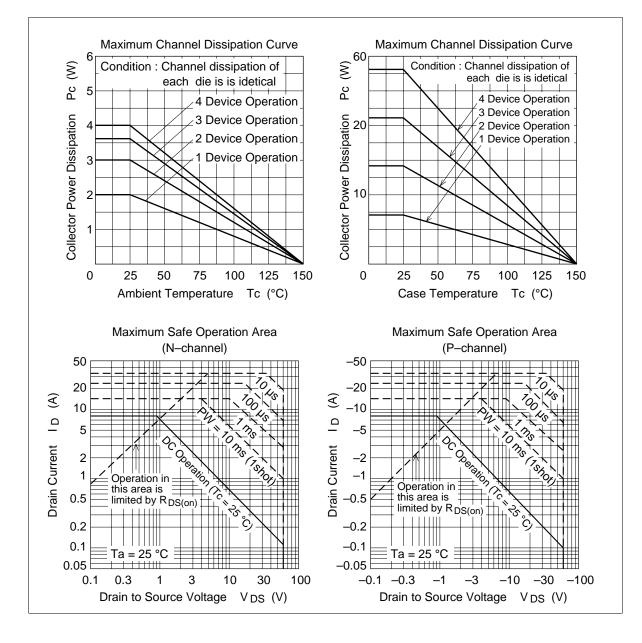
Note: 3. Pulse test

(Ρ	Channel)
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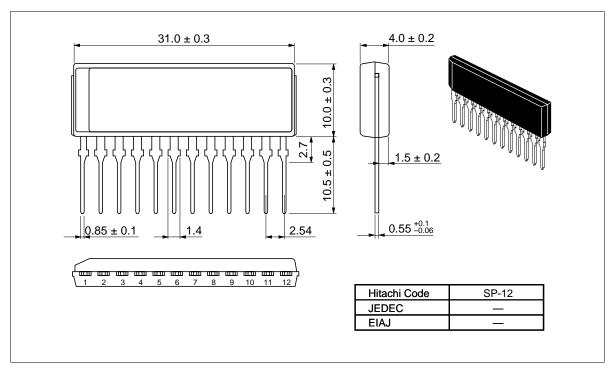
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
	-		тур	Max		
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	—	—	V	$I_{\rm D} = -10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_{\rm G}=\pm 100~\mu A,~V_{\rm DS}=0$
Gate to source leak current	I_{GSS}	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	-250	μΑ	$V_{\rm DS} = -50 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	-1.0	—	-2.5	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Static drain to source on state	$R_{DS(on)}$	—	0.15	0.2	Ω	$I_{D} = -4 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
resistance	R _{DS(on)}	—	0.2	0.27	Ω	$I_{\rm D} = -4$ A, $V_{\rm GS} = -4$ V ^{Note3}
Forward transfer admittance	y _{fs}	3.5	6.0	—	S	$I_{D} = -4 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	17	_	pF	$V_{\rm DS} = -10 \ V$
Output capacitance	Coss	—	460	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	1.2	—	pF	f = 1 MHz
Gate series resistance	Rg	—	3.2	—	kΩ	$V_{DS} = 0, V_{GS} = 0 f = 1 MHz$
Turn-on delay time	t _{d(on)}	—	0.6	—	ns	$V_{\rm GS} = -10 \text{ V}, \text{ I}_{\rm D} = -4 \text{ A}$
Rise time	t,	—	2.1	—	ns	$R_{L} = 7.5 \Omega$
Turn-off delay time	t _{d(off)}	_	12	_	ns	
Fall time	t _f	—	5.8	—	ns	
Body-drain diode forward voltage	V_{DF}	_	-1.2	_	V	$I_{\rm F} = -8$ A, $V_{\rm GS} = 0$
Body–drain diode reverse recovery time	t _{rr}		2.5		ns	$I_F = -8$ A, $V_{GS} = 0$ diF/ dt = 50 A/ μ s

Note: 3. Pulse test

Main Characteristics



Package Dimensions



Unit: mm

Cautions

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Hitachi, Ltd.

Semiconductor & IC Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to: Hitachi Semiconductor Hitachi Europe GmbH

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose, CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223

Electronic components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00 Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 049318 Tel: 535-2100 Fax: 535-1533

Hitachi Asia Ltd. Taipei Branch Office 3F, Hung Kuo Building. No.167, Tun-Hwa North Road, Taipei (105) Tel: <886> (2) 2718-3666 Fax: <886> (2) 2718-8180 Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Tsim Sha Tsui, Kowloon, Hong Kong Tel: <852> (2) 735 9218 Fax: <852> (2) 730 0281 Telex: 40815 HITEC HX

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