Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

# 2SK3265

# Chopper Regulators DC-DC Converter, and Motor Drive Applications

 $\begin{array}{ll} \bullet & \text{Low drain-source ON resistance} & : R_{DS} \, (\text{ON}) = 0.72 \, \Omega \, (\text{typ.}) \\ \bullet & \text{High forward transfer admittance} & : | Y_{fs} | = 7.0 \, \text{S} \, (\text{typ.}) \\ \bullet & \text{Low leakage current} & : I_{DSS} = 100 \, \mu\text{A} \, (\text{max}) \, (\text{V}_{DS} = 700 \, \text{V}) \\ \bullet & \text{Enhancement-mode} & : V_{th} = 2.0 \text{-} 4.0 \, \text{V} \, (\text{V}_{DS} = 10 \, \text{V}, \, I_{D} = 1 \, \text{mA}) \\ \end{array}$ 

#### **Maximum Ratings (Tc = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	700	V	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	700	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	10	Α	
	Pulse (Note 1)	I <sub>DP</sub>	30	Α	
Drain power dissipation	ı	$P_{D}$	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	420	mJ	
Avalanche current		I <sub>AR</sub>	10	Α	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

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SC-67

2-10R1B

Weight: 1.9 g (typ.)

JEITA

**TOSHIBA** 

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 7.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device.

Please handle with caution.

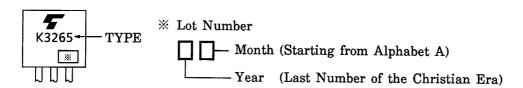
## **Electrical Characteristics (Tc = 25°C)**

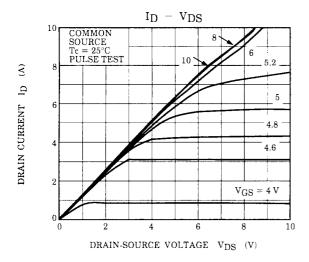
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	$I_{GSS}$	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V		_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off cur	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 700 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	700	_	_	V
Gate threshold v	roltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	_	0.72	0.75	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	4.0	7.0	_	S
Input capacitanc	е	C <sub>iss</sub>		_	1700	_	
Reverse transfer	capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	40	_	pF
Output capacitance		C <sub>oss</sub>			200	_	
Switching time	Rise time	tr	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{DD}$ $V_{DD}$ $V_{OUT}$	_	40	_	- ns
	Turn-on time	t <sub>on</sub>		_	72	_	
	Fall time	t <sub>f</sub>		ı	42		
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	-	145	_	
Total gate charge (Gate-source plus gate-drain)		Qg			53	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		25	_	nC
Gate-drain ("miller") charge		$Q_{gd}$			28	_	

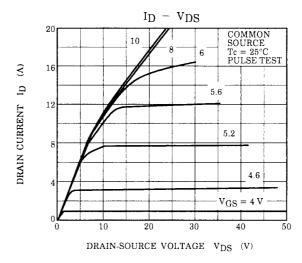
## **Source-Drain Ratings and Characteristics (Tc = 25°C)**

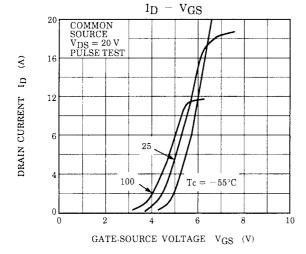
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	30	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	1400		ns
Reverse recovery charge	$Q_{rr}$	dl <sub>DR</sub> / dt = 100 A / μs	_	17.5	1	μC

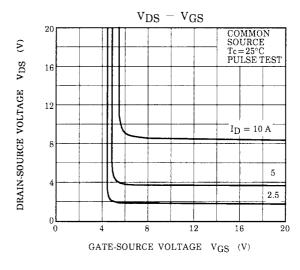
### Marking

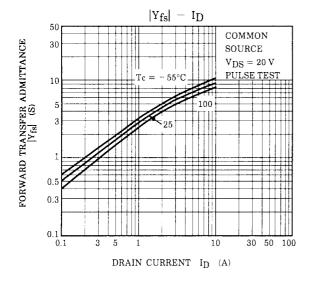


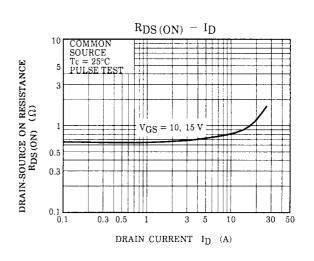




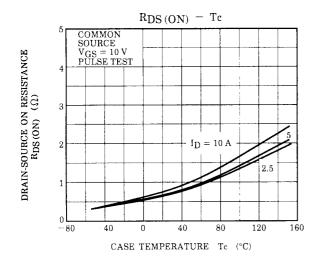


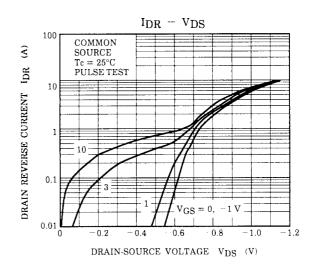


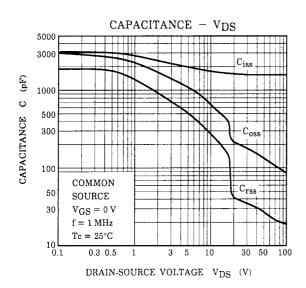


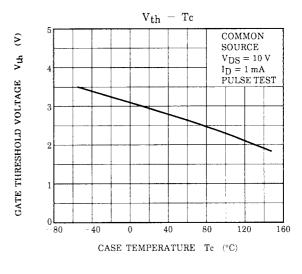


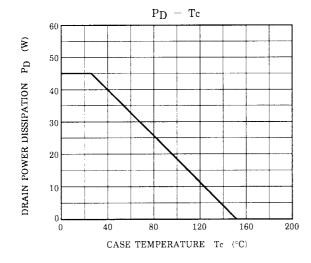
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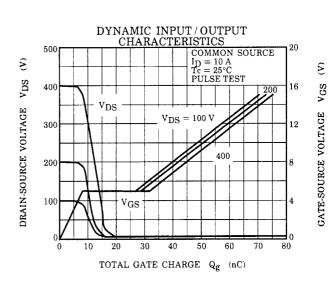


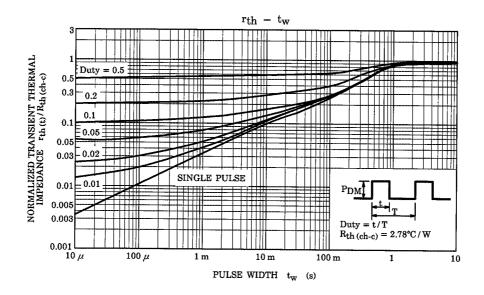


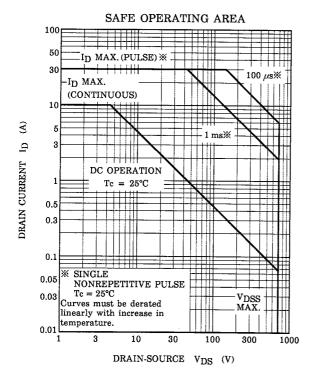


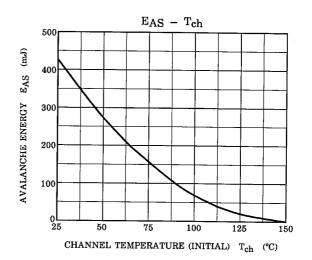


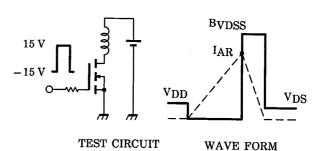












$$\begin{aligned} R_G &= 25 \ \Omega \\ V_{DD} &= 90 \ \text{V}, \ L = 7.5 \ \text{mH} \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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