



# 1N5711W

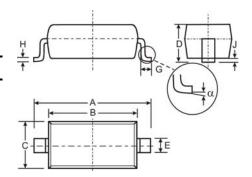
### SURFACE MOUNT SCHOTTKY BARRIER DIODE

### **Features**

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Fast Switching Time
- Low Reverse Capacitance
- Surface Mount Package Ideally Suited for Automatic Insertion
- Lead Free/RoHS Compliant (Note 3)

## **Mechanical Data**

- Case: SOD-123
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe)
- Polarity: Cathode Band
- Marking Information: See Page 3 Ordering Information: See Page 3 Weight: 0.01 grams (approximate)



SOD-123										
Dim	Min	Max								
Α	3.55	3.85								
В	2.55	2.85								
С	1.40	1.70								
D	_	1.35								
Е	0.45	0.65								
u	0.55 Typical									
G	0.25									
Н	0.11 T	Typical								
7		0.10								
α	0°	8°								
All Dimensions in mm										

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	70	V
DC Blocking Voltage	$V_R$		
RMS Reverse Voltage	$V_{R(RMS)}$	49	V
Maximum Forward Current	I <sub>FM</sub>	15	mA
Power Dissipation (Note 1)	$P_d$	333	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	300	°C/W
Operating Temperature Range	T <sub>j</sub>	-55 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	70			V	$I_R = 10\mu A$
Forward Voltage Drop	$V_{F}$	_	_	0.41 1.00	V	$I_{F} = 1.0 \text{mA}$ $I_{F} = 15 \text{mA}$
Reverse Leakage Current (Note 2)	$I_R$	_	_	200	nA	$V_R = 50V$
Total Capacitance	$C_T$	_	_	2.0	рF	$V_R = 0V$ , $f = 1.0MHz$
Reverse Recovery Time	t <sub>rr</sub>	_		1.0	ns	$I_F = I_R = 5.0 \text{mA}$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

Notes:

- 1. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- Short duration test pulse used to minimize self-heating effect.
- No purposefully added lead.



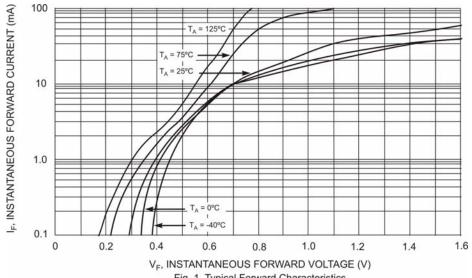
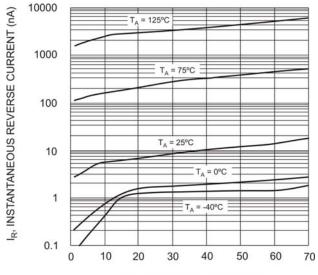


Fig. 1 Typical Forward Characteristics



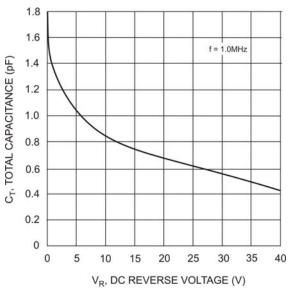
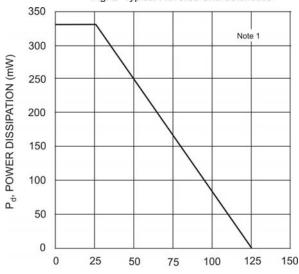


Fig. 3 Typical Capacitance

 $V_R$ , REVERSE VOLTAGE (V) Fig. 2 Typical Reverse Characteristics



T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 4 Power Derating Curve

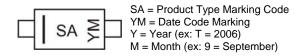


### **Ordering Information** (Note 4)

Device	Packaging	Shipping			
1N5711W-7-F	SOD-123	3000/Tape and Reel			

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	200	8 200	9 2010	2011	2012
Code	J	K	L	М	N	Р	R	S	Т	U	V	W	Х	Υ	Z
N	Month		Jan	Feb	Mar	Apr	May	Jun	Jul	A	ug	Sep	Oct	Nov	Dec
	Code		1	2	3	4	5	6	7		8	9	0	N	D

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