# PC901V0NSZX

#### Features

- 1. Normal ON operation, open collector output
- 2. TTL and LSTTL compatible output
- 3. Operating supply voltage (Vcc:3 to 15V)
- 4. Isolation voltage (Viso (rms):5kV)
- 5. High sensitivity

(IFLH:MAX. 2.0mA at Ta=25°C)

- 6. Under preparation for UL standard
- 7. 6-pin DIP package

### Applications

- 1. Programmable controllers
- 2. PC peripherals
- 3. Electronic musical instruments

■ Absolute Maximum Ratings (Ta=25°C)							
Parameter		Symbol	Rating	Unit			
Input	Forward current	IF	50	mA			
	*1 Peak forward current	Ifm	1	A			
	Reverse voltage	VR	6	V			
	Power dissipation	Р	70	mW			
Output	Supply voltage	Vcc	16	V			
	High level output voltage	Vон	16	V			
	Low level output current	Iol	50	mA			
	Power dissipation	Po	150	mW			
Total power dissipation		Ptot	170	mW			
*2 Isolation voltage		$V_{iso(rms)}$	5	kV			
Operating temperature		Topr	-25 to +85	°C			
Storage temperature		Tstg	-40 to +125	°C			
*3 Soldering temperature		Tsol	260	°C			

\*1 Pulse width≤100µs, Duty ratio=0.001

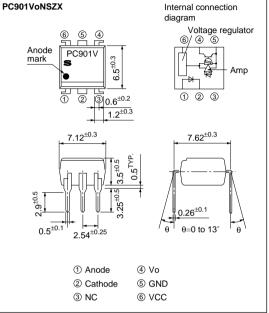
\*2 40 to 60%RH, AC for 1 min

\*3 For 10 s

## **Digital Output Type OPIC Photocoupler**

## Outline Dimensions

(Unit : mm)



\* "OPIC" (Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and signalprocessing circuit integrated onto a single chip.

Elect	ro-c	optical Characteristics	5	(Ta=0	to +70°C	c unless o	therwise s	spesified)	
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Input		VF	IF=4mA	4mA –		1.4	v		
	Forward voltage		IF=0.3mA	0.7	1.0	-	- v		
	Reverse current		Ir	Ta= $25^{\circ}$ C, V <sub>R</sub> = $4$ V	-	-	10	μΑ	
	Terminal capacitance		Ct	Ta=25°C, V=0, f=1kHz	-	30	250	pF	
Output	Operating supply voltage		Vcc		3	-	15	V	
	Low level output voltage		Vol	IoL=16mA, Vcc=5V, IF=4mA	_=16mA, Vcc=5V, IF=4mA – 0.2		0.4	V	
	High level output current		Іон	Vo=Vcc=15V, IF=0	-	-	100	μΑ	
	Low level supply current		ICCL	$V_{CC}=5V, I_{F}=0$	_	2.5	5.0	mA	
	High level supply current		Іссн	Vcc=5V, IF=4mA	-	2.7	5.5	mA	
	<sup>*4</sup> "Low→High" threshold input current		IFLH	Ta=25°C, Vcc=5V, RL=280 $\Omega$	-	1.1	2.0	mA	
				VCC=5V, RL= $280\Omega$	-	-	4.0		
	*5 "High→Low" threshold input current		IFHL	Ta=25°C, Vcc=5V, RL=280 $\Omega$	0.4	0.8	_	mA	
				VCC=5V, RL= $280\Omega$	0.3	-	-		
	*6 Hysteresis		IFHL/IFLH	Vcc=5V, Rl= $280\Omega$	0.5	0.7	0.9	-	
	Isolation resistance		Riso	Ta=25°C, DC=500V, 40 to 60% RH	5×10 <sup>10</sup>	1011	_	Ω	
Transfer charac- teristics	me	"Low→High" propagation delay time	<b>t</b> PLH		-	1	3	μs	
	i ti	"High→Low" propagation delay time	<b>t</b> PHL	Ta=25°C	-	2	6		
	bon	Rise time	tr	Vcc=5V, IF=4mA RL=280 $\Omega$	_	0.1	0.5		
	*7 Res	Fall time	tr	NL-20012	_	0.05	0.5		
	*8 Instantaneous common mode rejection voltage "Output : High level"		СМн	V <sub>CM</sub> =600V(peak), V <sub>0</sub> (MIN.)=2V I <sub>F</sub> =4mA, R <sub>L</sub> =280Ω, Ta=25°C	_	-2 000	_	V/µs	
	*8 Instantaneous common mode rejection voltage "Output : Low level"		CML	V <sub>CM</sub> =600V(peak), V <sub>0</sub> (MAX.)=0.8V I <sub>F</sub> =0, R <sub>L</sub> =280Ω, Ta=25°C	_	2 000	_	V/µs	

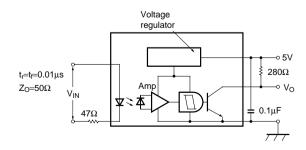
\*4 IFLH represents forward current when output goes from low to high. \*5 IFHL represents forward current when output goes from high to low.

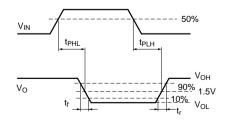
\*6 Hysteresis stands for IFHL/IFLH.

\*7 Test circuit for response time is shown below.

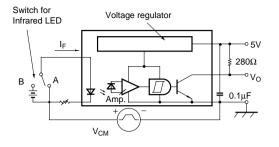
\*8 Test circuit for CMH, CML shown below.

## Fig.1 Test Circuit for Response Time





### Fig.2 Test Circuit for CMH and CML



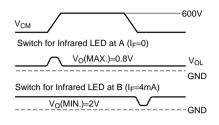


Fig.3 Forward Current vs. Ambient Temperature

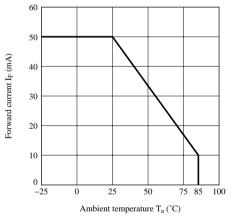


Fig.5 Forward Current vs. Forward Voltage

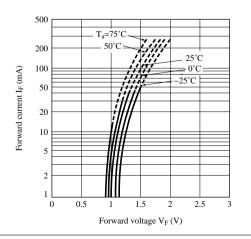


Fig.4 Power Dissipation vs. Ambient Temperature

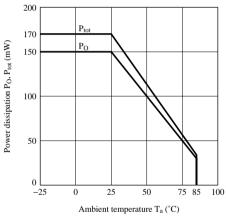


Fig.6 Relative Threshold Input Current vs. Supply Voltage

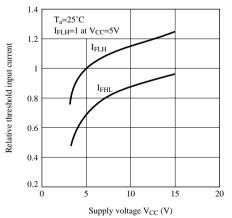


Fig.7 Relative Threshold Input Current vs. Ambient Temperature

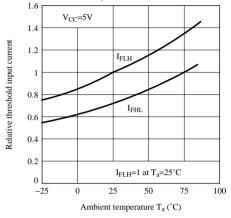
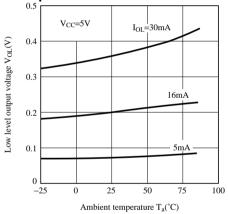
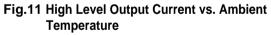


Fig.9 Low Level Output Current vs. Ambient Temperature





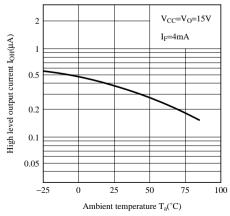


Fig.8 Low Level Output Voltage vs. Low Level Output Current

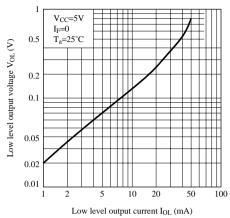


Fig.10 High Level Output Current vs. Forward Current

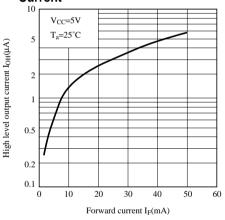


Fig.12 Supply Current vs. Supply Voltage

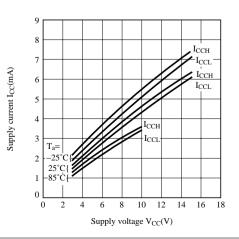
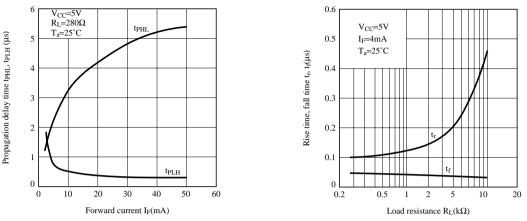


Fig.14 Rise Time, Fall Time vs. Load

Resistance

## Fig.13 Propagation Delay Time vs. Forward Current



#### Precautions for Use

- 1. It is recommended that a by-pass capacitor of more than  $0.01\mu$ F is added between V<sub>CC</sub> and GND near the device in order to stabilize power supply line.
- 2. Handle this product the same as with other integrated circuits against static electricity.
- 3. As for other general cautions, please refer to the chapter "Precautions for Use".

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