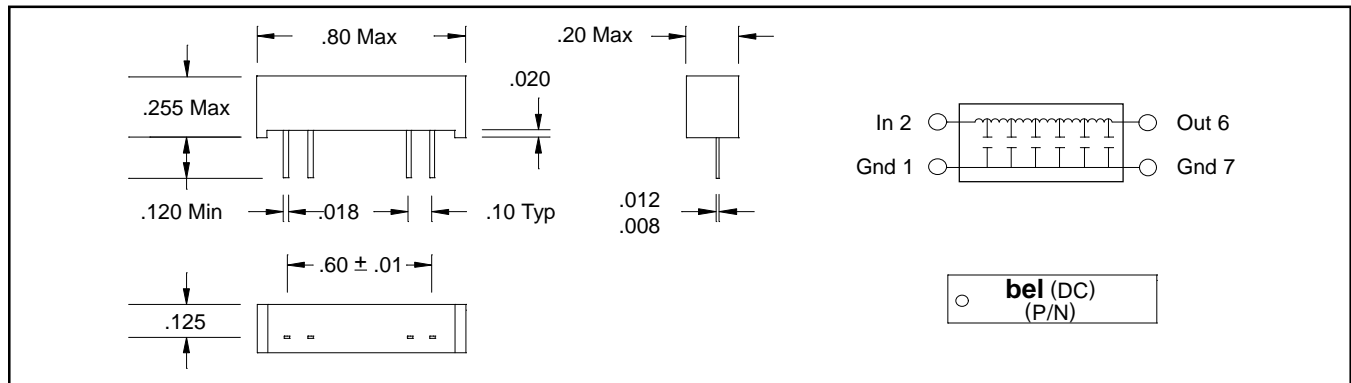


4 PIN SIP PASSIVE DELAY MODULES

Cat 40-R0



Part Numbers

50 Ohms ± 10%	75 Ohms ± 10%	100 Ohms ± 10%	200 Ohms ± 10%	Total Delay	Rise Time	Attenuation Maximum
0403-0001-50	0403-0001-75	0403-0001-01	0403-0001-02	1.0 ± 0.30 ns	2.65 ns	3%
0403-0002-50	0403-0002-75	0403-0002-01	0403-0002-02	2.0 ± 0.30 ns	2.75 ns	3%
0403-0003-50	0403-0003-75	0403-0003-01	0403-0003-02	3.0 ± 0.30 ns	2.90 ns	3%
0403-0004-50	0403-0004-75	0403-0004-01	0403-0004-02	4.0 ± 0.30 ns	3.15 ns	3%
0403-0005-50	0403-0005-75	0403-0005-01	0403-0005-02	5.0 ± 0.30 ns	3.20 ns	5%
0403-0006-50	0403-0006-75	0403-0006-01	0403-0006-02	6.0 ± 0.30 ns	3.35 ns	5%
0403-0007-50	0403-0007-75	0403-0007-01	0403-0007-02	7.0 ± 0.35 ns	3.50 ns	5%
0403-0008-50	0403-0008-75	0403-0008-01	0403-0008-02	8.0 ± 0.40 ns	3.65 ns	5%
0403-0009-50	0403-0009-75	0403-0009-01	0403-0009-02	9.0 ± 0.45 ns	3.80 ns	5%
0403-0010-50	0403-0010-75	0403-0010-01	0403-0010-02	10.0 ± 0.50 ns	4.00 ns	5%
0403-0011-50	0403-0011-75	0403-0011-01	0403-0011-02	11.0 ± 0.55 ns	4.25 ns	5%
0403-0012-50	0403-0012-75	0403-0012-01	0403-0012-02	12.0 ± 0.60 ns	4.50 ns	5%
0403-0013-50	0403-0013-75	0403-0013-01	0403-0013-02	13.0 ± 0.65 ns	4.75 ns	5%
0403-0014-50	0403-0014-75	0403-0014-01	0403-0014-02	14.0 ± 0.70 ns	5.00 ns	5%
0403-0015-50	0403-0015-75	0403-0015-01	0403-0015-02	15.0 ± 0.75 ns	5.25 ns	5%
0403-0020-50	0403-0020-75	0403-0020-01	0403-0020-02	20.0 ± 1.00 ns	6.10 ns	5%
0403-0025-50	0403-0025-75	0403-0025-01	0403-0025-02	25.0 ± 1.25 ns	7.50 ns	5%
0403-0030-50	0403-0030-75	0403-0030-01	0403-0030-02	30.0 ± 1.50 ns	9.00 ns	6%
0403-0035-50	0403-0035-75	0403-0035-01	0403-0035-02	35.0 ± 1.75 ns	10.50 ns	6%
0403-0040-50	0403-0040-75	0403-0040-01	0403-0040-02	40.0 ± 2.00 ns	12.50 ns	6%
0403-0045-50	0403-0045-75	0403-0045-01	0403-0045-02	45.0 ± 2.25 ns	13.50 ns	6%
0403-0050-50	0403-0050-75	0403-0050-01	0403-0050-02	50.0 ± 2.50 ns	15.00 ns	6%

Electrical Characteristics

Delay Measurement	50% Levels
Rise Time Measurement	10%-90% Levels
Distortion	± 10%
Insulation Resistance @ 50 Vdc	10K Megohms Min.
Dielectric Withstanding Voltage	50 Vdc
Operating Temperature Range	-55°C to +125°C
Temperature Coefficient of Delay	100 PPM/°C Max.
Minimum Input Pulse Width	3 x Trout or 5 ns W.I.G.
Maximum Duty Cycle	60%

Test Conditions @ 25°C

Ein	Pulse Voltage	1 Volt Typical
Trin	Rise Time	2.0 ns (10%-90%)
PW	Pulse Width	3 x Total Delay
PP	Pulse Period	4 x Pulse Width

Notes

Transfer molded for better reliability
 Compatible with ECL & TTL circuits
 Terminals: Electro-Tin plate phosphor bronze
 Performance warranty is limited to specified parameters listed

**Other Delays and Impedances Available
Consult Sales**

Specifications subject to change without notice.

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