

# Super Barrier Rectifier TM

Using state-of-the-art SBR IC process technology, the following features are made possible in a single device:

Major ratings and characteristics

Characteristics	Values	Units
$I_{F(AV)}$ Rectangular Waveform	40	А
V <sub>RRM</sub>	60	V
V <sub>F</sub> @20A, Tj=125 <sup>0</sup> C	0.48	V, typ
Tj (operating/storage)	-65 to 150	°C

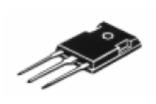
#### ELECTRICAL:

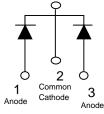
- \* Low Forward Voltage Drop
- \* Reliable High Temperature Operation
- \* Super Barrier Design
- \* Softest, fast switching capability
- \* 150°C Operating Junction Temperature

## Device optimized for low forward voltage drop to maximize efficiency in Power Supply applications

#### MECHANICAL:

\* Molded Plastic TO-3P package





### Maximum Ratings and Electrical Characteristics

	SYMBOL			UNITS
DC Blocking Voltage Working Peak Reverse Voltage Peak Repetitive Reverse Voltage	V <sub>rm</sub> V <sub>rwm</sub> V <sub>rrm</sub>	60		Volts
Average Rectified Forward Current (Rated V <sub>R</sub> -20Khz Square Wave) - 50% duty cycle	Io	40		Amps
Peak Forward Surge Current - 1/2 60hz	I <sub>FSM</sub>	300		Amps
Peak Repetitive Reverse Surge Current (2uS-1Khz)	I <sub>RRM</sub>	3		Amps
Instantaneous Forward Voltage (per leg) I <sub>F</sub> = 20A; T <sub>J</sub> = 25°C I <sub>F</sub> = 20A; T <sub>J</sub> = 125°C	Vŗ	Тур  	Max 0.69 0.60	Volts
Maximum Instantaneous Reverse Current at Rated $V_{RM}$ $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	I <sub>R</sub>	Тур  	Max 0.5 100	mA mA
Maximum Rate of Voltage Change (at Rated $V_R$ )	dv/dt	10,000		V/uS
Maximum Thermal Resistance JC (per leg)	Rθ <sub>JC</sub>	2		°C/W
Operating and Storage Junction Temperature	TJ	-65 to +150		°C

\* Pulse width < 300 uS, Duty cycle < 2%

## APD Semiconductor, Inc.

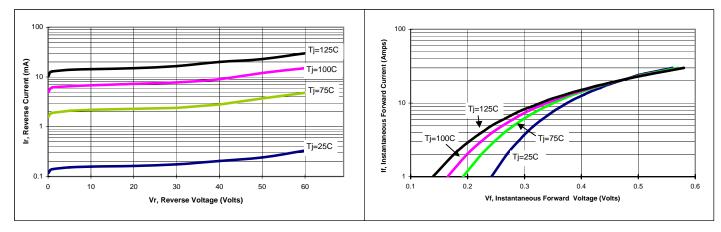


Figure 1: Typical Reverse Current (per leg)

Figure 2: Typical Forward Voltage (per leg)

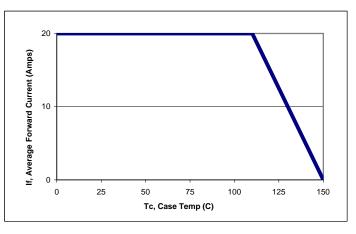


Figure 3: Current Derating, Case (per leg)

APD SEMICONDUCTOR reserves the right to make changes without further notice to any products herein. APD SEMICONDUCTOR makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APD SEMICONDUCTOR assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APD SEMICONDUCTOR data sheets and/or specifications can and to vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APD SEMICONDUCTOR does not convey any license under its patent rights nor the rights of others. APD SEMICONDUCTOR products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to surgical implant into the body, or other purchase or use APD SEMICONDUCTOR products for any such unintended or unauthorized application, Buyer shall indemnify and hold APD SEMICONDUCTOR and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized due use, even if such claim all egaes that APD SEMICONDUCTOR was negligent regarding the design or manufacture of the part.

APD Semiconductor, Inc.

1 Lagoon Drive, Suite 410, Redwood City, CA 94065, USA Ph: 650 508 8896 FAX: 650 508 8865 Homepage: www.apdsemi.com email: info@apdsemi.com