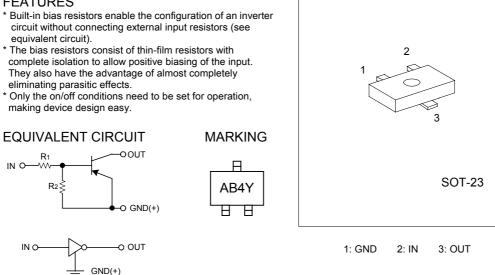
PNP DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

FEATURES

- circuit without connecting external input resistors (see
- complete isolation to allow positive biasing of the input. They also have the advantage of almost completely
- making device design easy.



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply voltage	Vcc	-50	V	
Input voltage	Vı	-40 ~ +6	V	
Output current	lo	-70	mA	
	IC (Max.)	-100		
Power Dissipation	Po	200	mW	
Junction temperature	Tj	150	$^{\circ}$	
Storage temperature	Tstg	-55 ~ +150	$^{\circ}$	

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VI (off)	Vcc=-5V, Io=-100 μ A			-0.3	.,
VI (on)	Vo=-0.3V, Io=-1mA	-1.4			V
Vo (on)	Io/I=-5mA/-0.25mA		-0.1	-0.3	V
lı	VI=-5V			-0.88	mΑ
IO (off)	Vcc=-50V, Vi=0V			-0.5	μA
Gı	Vo=-5V, Io=-5mA	68			
R1		7	10	13	ΚΩ
R2/R1		3.7	4.7	5.7	
f⊤	Vce=-10V, Ie=5mA, f=100MHz *		250		MHz
	VI (off) VI (on) VO (on) II IO (off) GI R1 R2/R1	Vi (off) Vcc=-5V, lo=-100 μA Vi (on) Vo=-0.3V, lo=-1mA Vo (on) lo/li=-5mA/-0.25mA II Vi=-5V Io (off) Vcc=-50V, Vi=0V Gi Vo=-5V, lo=-5mA R1 R2/R1	Vi (off) Vcc=-5V, Io=-100 μ A Vi (on) Vo=-0.3V, Io=-1mA -1.4 VO (on) Io/Ii=-5mA/-0.25mA -1.4 II Vi=-5V Vi=-5V IO (off) Vcc=-50V, Vi=0V 0 GI Vo=-5V, Io=-5mA 68 R1 7 R2/R1 3.7	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

^{*} Transition frequency of the device

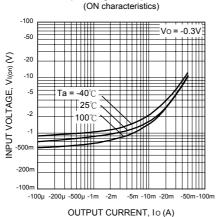


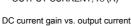
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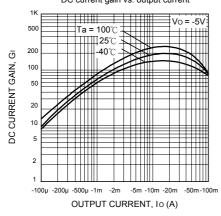
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ELECTRICAL CHARACTERISTIC CURVES

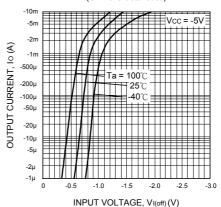
Input voltage vs. output current



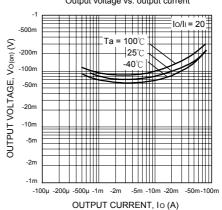




Output current vs. input voltage (OFF characteristics)



Output voltage vs. output current



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