

# **SRC1202N**

**NPN Silicon Transistor** 

### **Descriptions**

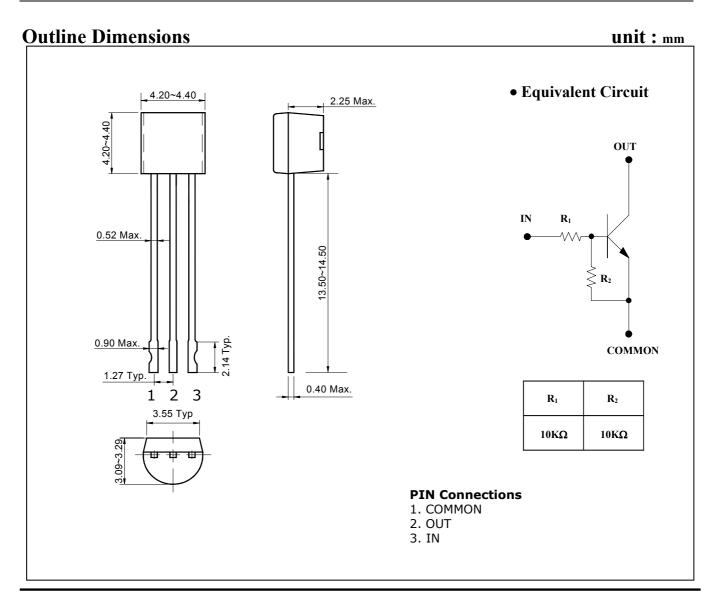
- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary pair with SRA2202N

### **Ordering Information**

Type NO.	Marking	Package Code	
SRC1202N	SRC1202	TO-92N	



## **Absolute Maximum Ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V <sub>I</sub>	30,-10	V
Output current	$I_{O}$	100	mA
Power dissipation	$P_{D}$	400	mW
Junction temperature	T <sub>1</sub>	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

## **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = 50V, V_I = 0$	-	-	500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =5V, I <sub>O</sub> =10mA	50	80	-	-
Output voltage	V <sub>O(ON)</sub>	$I_O$ =10mA, $I_I$ =0.5mA	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>O</sub> =0.2V, I <sub>O</sub> =5mA	-	1.8	2.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$ , $I_0 = 0.1$ mA	1.0	1.2	-	V
Transition frequency	$f_T^*$	$V_0$ =10V, $I_0$ =5mA, f=1MHz	-	200	-	MHz
Input current	$I_{\rm I}$	$V_I=5V$ , $I_O=0$	-	-	0.88	mA
Input resistor (Input to base)	$R_1$	-	7	10	13	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	7	10	13	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

#### **Electrical Characteristic Curves**

Fig. 1 I<sub>O</sub> - V<sub>I(ON)</sub>

100

Vo=0.2V

100°C

Ta=25°C

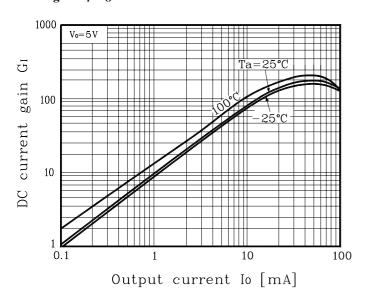
100°C

0.1

Input on voltage Vi(on) [V]

Fig. 2 I<sub>O</sub> - V<sub>I(OFF)</sub>

Fig. 3  $G_I$  -  $I_O$ 



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