SILICON EPITAXIAL PLANAR DIODE

Features

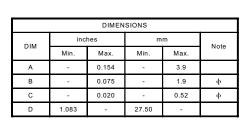
GOOD-ARK

Silicon Epitaxial Planar Diode fast switching diode.

<u>DO-35</u>

†с — п

This diode is also available in MiniMELF case with the type designation LL4448.



В

n

Absolute Maximum Ratings (Ta=25°C)

	Symbols	Values	Units	
Reverse Voltage	V _R	75	Volts	
Peak reverse voltage	V _{RM}	100	Volts	
Rectified current (Average) Half wave rectification with Resist. Load at T _{amb} =25 ℃and f≥50Hz	I _o	150 ¹⁾	mA	
Surge forward current at t<1s and $T_j {=} 25{\rm ^\circ C}$	I _{FSM}	500	mA	
Power dissipation at $T_{_{amb}}{=}25{\rm ^\circ C}$	P _{tot}	500 ¹⁾	mW	
Junction Temperature	T _i	200	°C	
Storage temperature range	Τ _s	-65 to +200	°C	

Note:

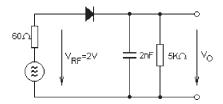
(1) Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature

Characteristics at T_=25°C

	Symbols	Min.	Тур.	Max.	Units
Forward voltage at I _F =5mA at I _F =100mA	V V _F	0.62	-	0.72 1	Volt
Leakage current at V =20V at V = 75V at V = 75V at V = 75V at V = 20V, T = 150 $^\circ\mathrm{C}$	l _R I _R I _R	- - -	- -	25 5 50	nA uA uA
Reverse breakdown voltage tested with 100uA pulses	V _{(BR)R}	100	-	-	Volts
Capacitance at $V_{\rm F} = V_{\rm R} = 0$	C _{tot}	-	-	4	ρF
Reverse recovery time from I_{\rm F}=10mA to I_{\rm R}=1mA, V_{\rm R}=6V, R_{\rm L}=100\Omega	t,,	-	-	4	nS
Thermal resistance junction to ambient Air	R _{thA}	-	-	0.35 1)	K/mW
Rectification efficiency at f=100MHz, V_{RF} =2V	η _ν	0.45	-	-	-

Note:

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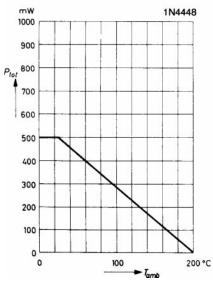


Rectification efficiency measurement circuit

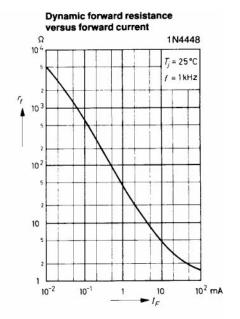
RATINGS AND CHARACTERISTIC CURVES

Forward characteristics

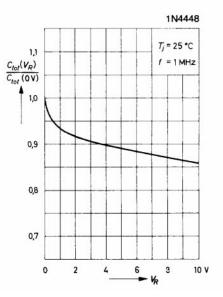
Admissible power dissipation versus ambient temperature



Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature



Relative capacitance versus reverse voltage



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