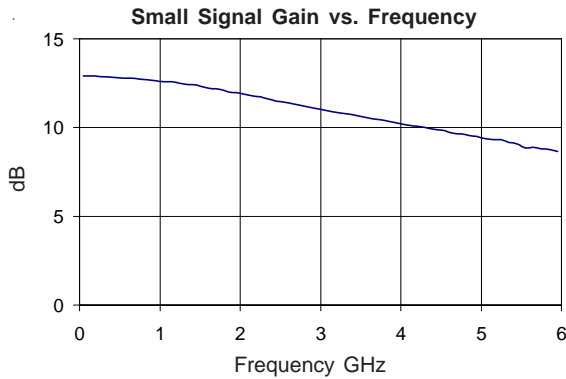




Product Description

The SGA-0163 is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring 1 micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only 2 DC-blocking capacitors, a bias resistor and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no antimony trioxide nor halogenated fire retardants.



Preliminary

SGA-0163

SGA-0163Z RoHS Compliant & Green Package

DC-4500 MHz, Silicon Germanium Cascadeable Gain Block



Product Features

- Now available in Lead Free, RoHS Compliant, & Green Packaging
- DC-4500 MHz Operation
- Single Voltage Supply
- Low Current Draw: 8mA at 2.1V typ.
- High Output Intercept: 10 dBm typ. at 1900 MHz

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameter	Frequency	Units	Min.	Typ.	Max.
P_{1dB}	Output Power at 1dB Compression	850 MHz	dBm		-1.8	
		1950 MHz	dBm		-1.8	
		2400 MHz	dBm		-2.4	
IP_3	Third Order Intercept Point	850 MHz	dBm		9.4	
		1950 MHz	dBm		9.8	
		2400 MHz	dBm		9.2	
S_{21}	Small Signal Gain	850 MHz	dB		12.7	
		1950 MHz	dB		12.0	
		2400 MHz	dB		11.6	
BW_{3dB}	3dB Bandwidth		MHz		4500	
$VSWR_{IN}$	Input VSWR	DC - 4500MHz	-		1.6:1	
$VSWR_{OUT}$	Output VSWR	DC - 4500MHz	-		1.3:1	
S_{12}	Reverse Isolation	850 MHz	dB		17.6	
		1950 MHz	dB		18.1	
		2400 MHz	dB		18.3	
NF	Noise Figure	1950 MHz	dB		4.6	
V_D	Device Operating Voltage		V		2.1	
I_D	Device Operating Current		mA	6	8	10
$R_{TH, j-l}$	Thermal Resistance (junction - lead)		°C/W		255	

Test Conditions:

$V_S = 5 V$

$R_{BIAS} = 360 \text{ Ohms}$

$I_D = 5 \text{ mA Typ.}$

$T_L = 25^\circ C$

$OIP_3 \text{ Tone Spacing} = 1 \text{ MHz, } P_{out} \text{ per tone} = -17 \text{ dBm}$

$Z_S = Z_L = 50 \text{ Ohms}$

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Key parameters, at typical operating frequencies:

Parameter	Typical 25°C	Unit	Test Condition ($I_D = 8\text{mA}$, unless otherwise noted)
100 MHz			
Gain	12.9	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm $Z_s = 50\ \text{Ohms}$
Output IP3	9.4	dBm	
Output P1dB	-1.5	dBm	
Input Return Loss	12.5	dB	
Reverse Isolation	17.3	dB	
Noise Figure	4.6	dB	
500 MHz			
Gain	12.8	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm $Z_s = 50\ \text{Ohms}$
Output IP3	9.5	dBm	
Output P1dB	-1.5	dBm	
Input Return Loss	12.7	dB	
Reverse Isolation	17.4	dB	
Noise Figure	4.6	dB	
850 MHz			
Gain	12.7	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm $Z_s = 50\ \text{Ohms}$
Output IP3	9.4	dBm	
Output P1dB	-1.8	dBm	
Input Return Loss	12.8	dB	
Reverse Isolation	17.6	dB	
Noise Figure	4.7	dB	
1950 MHz			
Gain	12.0	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm $Z_s = 50\ \text{Ohms}$
Output IP3	9.8	dBm	
Output P1dB	-1.8	dBm	
Input Return Loss	12.4	dB	
Reverse Isolation	18.1	dB	
Noise Figure	4.6	dB	
2400 MHz			
Gain	11.6	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm
Output IP3	9.2	dBm	
Output P1dB	-2.5	dBm	
Input Return Loss	12.1	dB	
Reverse Isolation	18.3	dB	
3500 MHz			
Gain	10.6	dB	Tone spacing = 1 MHz, Pout per tone = -17dBm
Output IP3	9.3	dBm	
Output P1dB	-2.7	dBm	
Input Return Loss	11.8	dB	
Reverse Isolation	18.5	dB	

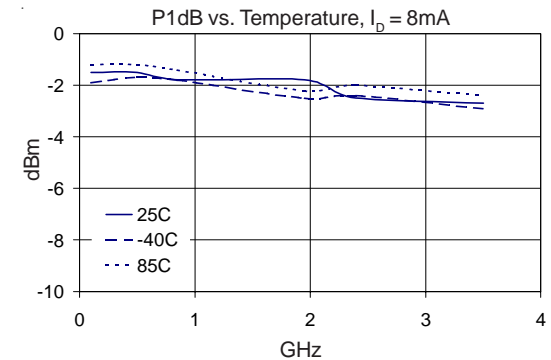
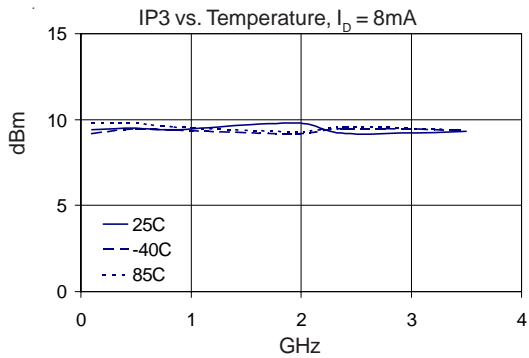
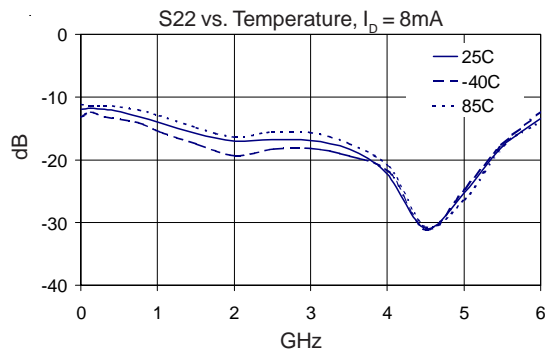
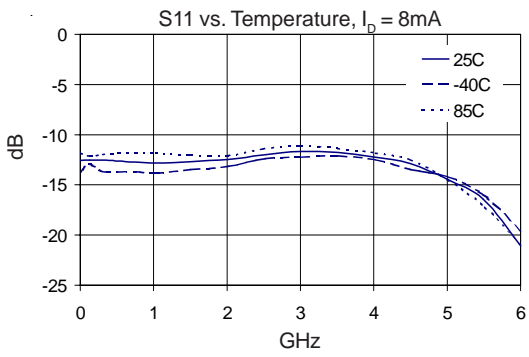
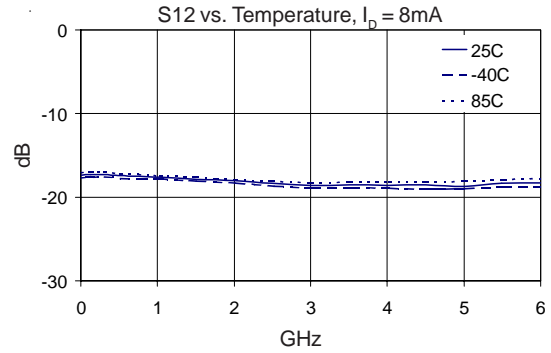
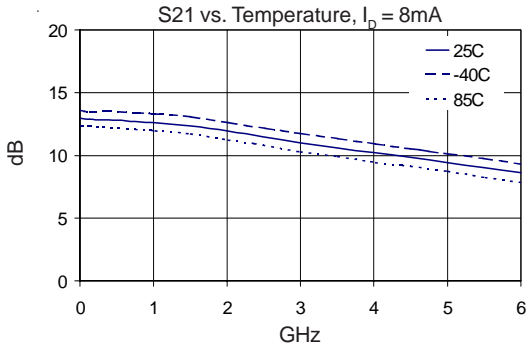
Absolute Maximum Ratings

Parameter	Absolute Limit
Max. Device Current (I_D)	16 mA
Max. Device Voltage (V_D)	6 V
Max. RF Input Power	-4 dBm
Max. Junction Temp. (T_J)	+150°C
Operating Temp. Range (T_L)	-40°C to +85°C
Max. Storage Temp.	+150°C

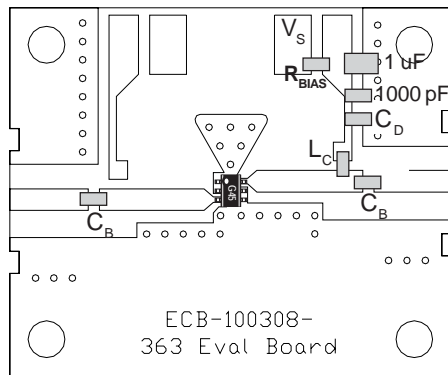
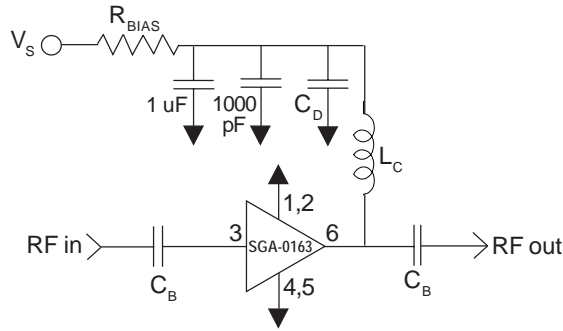
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias conditions should also satisfy the following expression:

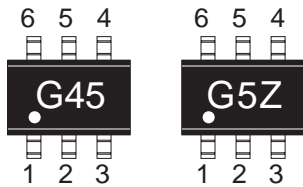
$$I_D V_D < (T_J - T_L) / R_{TH} \text{ } ^\circ\text{C}$$



Basic Application Circuit



Part Identification Marking



Caution: ESD sensitive
Appropriate precautions in handling, packaging and testing devices must be observed.

Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C _B	220 pF	100 pF	68 pF	56 pF	39 pF
C _D	100 pF	68 pF	22 pF	22 pF	15 pF
L _C	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for I_D=8mA

$$R_{BIAS} = (V_S - V_D) / I_D$$

Supply Voltage(V _S)	5 V	7.5 V	9 V	12 V
R _{BIAS}	360 Ω	680 Ω	820 Ω	1.2K Ω

Note: R_{BIAS} provides DC bias stability over temperature.

Mounting Instructions

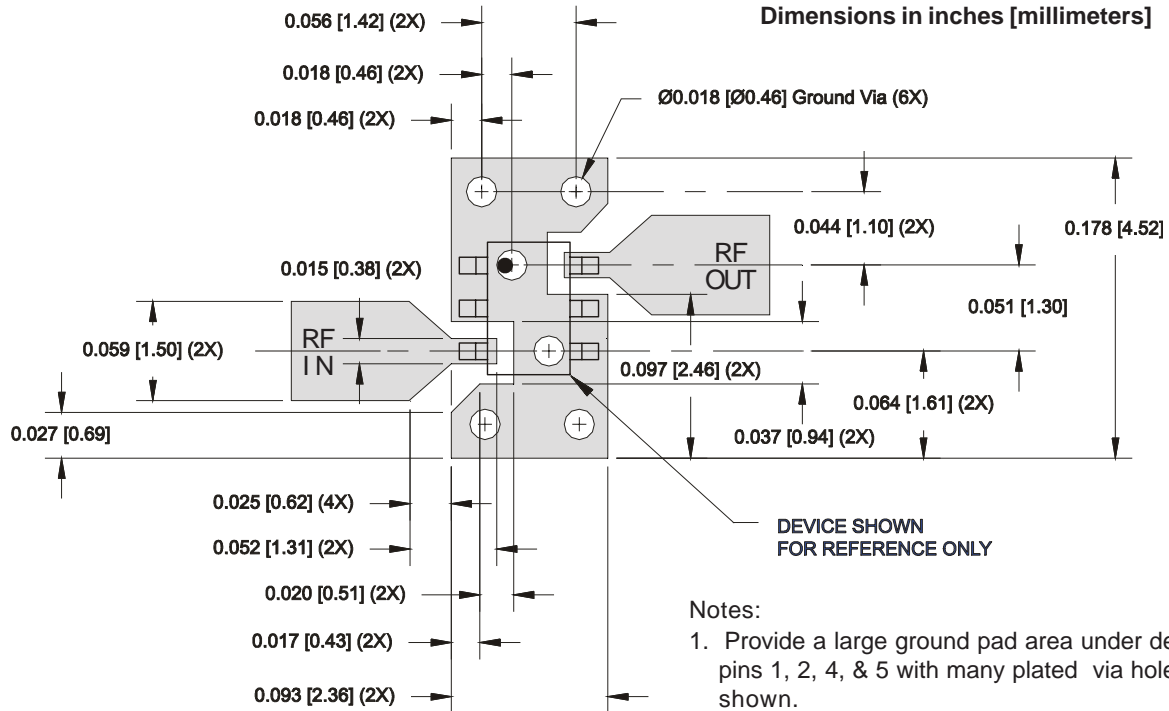
1. Use a large ground pad area near device pins 1, 2, 4, and 5 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
1, 2, 4, 5	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
6	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Part Number	Reel Size	Devices/Reel
SGA-0163	7"	3000
SGA-0163Z	7"	3000

SOT-363 PCB Pad Layout

Dimensions in inches [millimeters]



Notes:

1. Provide a large ground pad area under device pins 1, 2, 4, & 5 with many plated via holes as shown.
2. Dimensions given for 50 Ohm RF I/O lines are for 31 mil thick Getek. Scale accordingly for different board thicknesses and dielectric constants.
3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.

SOT-363 Nominal Package Dimensions

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.

