MIL-PRF-38534 CERTIFIED



VERY HIGH CURRENT, LOW DROPOUT VOLTAGE REGULATORS

5151 SERIES

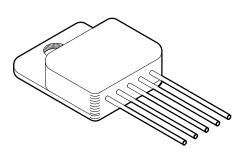
(315) 701-6751

4707 Dey Road Liverpool, N.Y. 13088

FEATURES:

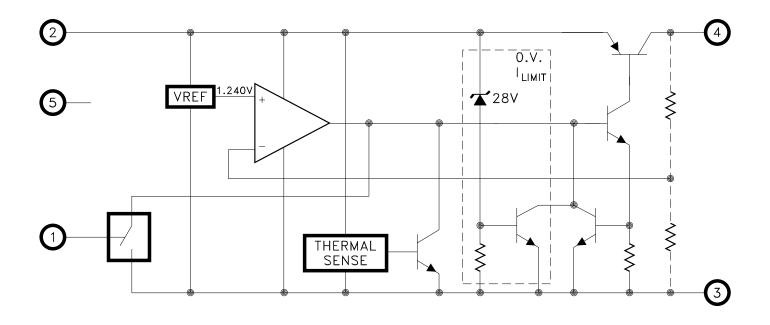
- Electrically Isolated Top Tab or Z Tab SIP
- Extremely Low Dropout Voltage: 370mV @ 5 Amps
- Available in +1.5V, +1.7V, +1.9V, +2.5V, +3.3V, +5.0V and +12.0V
- TTL Level Enable Pin: Zero Current Shutdown Mode
- Reverse Battery and Load Dump Protection
- · Low Ground Current: 80mA Typical at Full Load
- 1% Maximum Guaranteed Accuracy
- Output Current to 5 Amps
- Contact MSK for MIL-PRF-38534 Qualification Status

DESCRIPTION:



The MSK 5151 series voltage regulators are available in +1.5V, +1.7V, +1.9V, +2.5V, +3.3V, +5.0V and +12.0V configurations. All boast ultra low dropout specifications due to the utilization of a super PNP output pass transistor with monolithic technology. Dropout voltages of 370mV at 5 amps are typical in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1% maximum output voltage tolerance. The series also offers a TTL/CMOS compatible on/off enable function. The MSK 5151 series is packaged in a space efficient 5 pin power SIP available in two styles with three lead bend options.

EQUIVALENT SCHEMATIC



TYPICAL APPLICATIONS

- High Efficiency, Very High Current Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- Switching Power Supply Post Regulators
- Battery Powered Equipment

PIN-OUT INFORMATION

- 1 Enable
- 2 Vin
- 3 Ground
- 4 Vout
- 5 NC

ABSOLUTE MAXIMUM RATINGS

V_{INP}	Input Voltage (100mS 1%D.C.)-20V to +60V
Vin	Input Voltage
V_{EN}	Enable Voltage
Ιουτ	Output Current

9

Ts⊤	Storage Temperature Range	-65°C to +150°C
TLD	Lead Temperature	300°C
	(10 Seconds Soldering)	
ТJ	Operating Temperature	
	MSK 5151 Series	40°C to +85°C
	MSK 5151H/E Series	-55°C to +125°C

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions $\textcircled{1}$		Group A	MSK 5151H/E SERIES		MSK 5151 SERIES				
Falainetei			Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Output Voltage Tolerance	Iout = 10mA; VIN = Vout + 1V		1	-	±0.5	±1.0	-	±0.5	±1.0	%
			2,3	-	±1.0	±2.0	-	-	-	%
Dropout Voltage	② ΔVout = -1%; Iout=250m A		1	-	125	250	-	125	275	mV
Diopour voltage	② ΔVout = -1%; lout = 5A		1	-	370	600	-	370	625	mV
Load Regulation (8)	$V_{IN} = V_{OUT} + 1V$		1	-	±0.2	±1.0	-	±0.2	±1.2	%
	$10mA \le IOUT \le 4.5A$		2,3	-	±0.3	±2.0	-	±0.3	-	%
Line Regulation	$(Vout + 1V) \le Vin \le 26V$		1	-	±0.05	±0.5	-	±0.05	±0.6	%
	louт = 10mA		2,3	-	±0.5	±1.0	-	±0.5	-	%
Output Current Limit ②	Vout = $0V$; Vin = Vout + $1V$		-	-	7.5	10	-	7.5	10	А
Ground Current (2)	VIN = VOUT + 1V; IOUT = 2.5A		-	-	25	60	-	25	65	mA
	$V_{IN} = V_{OUT} + 1V; I_{OUT} = 5A$		-	-	80	-	-	80	-	mA
Output Noise 2	$CL = 20\mu F$; $10Hz \le f \le 100KHz$		-	-	400	-	-	400	-	μV
Enable Input Voltage (2)	HIGH/ON		1	2.4	1.2	-	2.4	1.2	-	V
		LOW/OFF	1	-	1.2	0.8	-	1.2	0.8	V
Enable Input Current (2)		HIGH/ON	1	-	100	600	-	100	600	μA
		LOW/OFF	1	-	-	2	-	-	2	μA
Shutdown Output Current(2) VENABLE $\leq 0.8V$		/	-	-	10	500	-	10	500	μA
Thermal Resistance (2) Junction to Case @12		125°C	-	-	1.1	1.5	-	1.1	1.7	°C/W
Thermal Shutdown 2 TJ			-	-	135	-	-	135	-	°C

NOTES:

- $\begin{pmatrix} 1 \\ 2 \end{pmatrix}$ Output decoupled to ground using 33µF minimum capacitance unless otherwise specified. 2 This parameter is guaranteed by design but need not be tested.
- Typical parameters are representative of actual device performance but are for reference only.
- (a) All output parameters are tested using a low duty cycle pulse to maintain $T_J = T_C$. (4) Industrial grade and "E" suffix devices shall be tested to subgroup 1 unless otherwise specified. (5) Military grade devices ('H' suffix) shall be 100% tested to subgroups 1,2 and 3. (6) Subgroup 1 $T_C = +25 \circ C$ (7) Contents 2 $T_C = +25 \circ C$

- $T_J = +125 \,^{\circ}C$ Subgroup 2
- Subgroup 3 $T_A = -55 \,^o C$
- ⑦ Please consult the factory if alternate output voltages are required.

 $\overset{\scriptstyle{\otimes}}{\otimes}$ Due to current limit, maximum output current may not be available at all values of VIN-VOUT and temperatures. See typical performance curves for clarification.

(9) Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

REGULATOR PROTECTION:

The MSK 5151 series is fully protected against reversed input polarity, overcurrent faults, overtemperature conditions (Pd) and transient voltage spikes of up to 60V. If the regulator is used in dual supply systems where the load is returned to a negative supply, the output voltage must be diode clamped to ground.

OUTPUT CAPACITOR:

The output voltage ripple of the MSK 5151 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next, but a minimum of 20μ F is recommended for optimum performance. This capacitor need not be an expensive low ESR type: aluminum electrolytics are adequate. In fact, extremely low ESR capacitors may contribute to instability. Tantalum capacitors are recommended for systems where fast load transient response is important. Transient load response can also be improved by placing a capacitor directly across the load.

LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load.

ENABLE PIN:

The MSK 5151 series of voltage regulators are equipped with a TTL compatible ENABLE pin. A TTL high level on this pin activates the internal bias circuit and powers up the device. A TTL low level on this pin places the controller in shutdown mode and the device draws approximately 10μ A of quiescent current. If the enable function is not used, simply connect the enable pin to the input.

DEVICE/CASE CONNECTION:

The MSK 5151 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Since the case is electrically isolated from the internal circuitry, the package can be directly connected to a heat sink.

HEAT SINK SELECTION:

To select a heat sink for the MSK 5151, the following formula for convective heat flow may be used.

$$Tj = Pd x (R_{\theta}jc + R_{\theta}cs + R_{\theta}sa) + Ta$$

WHERE:

Tj = Junction Temperature Pd = Total Power Dissipation $R_{\theta jc}$ = Junction to Case Thermal Resistance $R_{\theta cs}$ = Case to Heat Sink Thermal Resistance $R_{\theta sa}$ = Heat Sink to Ambient Thermal Resistance Ta = Ambient Temperature

First, the power dissipation must be calculated as follows:

Power Dissipation = $(Vin - Vout) \times Iout$

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 125° C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (Resa).

EXAMPLE:

An MSK 5151-3.3 is configured for Vin = +5V and Vout = +3.3V. lout is a continuous 1A DC level. The ambient temperature is $+25^{\circ}C$. The maximum desired junction temperature is $125^{\circ}C$.

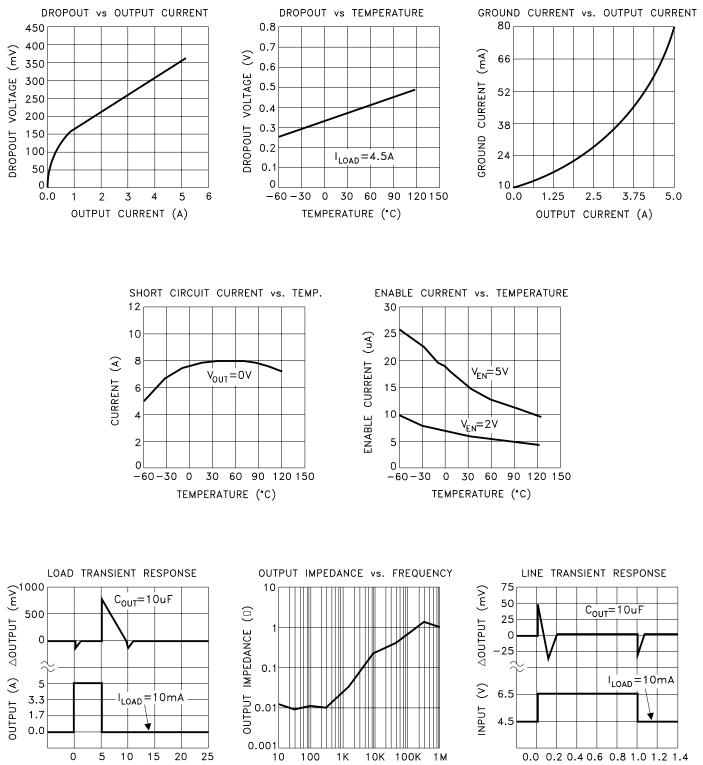
Solve for Resa:

$$R_{\theta}sa = \left[\frac{125^{\circ}C - 25^{\circ}C}{1.7W}\right] - 1.5^{\circ}C/W - 0.15^{\circ}C/W$$
$$= 57.17^{\circ}C/W$$

In this example, a heat sink with a thermal resistance of no more than $57^{\circ}C/W$ must be used to maintain a junction temperature of no more than $125^{\circ}C$.

TYPICAL PERFORMANCE CURVES

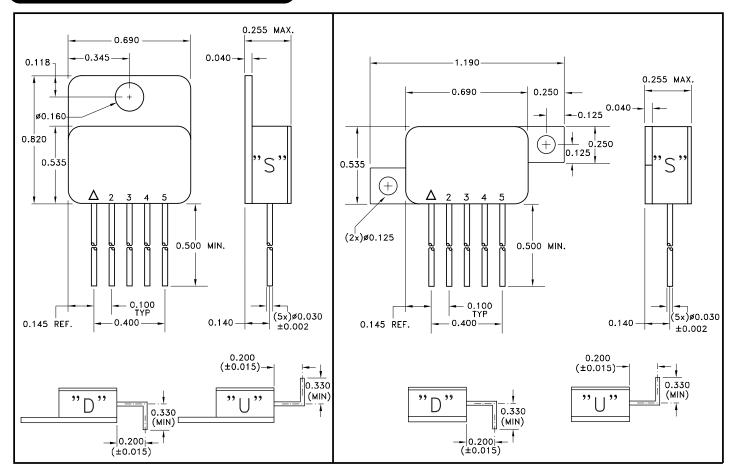
TIME (mS)



TIME (mS)

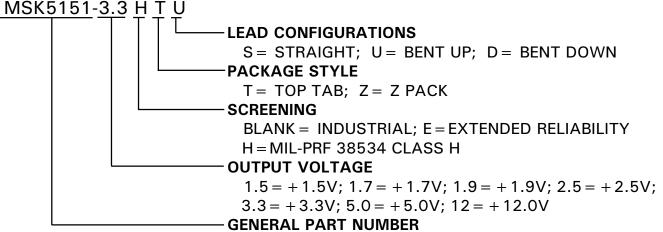
FREQUENCY (Hz)

MECHANICAL SPECIFICATIONS



NOTE: ALL DIMENSIONS ARE ± 0.010 INCHES UNLESS OTHERWISE LABELED. ESD Triangle indicates Pin 1.

ORDERING INFORMATION



The above example is a +3.3V, Military regulator using the top tab package with leads bent up.

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