

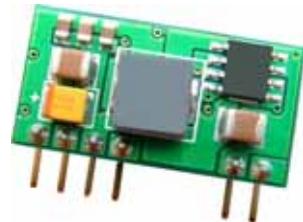
NON-ISOLATED DC/DC CONVERTERS

8.3V-14V Input 0.75V-5.0V/6A Output



V7BA-06A2Ax Series

- Non-Isolated
- High Efficiency
- High Power Density
- Fixed Frequency
- Remote On/Off
- Active Low/High (option)
- Under-Voltage Lockout (UVLO)
- OCP/SCP
- Wide Input Range
- Wide Trim Range
- Flexible Output Voltage Sequencing



Description

The Bel V7BA-06A2Ax modules are a series of non-isolated DC/DC converters that deliver up to 6A of output current with full load efficiency of 92% at 5.0V output. These modules provide precisely regulated voltage programmable via external resistor from 0.75V to 5.0V over a wide range of input voltage (8.3V-14V). These modules have a sequencing feature that enables designers to implement various types of output voltage sequencing when powering multiple voltages on a board. The open-frame construction and small footprint enable designers to develop cost and space-efficient solutions. Standard features include remote On/Off, over current protection, short current protection, wide input, and programmable output voltage.

Part Selection

Output Voltage	Input Voltage	Max. Output Current	Max. Output Power	Typical Efficiency	Model Number Active Low	Model Number Active High
0.75V - 5.0V	8.3V - 14V	6A	30.0W	92%	V7BA-06A2AL	V7BA-06A2A0

Note: Add "G" suffix at the end of the model number to indicate Tray Packaging.

Absolute Maximum Ratings

Parameter	Min	Typ	Max	Notes
Input Voltage (continuous)	-0.3V	-	15V	
Output Enable Terminal Voltage	-0.3V	-	15V	
Sequencing Voltage ¹	-0.3V	-	Vin	
Ambient Temperature	-40°C	-	85°C	
Storage Temperature	-55°C	-	125°C	

Notes: All specifications are typical at 25°C unless otherwise stated.

1. V7BA-06A2Ax series of modules include a sequencing feature that enables users to implement various types of output voltage sequencing in their applications. This is accomplished via an additional sequencing pin. When the sequencing feature is not used, tie the SEQ pin to Vin.

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Input Specifications

Parameter	Min	Typ	Max	Notes
Input Voltage	8.3V	12V	14V	
Input Current (full load)				
Vo=5.0V	-	2.75A	4.0A	
Vo=3.3V	-	1.85A	2.8A	
Vo=2.5V	-	1.45A	2.2A	
Vo=1.8V	-	1.05A	1.6A	
Vo=0.75V	-	0.55A	0.8A	
Input Current (no load)				
Vo=5.0V	-	-	100mA	
Vo=0.75V	-	-	20mA	
Remote Off Input Current	-	1mA	2mA	
Input Reflected Ripple Current (pk-pk)	-	120mA	-	Tested with two 100uF/25V tantalum input capacitors & simulated source impedance of 1uH, 5Hz to 20MHz.
Input Reflected Ripple Current (RMS)	-	40mA	-	
I ² t Inrush Current Transient	-	0.002A ² s	0.02A ² s	
Turn-on Voltage Threshold	-	8.1V	8.2V	
Turn-off Voltage Threshold	-	7.5V	8.0V	

Notes: All specifications are typical at 25°C unless otherwise stated.

Output Specifications

Parameter	Min	Typ	Max	Notes
Output Voltage Set Point	-2%Vo,set	-	2%Vo,set	Vin=12V, Io=Iomax, full load
Output Voltage Set Point	-2.5%Vo,set	-	3.5%Vo,set	Over all operating input voltages, resistive loads and temperature conditions
Adjustment Range Selected by External Resistor or Voltage	0.7525V	-	5.0V	
Load Regulation	-0.5%Vo,set	0.4%Vo,set	0.5%Vo,set	Io=Io, min to Io, max
Line Regulation	-0.4%Vo,set	0.3%Vo,set	0.4%Vo,set	Vin=Vin, min to Vin, max
Regulation Over Temperature (-40°C to +85°C)	-	0.5%Vo,set	-	Tref=Ta, min to Ta, max
Output Current	0A	-	6A	
Current Limit Threshold	7.2A	-	18A	
Short Circuit Surge Transient	-	0.25A ² s	-	
Ripple and Noise (pk-pk)				
Vo=0.75V	-	20mV	-	Tested with 0-20MHz, with 10uF/10V tantalum capacitor & 1uF/10V TDK ceramic capacitor at the output.
Vo=3.3V	-	60mV	-	
Vo=5.0V	-	75mV	-	
Ripple and Noise (RMS)				
Vo=0.75V	-	7mV	-	
Vo=3.3V	-	20mV	-	
Vo=5.0V	-	25mV	-	
Turn on Time	-	8mS	10mS	
Overshoot at Turn on	-	0%	3%	
Output Capacitance				
ESR ≥ 1mohm	0uF	-	1000uF	
ESR ≥ 10mohm	0uF	-	3000uF	

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Output Specifications (Continued)

Parameter	Min	Typ	Max	Notes
Transient Response				
50% ~ 100% Max Load	Vo = 0.75V - 5V	-	200mV	di/dt=2.5A/uS; Vin=12V; and with 10uF/10V tantalum capacitor & 1uF/10V ceramic capacitor at the output.
Settling Time		-	50uS	
100% ~ 50% Max Load		-	200mV	
Settling Time		-	50uS	

Note: All specifications are typical at nominal input (Vin=12V), full load at 25°C unless otherwise stated.

General Specifications

Parameter	Min	Typ	Max	Notes
Efficiency				Measured at Vin=12V, full load
	Vo=5.0V	87%	90%	
	Vo=3.3V	85%	88%	
	Vo=2.5V	82%	85%	
	Vo=1.8V	80%	83%	
Switching Frequency	Vo=0.75V	68%	71%	
	250KHz	300KHz	350KHz	
	-	135°C	-	
	Output Trim Range (wide trim)	0.7525V	-	5V
			3,266,517 hours	Calculated Per Bell Core TR-332 (Io = Nominal; Ta = 25°C)
Dimensions	Inches (L × W × H)	1 x 0.5 x 0.243		
	Millimeters (L × W × H)	25.4 x12.7 x 6.16		
Weight	-	3g	-	

Note: All specifications are typical at 25°C unless otherwise stated.

Control Specifications

Parameter	Min	Typ	Max	Notes
Signal Low (Unit Off)	-0.3V	-	0.4V	V7BA-06A2A0; Remote On/Off pin open, Unit on.
Signal High (Unit On)	2.5V	-	14V	
Signal Low (Unit On)	-0.3V	-	0.4V	V7BA-06A2AL; Remote On/Off pin open, Unit on.
Signal High (Unit Off)	2.5V	-	14V	
Sequencing Voltage	0V	-	Vin	Sequencing Voltage applied on SEQ pin should be higher than output voltage.
Sequencing Slew Rate Capability	-	-	2V/mS	
Sequencing Delay Time	10mS	-	-	Delay from Vin, min to application of voltage on SEQ pin
Tracking Accuracy	Power-Up	100mV	200mV	
		200mV	400mV	

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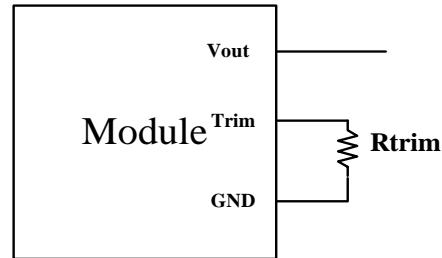
0.75V-5.0V/6A Output



Output Trim Equations

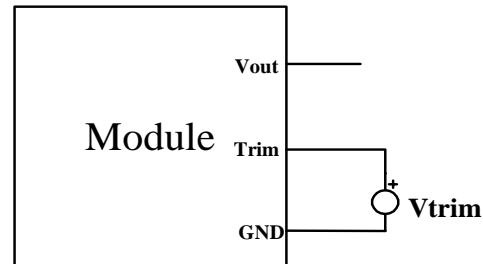
Equation for calculating the trim resistor (in kΩ) given the desired adjusted voltage (V_{adj}) is shown below. The Trim Up resistor should be connected between the Trim pin and Ground.

$$R_{trim} = \frac{10.507}{V_{adj} - 0.7525} - 1$$



Equation for calculating the trim voltage (in V) given the desired adjusted voltage (V_{adj}) is shown below. The Trim Up voltage should be connected between the Trim pin and Ground.

$$V_{trim} = 0.7 - 0.0667 \times (V_{adj} - 0.7525)$$



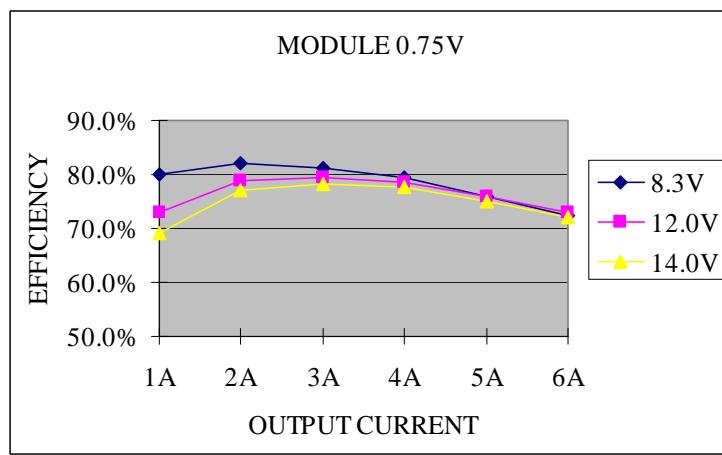
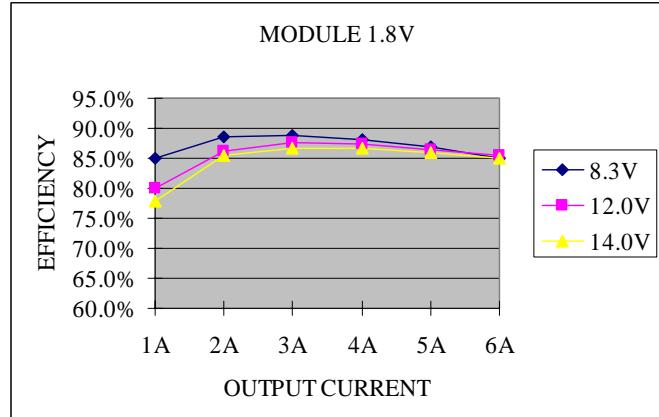
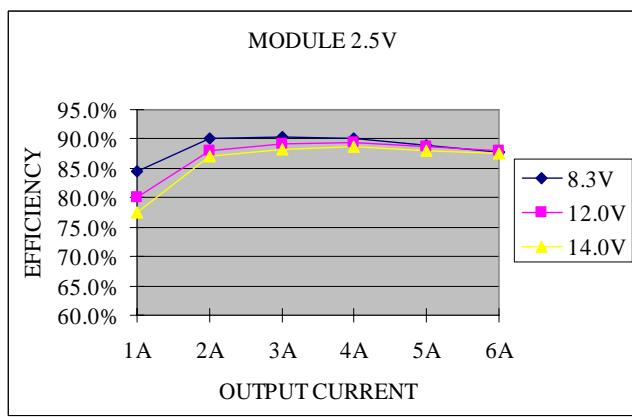
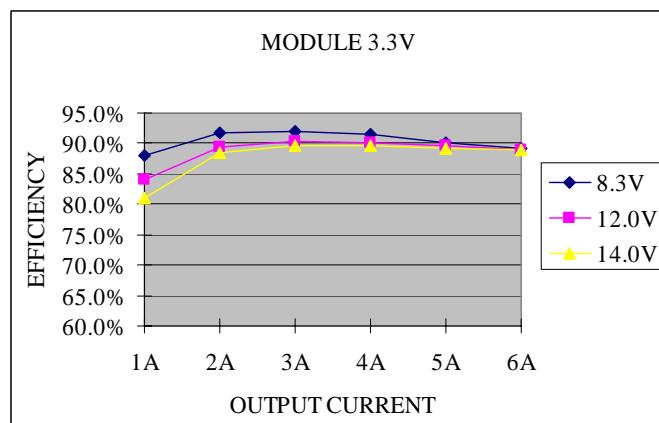
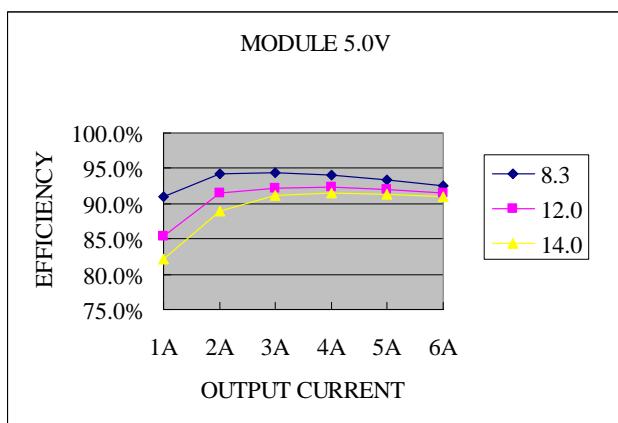
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Efficiency Data



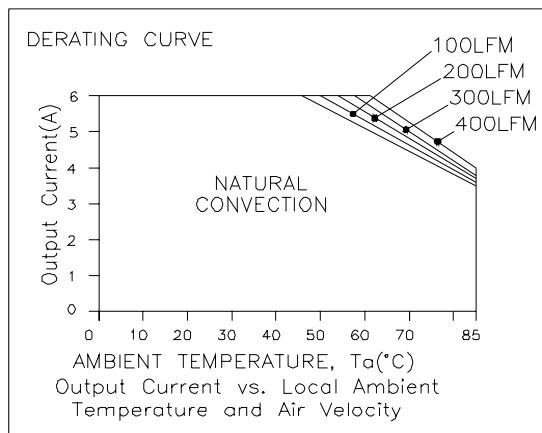
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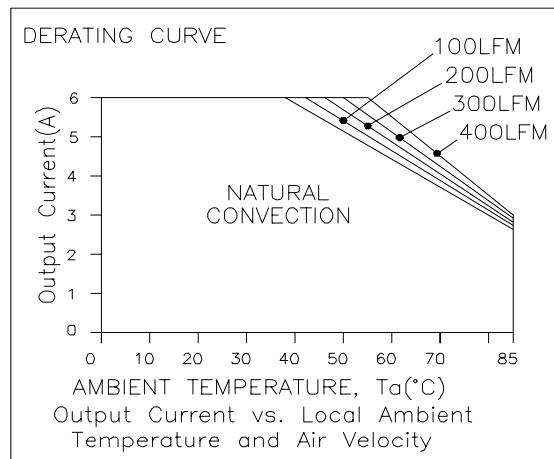
0.75V-5.0V/6A Output



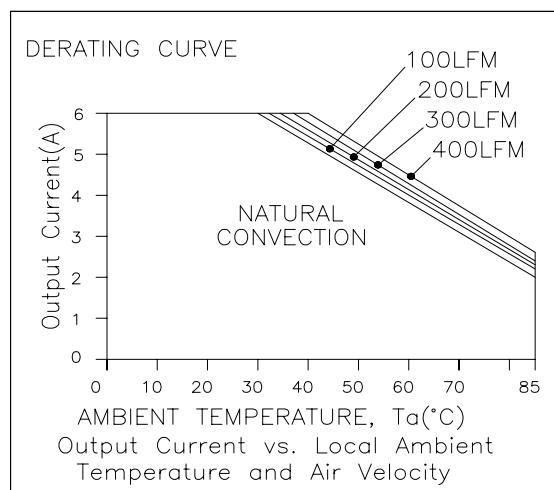
Thermal Derating Curves



Vin=12V, Vo=0.75V



Vin=12V, Vo=2.5V

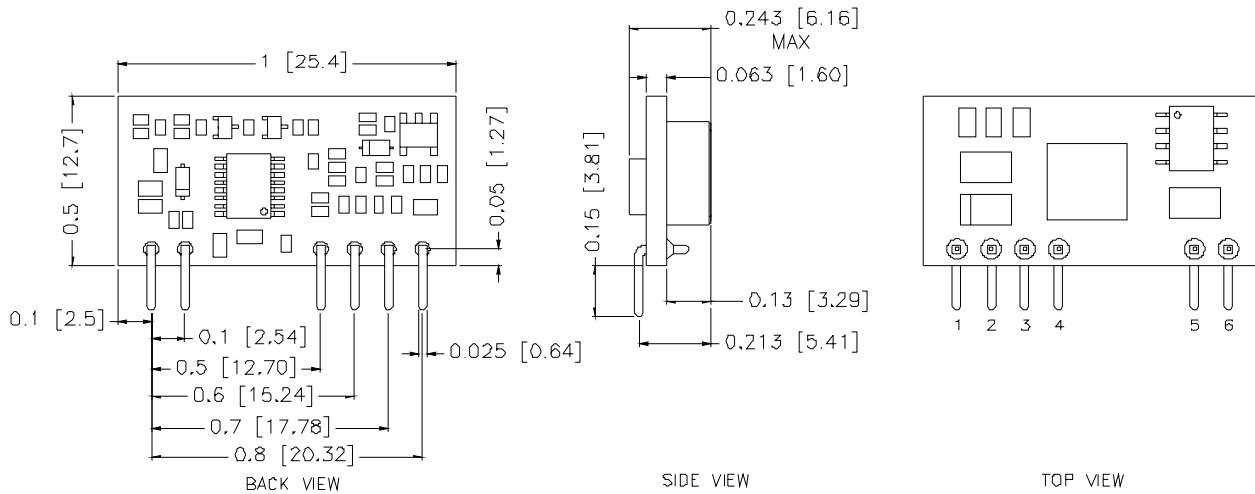


Vin=12V, Vo=5.0V

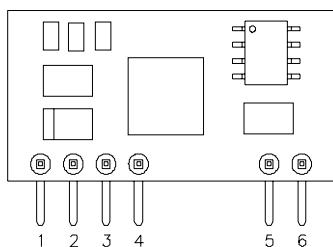
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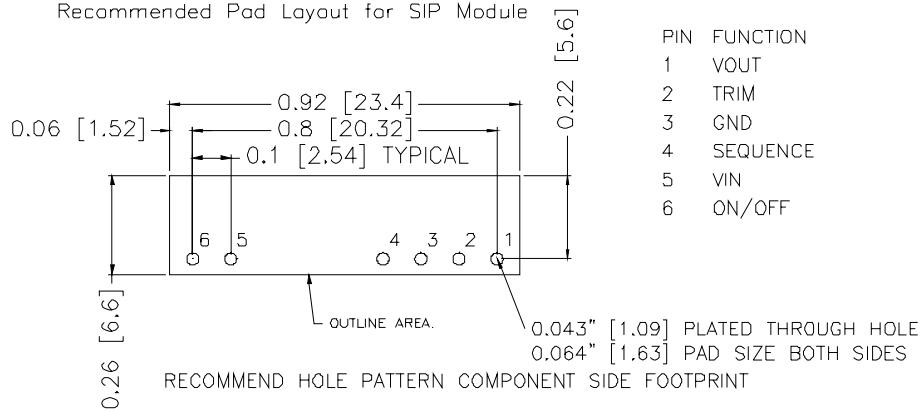


Pin Connections



Pin	Function
1	Vout+
2	Trim
3	Ground
4	SEQ
5	Vin+
6	Remote On/Off

Recommended Pad Layout for SIP Module



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CORPORATE

Bel Fuse Inc.
206 Van Vorst Street
Jersey City, NJ 07302
Tel 201-432-0463
Fax 201-432-9542
www.belfuse.com

FAR EAST

Bel Fuse Ltd.
8F/ 8 Luk Hop Street
San Po Kong
Kowloon, Hong Kong
Tel 852-2328-5515
Fax 852-2352-3706
www.belfuse.com

EUROPE

Bel Fuse Europe Ltd.
Preston Technology Management Centre
Marsh Lane, Suite G7, Preston
Lancashire, PR1 8UD, U.K.
Tel 44-1772-556601
Fax 44-1772-888366
www.belfuse.com