

NOTES:

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

ELECTRICAL SPECIFICATIONS:

CURRENT CAPABILITY: : 350 mA

TURNS RATIO: (P5-P2-P4) : (J6-J3) : 1CT : 1 ±3% (P3-P2-P1) : (J2-J1): 1CT : 1 ±3%

INDUCTANCE: (P5-P4) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

LEAKAGE INDUCTANCE: P5-P4 (WITH J6 AND J3 SHORT) : 0.3 MAX. @ 1MHz

P3-P1 (WITH J2 AND J1 SHORT) : 0.3 MAX. @ 1MHz

INTERWINDING CAPACITANCE: (P5,P2,P4) TO (J6,J3) : 40pf MAX @ 1MHz

(P3,P2,P1) TO (J2,J1) : 40pf MAX @ 1MHz

DC RESISTANCE: (J1-J2) : 1.2 ohms Max. (J3-J6): 1.2 ohms Max.

RETURN LOSS: 1MHz TO 30MHz : 18dB MIN.

> 60MHz TO 80MHz : 12dB MIN.

DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 VAC : 1500 VAC

(J3, J6) TO (P4, P5)

INSERTION LOSS: 100KHz TO 100MHz : 1.1 dB TYP

RISE TIME: OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX

PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100MHz : 35 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35 dB TYP

Bel Stewart Connector 11118 Susquehanna Trail, South Glen Rock, Pa 17327-9199

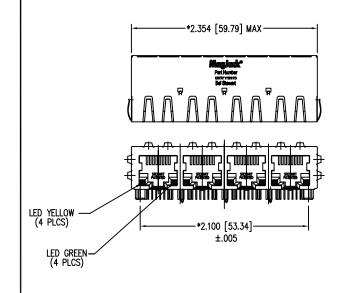
http://www.stewartconnector.com

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SHEET DRAWING NO. 1 OF 3

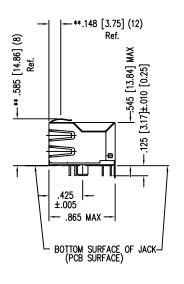
717.234.7512

REV.



ø .035 [0.89] (32)

160 [4.06]



NOTES:

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- "**" ON DIMENSION INDICATES HIGHEST POINT OF BEAM
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE

LED SPECIFICATIONS

YELLOW GREEN FORWARD VOLTAGE(20mA): 2.2v (MAX) 2.5v (MAX) FORWARD VOLTAGE(20mA): 2.1v (TYP) 2.2v (TYP) POWER DISSIPATION: 105mW 105mW WAVE LENGTH: 590nm 565nm INTENSITY @ 10mA: 2-8 MCD 8-32 MCD

090 [2.28] -- *.309 [7.85]TYP. →*.509 [12.93]TYP. P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

·*2.100 [53.34]·

.275 [6.99]-

 $G \phi \phi^{\varphi} \phi^{\Upsilon}$

.550 [13.97] TYP --

─.025 [0.63]

-550 [13.97] TYP TOL NON-ACCUM

·.050 [1.27] TYP TOL NON-ACCUM

TOL NON-ACCUM

ø.040 [1.02](16)

.025 [0.63] -.100 [2.54] TYP

ø.066 [1.68] (3)

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CT720091/CT720074/24-0028

PIN 1

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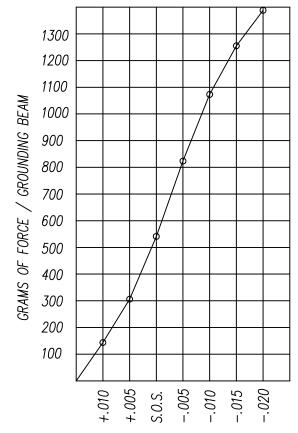
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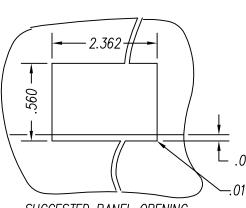
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SHEET 2 OF 3 DRAWING NO.

SI - 4200



PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE



POINT OF CONTACT WITH PANEL - .080 .275 MAX -

> THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLFRANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .005 ON THE BOTTOM.

.000 (TOP OF PCB TO BOTTOM OF OPENING) -.010 MAX. RADIUS(4)

SUGGESTED PANEL OPENING

CT720035X1/24-001701

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SHEET DRAWING NO. 3 OF 3

SI - 4200