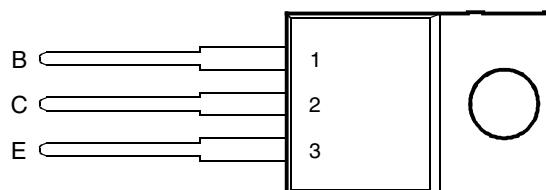


- Designed for Complementary Use with BDW73, BDW73A, BDW73B, BDW73C and BDW73D
- 80 W at 25°C Case Temperature
- 8 A Continuous Collector Current
- Minimum h_{FE} of 750 at 3V, 3 A

TO-220 PACKAGE
(TOP VIEW)

Pin 2 is in electrical contact with the mounting base.

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absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT
Collector-base voltage ($I_E = 0$)	V_{CBO}	BDW74	-45
		BDW74A	-60
		BDW74B	-80
		BDW74C	-100
		BDW74D	-120
Collector-emitter voltage ($I_B = 0$) (see Note 1)	V_{CEO}	BDW74	-45
		BDW74A	-60
		BDW74B	-80
		BDW74C	-100
		BDW74D	-120
Emitter-base voltage	V_{EBO}	-5	V
Continuous collector current	I_C	-8	A
Continuous base current	I_B	-0.3	A
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)	P_{tot}	80	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)	P_{tot}	2	W
Unclamped inductive load energy (see Note 4)	$\frac{1}{2}LI_C^2$	75	mJ
Operating junction temperature range	T_j	-65 to +150	°C
Operating temperature range	T_{stg}	-65 to +150	°C
Operating free-air temperature range	T_A	-65 to +150	°C

NOTES: 1. These values apply when the base-emitter diode is open circuited.

2. Derate linearly to 150°C case temperature at the rate of 0.64 W/°C.

3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.

4. This rating is based on the capability of the transistor to operate safely in a circuit of: $L = 20 \text{ mH}$, $I_{B(on)} = -5 \text{ mA}$, $R_{BE} = 100 \Omega$, $V_{BE(off)} = 0$, $R_S = 0.1 \Omega$, $V_{CC} = -20 \text{ V}$.**PRODUCT INFORMATION**

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
V _{(BR)CEO} Collector-emitter breakdown voltage	I _C = -30 mA	I _B = 0	(see Note 5)	BDW74 BDW74A BDW74B BDW74C BDW74D	-45 -60 -80 -100 -120		V
I _{CEO} Collector-emitter cut-off current	V _{CE} = -30 V	I _B = 0		BDW74		-0.5	
	V _{CE} = -30 V	I _B = 0		BDW74A		-0.5	
	V _{CE} = -40 V	I _B = 0		BDW74B		-0.5	
	V _{CE} = -50 V	I _B = 0		BDW74C		-0.5	
	V _{CE} = -60 V	I _B = 0		BDW74D		-0.5	
I _{CBO} Collector cut-off current	V _{CB} = -45 V	I _E = 0		BDW74		-0.2	
	V _{CB} = -60 V	I _E = 0		BDW74A		-0.2	
	V _{CB} = -80 V	I _E = 0		BDW74B		-0.2	
	V _{CB} = -100 V	I _E = 0		BDW74C		-0.2	
	V _{CB} = -120 V	I _E = 0		BDW74D		-0.2	
	V _{CB} = -45 V	I _E = 0	T _C = 150°C	BDW74		-5	
	V _{CB} = -60 V	I _E = 0	T _C = 150°C	BDW74A		-5	
	V _{CB} = -80 V	I _E = 0	T _C = 150°C	BDW74B		-5	
	V _{CB} = -100 V	I _E = 0	T _C = 150°C	BDW74C		-5	
	V _{CB} = -120 V	I _E = 0	T _C = 150°C	BDW74D		-5	
I _{EBO} Emitter cut-off current	V _{EB} = -5 V	I _C = 0				-2	mA
h _{FE} Forward current transfer ratio	V _{CE} = -3 V	I _C = -3 A	(see Notes 5 and 6)	750		20000	
	V _{CE} = -3 V	I _C = -8 A		100			
V _{BE(on)} Base-emitter voltage	V _{CE} = -3 V	I _C = -3 A	(see Notes 5 and 6)			-2.5	V
V _{CE(sat)} Collector-emitter saturation voltage	I _B = -12 mA	I _C = -3 A	(see Notes 5 and 6)			-2.5	V
	I _B = -80 mA	I _C = -8 A				-4	
V _{EC} Parallel diode forward voltage	I _E = -8 A	I _B = 0				-3.5	V

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 μs, duty cycle ≤ 2%.

6. These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
R _{θJC} Junction to case thermal resistance			1.56	°C/W
R _{θJA} Junction to free air thermal resistance			62.5	°C/W

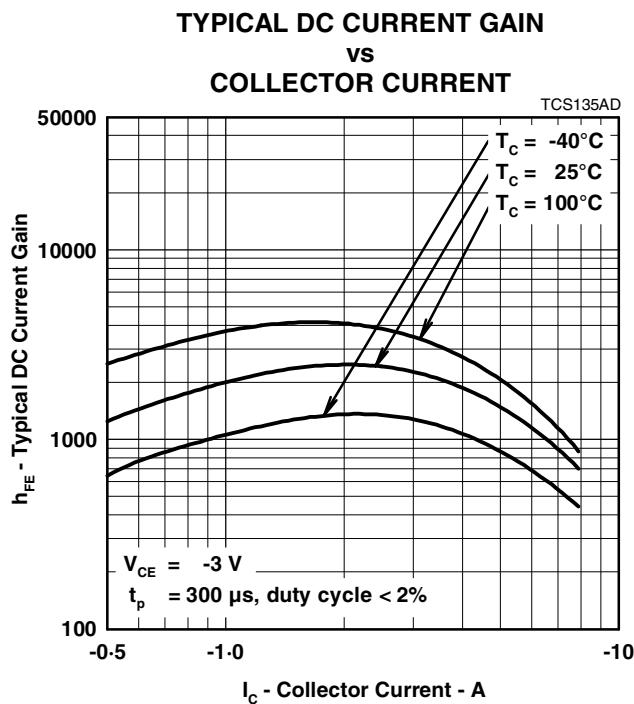
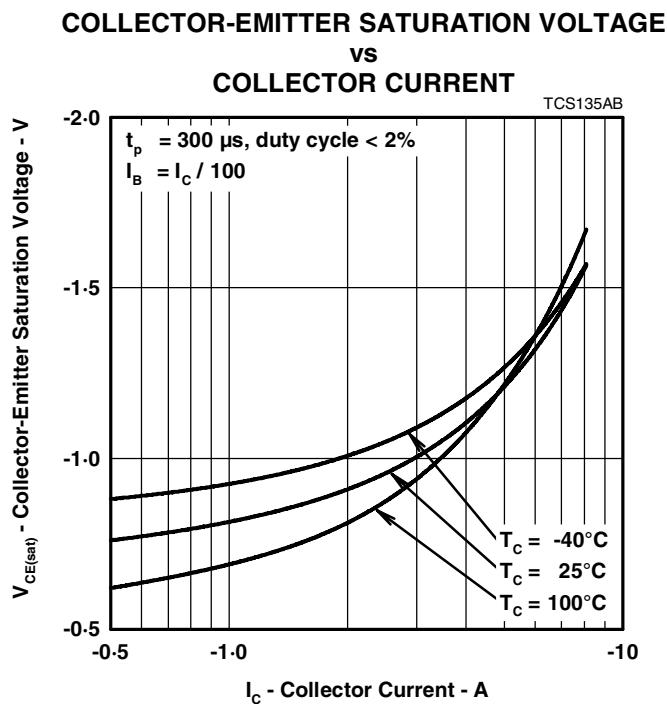
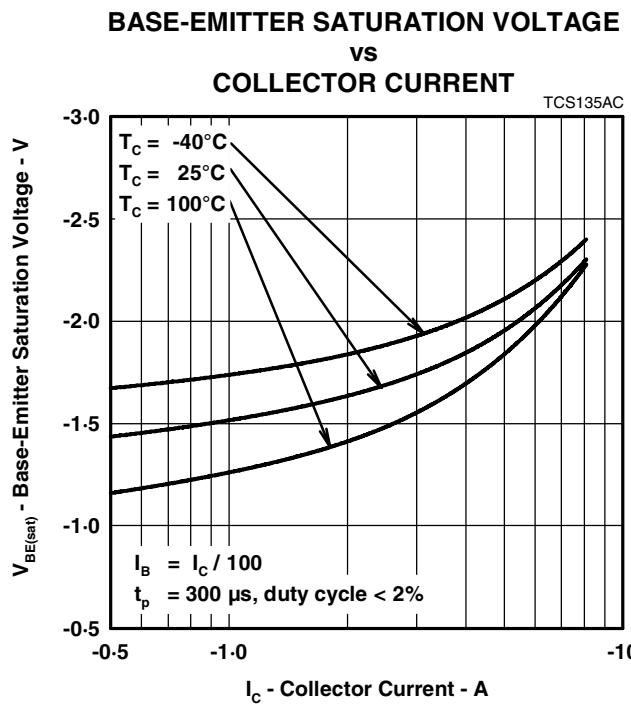
resistive-load-switching characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on} Turn-on time	I _C = -3 A	I _{B(on)} = -12 mA	I _{B(off)} = 12 mA		1		μs
t _{off} Turn-off time	V _{BE(off)} = 3.5 V	R _L = 10 Ω	t _p = 20 μs, dc ≤ 2%		5		μs

† Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

PRODUCT INFORMATION

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 Specifications are subject to change without notice.

TYPICAL CHARACTERISTICS

Figure 1.

Figure 2.

Figure 3.
PRODUCT INFORMATION

MAXIMUM SAFE OPERATING REGIONS

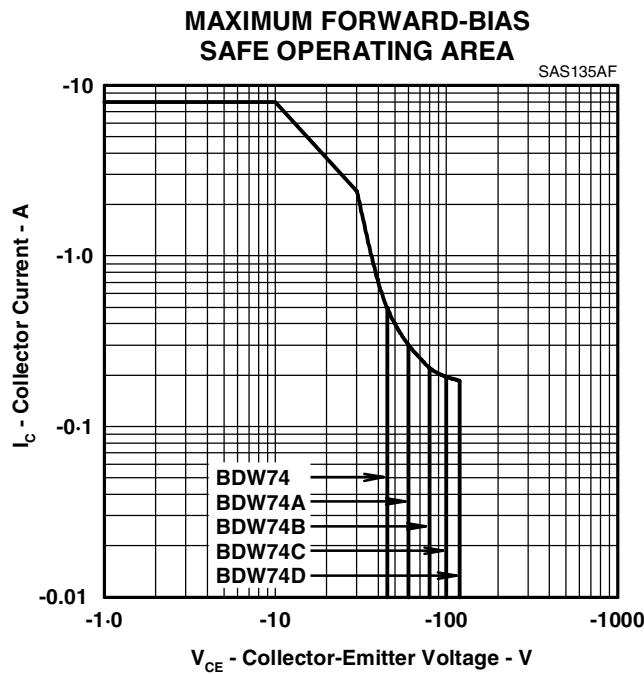


Figure 4.

THERMAL INFORMATION

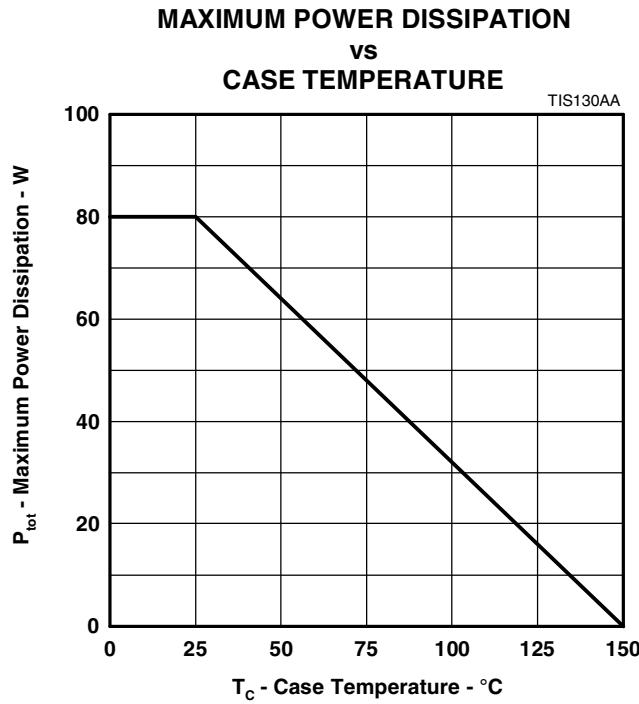


Figure 5.

PRODUCT INFORMATION

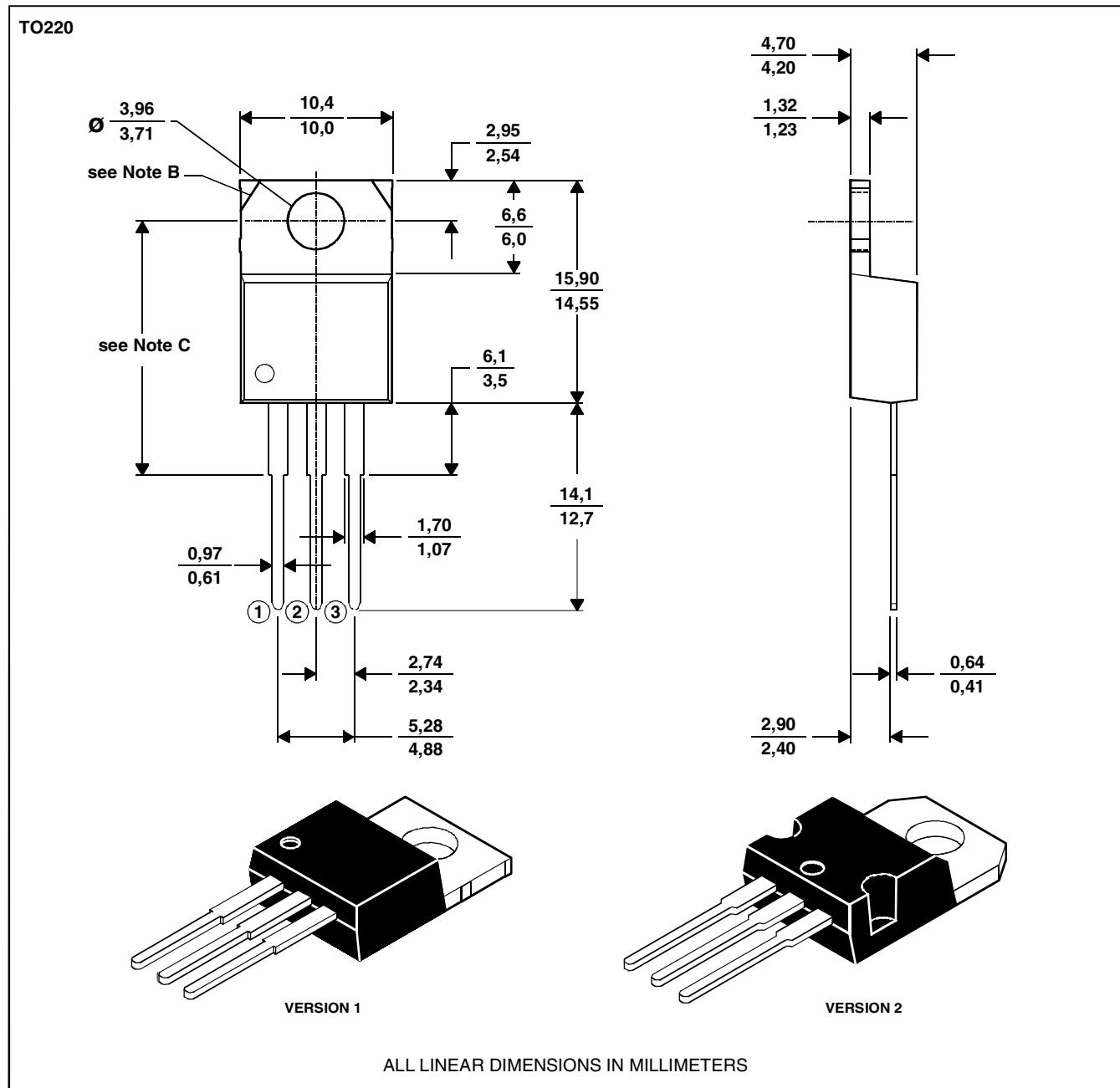
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MECHANICAL DATA

TO-220

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.
B. Mounting tab corner profile according to package version.
C. Typical fixing hole centre stand off height according to package version.
Version 1, 18.0 mm. Version 2, 17.6 mm.

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